

Special Issue

Radiation Tolerant Digital and Analog Circuits and Systems

Message from the Guest Editors

The main aim of this Special Issue is to seek high-quality submissions that highlight emerging applications of radiation-tolerant systems, address recent breakthroughs in modeling radiation effects in advanced electronic devices and circuits, present designs of radiation-hardened digital, analog, mixed-signal, and RF integrated circuits, and radiation hardness testing methodologies. The topics of interest include, but are not limited to:

- Basic mechanisms of radiation effects in electronic devices;
- Compact modeling of radiation effects and circuit/layout level optimization (TID and SEE);
- Radiation effects in power devices/circuits;
- Design of radiation-hardened integrated circuits (analog/RF/mixed-signal/digital);
- Radiation hardening and fault tolerance in FPGAs;
- Radiation hardness assurance testing.

Guest Editors

Prof. Dr. Li Chen

Prof. Dr. Haibin Wang

Dr. Kai Xi

Dr. Liang Wang

Dr. Rui Chen

Deadline for manuscript submissions

closed (20 September 2023)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.4 days after submission; acceptance to publication is undertaken in 2.6 days (median values for papers published in this journal in the second half of 2025).