Special Issue

Low-Power CMOS and Beyond-CMOS Front-End Circuits and Systems

Message from the Guest Editors

This Special Issue aims to collect contributions focusing on different topics in the design of low-power front-end circuits and systems using CMOS and emerging technologies. The topics of interest include, but are not limited to:

- Device modeling and biasing techniques for low-power circuits.
- The design of test circuits.
- Low-power analog and digital circuit design.
- Energy-harvesting circuits and systems.
- 4. Variability-aware design techniques for low-power design.
- Low-power sensor readout interfaces.
- Low-power data converters.
- The benchmarking of novel devices for front-end circuits.
- Implantable devices for biomedical applications.
- Low-power wearable electronics for body sensor networks.
- Autonomous healthcare circuits and systems.
- Neuromorphic computing systems.

Guest Editors

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Deadline for manuscript submissions

closed (31 May 2024)

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

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