



Electron Devices and Solid-State Circuits

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Deadline for manuscript
submissions:

closed (15 November 2023)

Message from the Guest Editor

Dear Colleagues,

This Special Issue aims to examine the recent breakthroughs in electron devices and solid-state circuits and the next design challenges that should be faced in the edge-computing era.

Manuscripts are solicited on topics related but not limited to the following:

- i. CMOS and SOI technology for low-power design, modelling and methodology issues and solutions.
- ii. Power-consumption-aware analog and digital design.
- iii. Analysis and comparison among efficient digital algorithms for edge data manipulation.
- iv. Comparison or proposal of new architectures for low-power transceivers.
- v. Analysis and efficiency optimization of system blocks of transceivers (PLL, VCO, DPA, ADC, DAC, etc.).
- vi. Methodologies for optimized hardware for neural networks in edge computing. Manuscripts may either present original research results or comprehensive reviews on specific topics in the field of Electron Devices and Solid-State Circuits.





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Editor-in-Chief

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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