



Low-Power Techniques for Embedded Systems and Network-on-Chip Architectures

Guest Editor:

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Message from the Guest Editor

This Special Issue will explore emerging approaches, ideas, and contributions to address the challenges in the design of energy efficient computational-centric embedded systems and on-chip networks.

Potential topics include, but are not limited to:

- Approximate computing for energy-efficient applications
- Novel architectures for embedded low power computing
- Communication infrastructures for energy efficient IoT environments
- Energy efficient neural networks
- Energy-aware parallel architectures for high-performance computing
- Design Platforms and Tools for optimizing energy/performance trade-offs

Click [here](#) to find more information!

Welcome to contribute!

