



Nanoscale CMOS Technologies

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Message from the Guest Editors

The scope of this Special Issue is to focus and report on the development of emerging techniques to overcome these challenges.

Specifically, the research portfolio for this issue includes research on low-power RF, mixed-signal and analog CMOS design of circuits and systems, and power-efficient integrated circuit design for communications, sensing and biomedical applications. The topics of primary research include but are not limited to:

- Recent developments in nanoscale circuits and systems including mixed-signal circuits, RFICs and analog building blocks;
- Circuit design for emerging 2.5D or 3D IC CMOS technologies;
- Sub-threshold digital and analog circuits;
- Process and mismatch insensitive integrated circuit design;
- Signal processing and digital assisted methods for high performances circuits;
- Biologically-Inspired system and circuit design;
- Optimization methods for energy-efficient integrated circuits

Welcome to contribute!

