Advances in System-on-Chip Design

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Deadline for manuscript
submissions:
closed (31 December 2021)

Message from the Guest Editor

The main aim of this Special Issue is to seek high-quality submissions that address emerging challenges in complex SoC architectures, such as the design of specialized accelerators, their integration with pre-existing components, the design of scalable and efficient memory architectures, and the identification of additional vulnerabilities arising from the integration of several different components. The topics of interest include but are not limited to:

- Novel SoC architectures and design methodologies;
- Layout- and energy-driven SoC architectures;
- Design space exploration methods for SoC design;
- High power density dower electronic systems;
- High-level synthesis and hardware/software co-design methodologies;
- Efficient memory architectures for scalable and distributed SoC architectures.;
- Hardware security and hardware-assisted security in SoC architectures.

Special Issue

Editor-in-Chief

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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