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Millimeter-Wave-Integrated CMOS Radars and Communication Systems: Architecture and Circuit Designs

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Deadline for manuscript submissions: closed (30 June 2022)

Message from the Guest Editors

This Special Issue will host the latest results in the field of integrated mm-wave CMOS ICs for radar and communication, with a focus on circuit design, architectures, component modelling, electromagnetic (EM) simulation, SoC integration, advanced package-to-chip co-design, and antenna-to-chip co-design.

The topics of interest include, but are not limited to:

- Mm-wave CMOS front-end circuits (LNAs, mixers, VGAs, T/R switches, amplifiers, filters, demodulators)
- CMOS Oscillators and frequency synthesizers (VCOs, frequency dividers, multipliers, PLLs, charge pumps)
- CMOS transmitters and power amplifiers for mmwave applications
- Integrated radar sensors
- mm-wave communication circuits and systems-onchip

Specialsue



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Message from the Editor-in-Chief

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