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Millimeter-Wave Integrated Circuits and Systems for 5G Applications

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Deadline for manuscript submissions:
closed (31 December 2021)

Message from the Guest Editor

The main aim of this Special Issue is to disseminate latest findings, new research developments, and future trends and innovations in MMW integrated circuits and systems for 5G applications. Both theoretical and experimental studies for MMW IC design, architectures, technologies, devices, circuits, and systems are encouraged.

The papers considered for possible publication may focus on but not necessarily be limited to the following areas:

- MMW circuits, such as low noise amplifiers, mixers, voltage-controlled oscillators, power amplifiers, variable gain amplifiers, etc.;
- MMW architectures, systems and subsystems, such as receivers, transmitters, transceivers, phase-locked loops, frequency synthesizers, multistandard transceivers, digital radio, etc.;
- MMW passive structures such as transformers, hybrid couplers, filters, baluns, switches, antennae, etc.;
- MMW digital baseband; MMW data converters; wireless communication systems; wideband integrated circuits and systems; low-power and energy-efficient MMW digital systems;
- Advanced MMW IC; emerging MMW nanoscale CMOS IC; MMW 3D integrations; MMW SiP and SOC.

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Message from the Editor-in-Chief

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