



Recent FPGA Architectures and Applications

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Message from the Guest Editors

Dear Colleagues,

FPGAs are currently involved in a continuous update of characteristics and features, thus becoming the technology of choice for many final designs, with new fields of application arising. As a consequence, low-cost FPGAs are increasing the number of logic elements while reducing power consumption, becoming a valid option for IoT applications requiring high performance and/or high levels of security. On the other hand, the latest generations of high-performance FPGAs include embedded processors and high-speed memories within the same device, opening up new possibilities in the development of hardware accelerators, AI applications, database servers, and so on. This Special Issue, entitled “Recent FPGA Architectures and Applications”, is intended to present the latest advances in applications benefiting from new FPGA architectures and features, new applications of low-cost FPGAs to fields such as IoT or digital control, and the emerging applications of high-performance FPGAs to AI, high-performance servers, and hardware-accelerated systems.





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Message from the Editor-in-Chief

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