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Efficient Algorithms and Architectures for DSP Applications

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Deadline for manuscript submissions:
closed (31 December 2022)

Message from the Guest Editors

Optimization of the implementation of DSP algorithms and architectures is an essential part of research and design for many modern applications. Optimizing such computationally intensive applications is a challenging issue that requires a clever design or restructuring of the algorithm or architecture. This Special Issue focuses on papers that demonstrate how these design challenges can be overcome using innovative solutions.

Topics of interest for this Special Issue include but are not limited to:

- VLSI signal processing;
- Signal processing methods for an efficient implementation;
- Optimization of the VLSI implementation of multimedia blocks;
- Low-power circuits and systems for DSP applications;
- Efficient adaptive/learning algorithms (low complexity/fast versions, optimized parameters, etc.);
- Tensor-based signal processing (efficient decomposition methods, low-rank approximations, etc.);
- Sparsity-aware algorithms.

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Message from the Editor-in-Chief

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