



Ultra Low Power Integrated Circuits for IoT

Guest Editors:

Dr. Paulo César C. De Aguirre

Graduate Program in Electrical Engineering, Federal University of Pampa, Alegrete 97546-550, Brazil

Dr. Orazio Aiello

Department of Electrical, Electronic, Telecommunications Engineering and Naval Architecture (DITEN), University of Genoa, Via Opera Pia 11a, I-16145 Genova, Italy

Dr. Elisabetta Moiselto

Department of Electrical, Computer, and Biomedical Engineering, University of Pavia, 27100 Pavia, Italy

Deadline for manuscript submissions:

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Message from the Guest Editors

This Special Issue aims to cover emerging trends and techniques to design ultra-low-power integrated circuits and to attract original research outcomes related to ultra-low-power, ultra-low-voltage, digital-based, and fully synthesizable circuits to enable the next generation of IoT devices.

The topics of interest include, but are not limited to:

- Ultra-low-power signal conditioning circuits for IoT;
- Energy-efficient analog and mixed-signal circuits;
- Power/voltage scalable analog and mixed-signal circuits;
- IC solutions for ultra-low-voltage, energy, and standby power consumption systems;
- Energy harvesting and power management circuits for IoT devices;
- Ultra-low power circuit design techniques for a variety of IC building blocks such as amplifiers, voltage and current references, analog filters, data converters, and voltage regulators;
- Ultra-low-power hardware architectures for energy-constrained devices;
- Ultra-low-power/voltage ICs for instrumentation and communication applications.





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Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and
Telecommunications,
Politecnico di Torino, 10129
Torino, Italy

Message from the Editor-in-Chief

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Electronics Editorial Office
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