



Radiation Tolerant Digital and Analog Circuits and Systems

Guest Editors:

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Message from the Guest Editors

Dear Colleagues,

The main aim of this Special Issue is to seek high-quality submissions that highlight emerging applications of radiation-tolerant systems, address recent breakthroughs in modeling radiation effects in advanced electronic devices and circuits, present designs of radiation-hardened digital, analog, mixed-signal, and RF integrated circuits, and radiation hardness testing methodologies. The topics of interest include, but are not limited to:

Deadline for manuscript
submissions:

closed (20 September 2023)

- Basic mechanisms of radiation effects in electronic devices;
- Compact modeling of radiation effects and circuit/layout level optimization (TID and SEE);
- Radiation effects in power devices/circuits;
- Design of radiation-hardened integrated circuits (analog/RF/mixed-signal/digital);
- Radiation hardening and fault tolerance in FPGAs;
- Radiation hardness assurance testing.





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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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