

Special Issue

Physical Layer Security and Trust for Legacy Systems and Supply Chain Assurance

Message from the Guest Editor

Physical-layer security and trust of microelectronic systems is threatened by side-channel signal acquisition attacks, semi-invasive probing and sophisticated reverse engineering process flows. Legacy systems are particularly vulnerable and the large investment in current deployments across medical, military, industrial, and critical infrastructures makes it imperative that these systems are retrofit with countermeasures designed to improve their situational-awareness and resilience to attacks. Techniques that provide a root-of-trust through, for example, secure boot, and/or provide intra-SoC security firewalls between IP blocks would further improve the resilience of both legacy and emerging systems. Similarly, methods that provide assurance of authenticity of chips and systems as they move through the supply chain would also greatly alleviate security concerns related to hidden back door access mechanisms in newly deployed systems. This Special Issue covers these topics as well as extended versions of papers presented at the HOST 2018 (<http://www.hostsymposium.org/>).

Guest Editor

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Message from the Editor-in-Chief

Cryptography is a new international journal which provides the state-of-the-art forum for original results in all areas of modern cryptography. *Cryptography* is published in open access format: research articles, reviews and other contents are released on the internet immediately after acceptance. Our journal welcomes submissions written from the theory and practices of modern cryptography, so that it may become a forum for exchange of new scientific developments between the cryptographers and the practitioners.

We would be pleased to welcome you as one of our authors.

Editor-in-Chief

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