

# Special Issue

## Feature Papers in Hardware Security

### Message from the Guest Editor

This is a Special Issue for high-quality papers by the editorial board members or those invited by the editorial office and the Editor-in-Chief. Papers would be published, **free of charge**, in open access form after peer review. We invite authors to submit research papers on topics related to hardware-based authentication, encryption and secure boot protocols for resource-constrained embedded systems, on novel side-channel analysis attacks and countermeasures, on PUFs for ICs and printed circuit boards (PCBs) capable of providing security, trust and detection of tamper, on hardware Trojan attacks, analysis, detection methods and countermeasures, on supply-chain authentication and hardware assurance methods, on hardware-based security and trust primitives for RFID (Radio-frequency identification), IoT, autonomous vehicle, embedded medical, and industrial control, communication and other types of critical infrastructures, and on reverse engineering techniques and countermeasures to protect ICs and IPs through obfuscation and active metering schemes.

### Guest Editor

Prof. Dr. Jim Plusquellic

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### Deadline for manuscript submissions

closed (30 April 2021)



## Cryptography

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## About the Journal

### Message from the Editor-in-Chief

*Cryptography* is a new international journal which provides the state-of-the-art forum for original results in all areas of modern cryptography. *Cryptography* is published in open access format: research articles, reviews and other contents are released on the internet immediately after acceptance. Our journal welcomes submissions written from the theory and practices of modern cryptography, so that it may become a forum for exchange of new scientific developments between the cryptographers and the practitioners.

We would be pleased to welcome you as one of our authors.

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### Editor-in-Chief

Prof. Dr. Josef Pieprzyk

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