

Special Issue

IC Design Techniques for Power/Energy-Constrained Applications

Message from the Guest Editors

Power- and/or energy-constrained applications are critical tasks in IC design. Cases like the Internet of Things (IoT) or Implanted Medical Devices (IMDs), in many practical cases, cannot be utilized if the constituting circuits do not involve adequately scaled electronic engineering (in terms of power/voltage performance). To accomplish these challenges, design techniques for integrated circuits need particular refinements and new approaches/topologies need to be defined. Small area occupation, low design effort, and technology/design portability are among the keywords in this framework. So, this Special Issue aims to attract reviews and original research submissions related to the design and application of ultra-low-voltage/power, analog/digital, or mixed-signal-based integrated circuits.

Guest Editors

Dr. Andrea Ballo

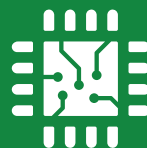
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Deadline for manuscript submissions

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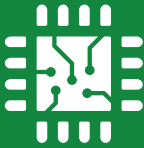
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About the Journal

Message from the Editor-in-Chief

Chips is a new journal with the aim to become a leading reference on all aspects of the IC domain. The journal is devoted to publishing rigorously peer-reviewed articles (such as original research, reviews, and communications) with the specific target to disseminate novelties in terms of research and knowledge as well as the most advanced state of the art on IC technologies, design, testing, and production. The journal offers the opportunity to actively spread new concepts and advancements in the IC domain and its increasing interrelated and multidisciplinary areas in a timely manner. More specifically, the journal will cover chip design, including CAD tools, chip production, and their wide spectrum of applications.

Editor-in-Chief

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