# Special Issue

# Emerging Memory Devices for Smart Power Circuits and Analog Computing Applications

## Message from the Guest Editors

Emerging memory technologies are rapidly transforming the landscape of modern electronics, enabling new paradigms in computing and energy-efficient system design. These technologies offer unique features, including non-volatility, scalability, and the capability to perform computation directly within memory, thus overcoming the limitations of traditional von Neumann architectures. This Special Issue aims to explore recent advances in emerging memory devices and their integration into smart power and analog computing systems. The focus will be on innovative device technologies, circuit-level implementations, and system-level applications that leverage memory-enabled computation and ultra-low-power operation. Contributions addressing modeling, design methodologies, reliability, and novel applications are particularly welcome. Topics of interest include, but are not limited to: memory-based computing architectures, neuromorphic and in-memory processing, energy-efficient circuit design, integration of emerging memories in power electronics, and experimental demonstrations of advanced memory technologies.

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## Deadline for manuscript submissions

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As the world of science becomes ever more specialized, researchers may lose themselves in the deep forest of the ever increasing number of subfields being created. This open access journal *Applied Sciences* has been started to link these subfields, so researchers can cut through the forest and see the surrounding, or quite distant fields and subfields to help develop his/her own research even further with the aid of this multi-dimensional network.

## Editor-in-Chief

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