

Special Issue

Advancements in Compiler Optimizations

Message from the Guest Editor

This Special Issue aims to present the latest findings pertaining to compiler optimizations, optimization frameworks, as well as innovative methods and tools being developed to tackle the existing problems. The topics of interest include, but are not limited to, the following:

- Code generation, translation, transformation, and optimization for performance, energy consumption, code size, portability, security, or reliability;
- Hardware-dependent optimizations for heterogeneous hardware architectures or specialized co-processors;
- Machine learning compilers and optimizers;
- Profile-guided, feedback-directed, and machine learning-based optimization;
- Optimizing scientific applications on parallel hardware architectures;
- Compiler optimizations for graphics processor units (GPUs) or other programmable accelerators;
- Compiler techniques or loop transformations for optimizing memory hierarchy;
- Programming environments for GPU-based computing;
- Optimization and code generation for emerging programming models and platforms;
- Compiler support for vectorization, thread extraction, task scheduling, memory management, data distribution and synchronization.

Guest Editor

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Deadline for manuscript submissions

closed (29 February 2024)



Applied Sciences

an Open Access Journal
by MDPI

Impact Factor 2.5
CiteScore 5.5



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About the Journal

Message from the Editor-in-Chief

As the world of science becomes ever more specialized, researchers may lose themselves in the deep forest of the ever increasing number of subfields being created. This open access journal Applied Sciences has been started to link these subfields, so researchers can cut through the forest and see the surrounding, or quite distant fields and subfields to help develop his/her own research even further with the aid of this multi-dimensional network.

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