# Special Issue

# Low-Power Integrated Circuit Design and Application

## Message from the Guest Editors

The number of connected devices in IoT applications is expected to exceed 40 billion in 2025. Thus, it is vital to tackle the challenges involved in the design of ultra-low power solutions to ensure the feasibility of current and future systems, as well as to propose new mechanisms and energy extraction circuits that can complement or even replace the use of batteries. To achieve these goals, it is necessary to use special techniques to design low-power analog/digital integrated circuits. For this Special Issue, authors are encouraged to submit their original research on the use of low-power integrated circuits to improve the energy efficiency of systems. Topics of interest include, but are not limited to, the following fields:

- Novel energy harvesters;
- Novel power management techniques and circuits;
- Low-power sensor interfaces;
- Low-power analog and mixed-signal circuits.

## Guest Editors

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## Deadline for manuscript submissions

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## Message from the Editor-in-Chief

As the world of science becomes ever more specialized, researchers may lose themselves in the deep forest of the ever increasing number of subfields being created. This open access journal Applied Sciences has been started to link these subfields, so researchers can cut through the forest and see the surrounding, or quite distant fields and subfields to help develop his/her own research even further with the aid of this multi-dimensional network.

## Editor-in-Chief

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