

## Special Issue

# Low-Power Integrated Circuit Designs and Applications

### Message from the Guest Editors

We are pleased to invite experts and scholars in this field to discuss various technologies and their applications for reducing the power consumption of integrated circuits. This Special Issue aims to explore design technologies for reducing the power consumption of integrated circuits, including new devices, low-voltage design methods, digital logic circuits, memory, chip architecture, algorithms, and other related technologies. In this Special Issue, original research articles and reviews are welcome. Research areas may include (but are not limited to) the following: 1. subthreshold circuits, 2. low voltage circuits, 3. computing in memory, 4. integrated sensing and computation, 5. AVFS, 6. EDA, 7. low-power algorithms, 8. low-power processors, 9. AI chips, 10. low-power systems, etc. We look forward to receiving your contributions.

### Guest Editors

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### Deadline for manuscript submissions

20 March 2026



## Applied Sciences

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## About the Journal

### Message from the Editor-in-Chief

As the world of science becomes ever more specialized, researchers may lose themselves in the deep forest of the ever increasing number of subfields being created. This open access journal Applied Sciences has been started to link these subfields, so researchers can cut through the forest and see the surrounding, or quite distant fields and subfields to help develop his/her own research even further with the aid of this multi-dimensional network.

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### Editor-in-Chief

Prof. Dr. Giulio Nicola Cerullo  
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