

Special Issue

Algorithms in Reconfigurable Computing

Message from the Guest Editors

Reconfigurable Computing has emerged as a low cost, high-performance computing platform able to execute algorithms in many application domains faster than other computing platforms, like CPU-based systems. Its reconfigurability allows the design and implementation of circuits following the most appropriate computing paradigm for a particular algorithm. This Special Issue aims to collect recent innovations to deploy algorithms in reconfigurable computing. Potential topics include, but are not limited to:

- Mapping algorithms in FPGA with high-level synthesis;
- Template-based reconfigurable architectures for particular domains of algorithms;
- FPGA accelerators for algorithms;
- Algorithm optimization with arbitrary-precision data types;
- Algorithms in coarse-grained reconfigurable architectures;
- Design methodologies to map algorithms on reconfigurable computing devices;
- Design of data-centric algorithms with reconfigurable computing;
- Reconfigurable embedded devices for algorithms applied to health, smart-home, etc;
- Design of algorithms in high-performance reconfigurable computing platforms;
- Designing algorithms in SoC (System-on-Chip) FPGAs.

Guest Editors

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Deadline for manuscript submissions

closed (15 March 2023)



Algorithms

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Impact Factor 2.1
CiteScore 4.5



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About the Journal

Message from the Editor-in-Chief

Algorithms are the very core of Computer Science. The whole area has been considered from quite different perspectives, having led to the development of many sub-communities: Complexity theory (limitations), approximation or parameterized algorithms (types of problems), geometric algorithms (subject area), metaheuristics, algorithm engineering, medical imaging (applications), indicates the range of perspectives. Our journal welcomes submissions written from any of these perspectives, so that it may become a forum for exchange of ideas between the corresponding scientific subcommunities.

Editor-in-Chief

Prof. Dr. Frank Werner

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