High Performance Reconfigurable Computing

Guest Editor:

Dr. Seyong Lee
Future Technologies Group, Oak Ridge National Laboratory, Oak Ridge, TN, USA
lees2@ornl.gov

Deadline for manuscript submissions:
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Message from the Guest Editor

We invite you to submit your latest research on the theoretical and practical issues in applying reconfigurable computing to high performance computing (HPC) and data analytics. Potential topics of this Special Issue include, but are not limited to:

- HPC and machine learning algorithms and applications implemented on reconfigurable devices
- Programming models, compilers, and system software for reconfigurable computing
- Algorithms and methods for leveraging dynamic reconfiguration to increase performance, flexibility, reliability, or programmability.
- Mapping algorithms and tools for heterogeneous system-on-chip devices
Editor-in-Chief

Prof. Dr. Henning Fernau
Theoretical Computer Science,
FB 4-Abteilung Informatik,
Universität Trier, D-54286 Trier, Germany

Message from the Editor-in-Chief

Algorithms are the very core of Computer Science. The whole area has been considered from quite different perspectives, having led to the development of many sub-communities: Complexity theory (limitations), approximation or parameterized algorithms (types of problems), geometric algorithms (subject area), metaheuristics, algorithm engineering, medical imaging (applications), indicates the range of perspectives. Our journal welcomes submissions written from any of these perspectives, so that it may become a forum for exchange of ideas between the corresponding scientific subcommunities.

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Contact Us

Algorithms
MDPI, St. Alban-Anlage 66
4052 Basel, Switzerland
Tel: +41 61 683 77 34
Fax: +41 61 302 89 18
www.mdpi.com
mdpi.com/journal/algorithms
algorithms@mdpi.com