Special Issue

Verification Approaches for Nano- and Micro-Satellites

Message from the Guest Editors

There is growing interest for the development of light, small, high-performance spacecraft (S/C) platforms for a wide range of missions. We are now at a turning point, where nano-/micro-sat systems can accomplish interplanetary missions beyond the boundaries of LEO orbits (Low Earth *Orbits).* The topics for this Special Issue include both system analysis for future projects and in-flight experience from ongoing missions. Submission of manuscripts dealing with both subsystem and system-level Assembly Integration and Verification (AIV), with a focus on verification approach, verification methods, verification levels, verification stages, models philosophy and verification tools is encouraged. Papers also are sought which review recent research developments in comprehensive ground verification systems, including not only Software-in-the-Loop (SIL) and component-level Hardware-in-the-loop (HIL) tests, but also system-level HIL tests.

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You are welcome to contribute a research article or a comprehensive review for consideration and publication in *Aerospace* (ISSN 2226-4310), an on-line, open access journal.
Aerospace adheres to rigorous peer-review as well as editorial processes and publishes high quality manuscripts that address both the fundamentals and applications of aeronautics and astronautics. Our goal is to enable rapid dissemination of high impact works to the scientific community.

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