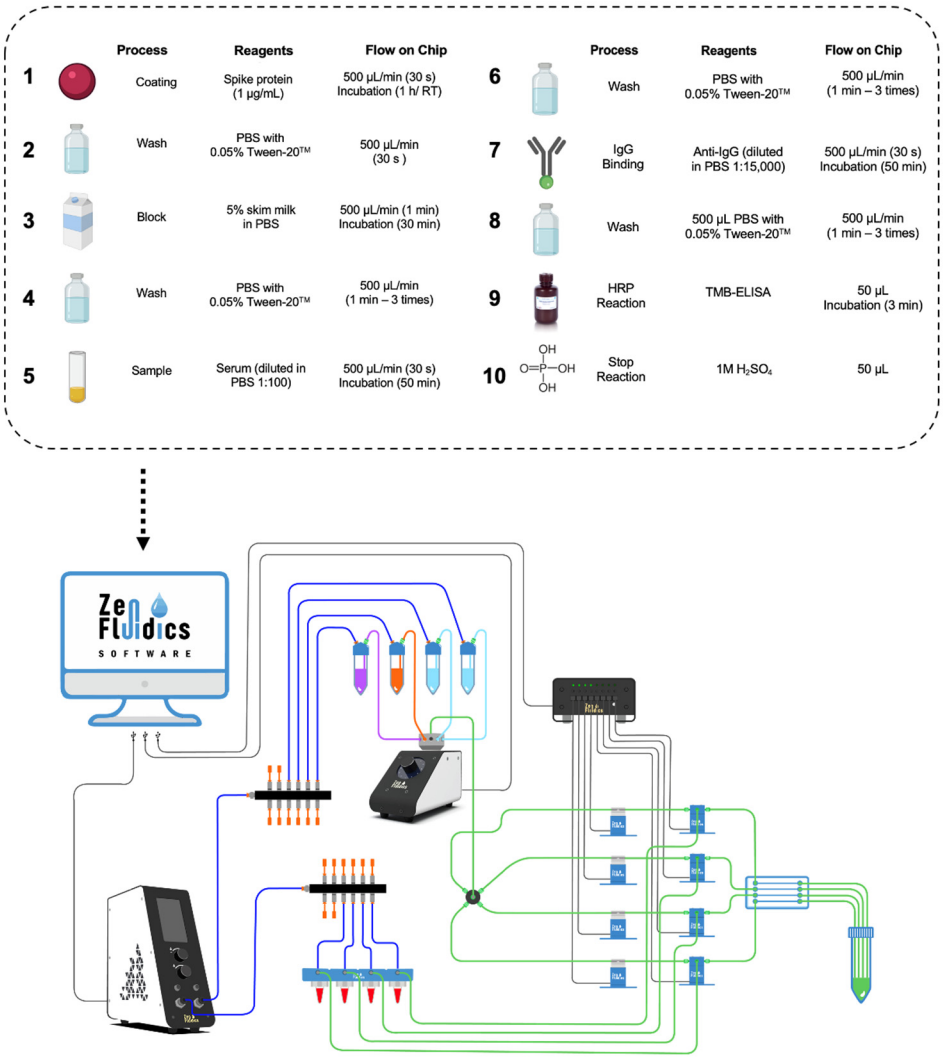

Figure S1. Established protocol and experimental setup of our automated ELISA on-chip.