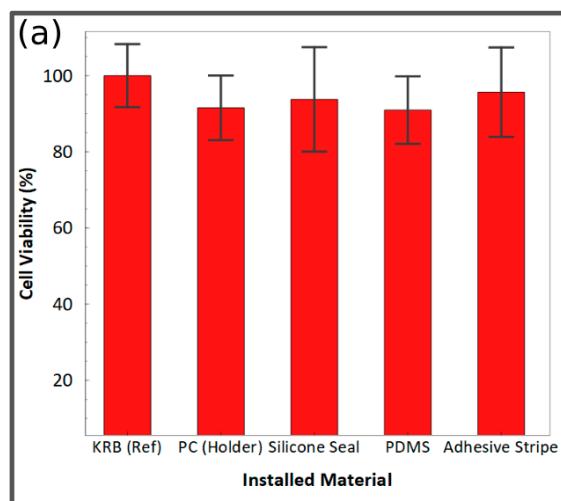
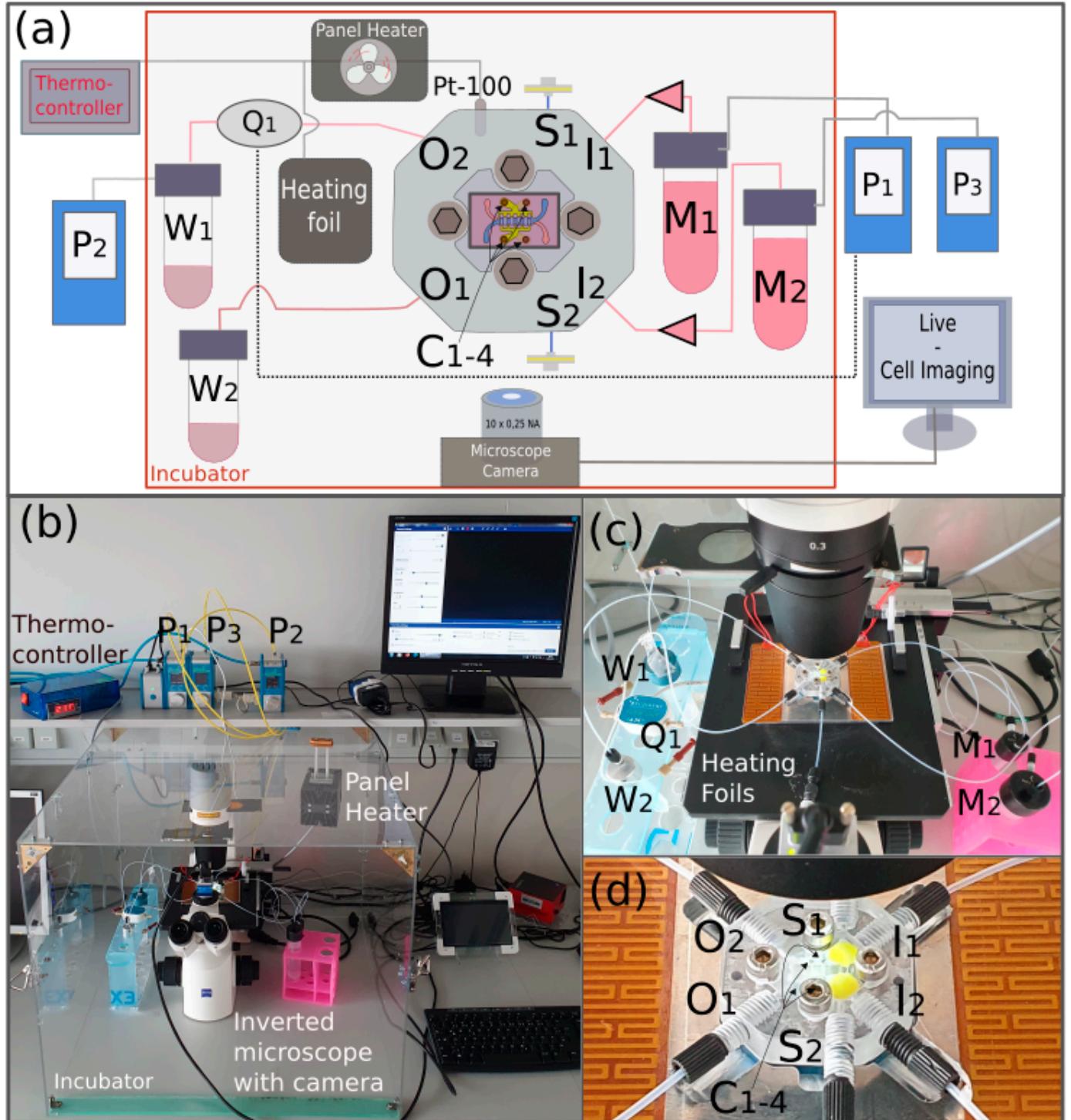


**Figure S1.**



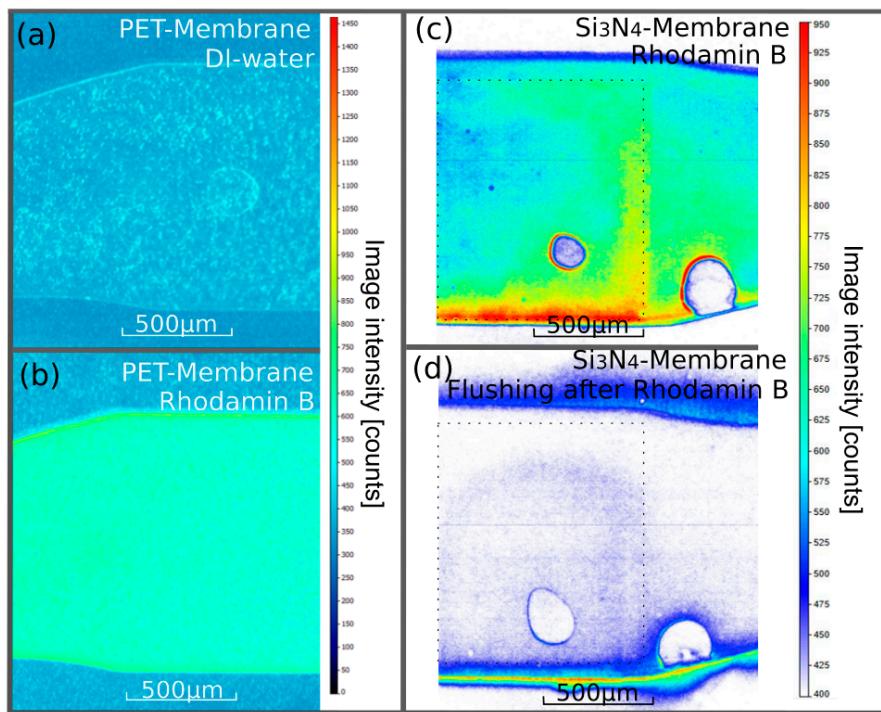
**Figure S1.** MTT-cell viability test (a) Cell viability of MDCK cells after five days of exposure to the different materials used in the microfluidic experimental setup compared to the negative control (KRB Ref). Sample quantities are ranging n=3-6 (adhesive tape only 3 material samples, in all other cases 6 material samples (2 runs with 3 samples had been tested) with error bars representing the SD (Standard Deviation).

**Figure S2.**



**Figure S2.** (a) Schematic view of the experimental setup for flow control showing the microsystem in the center of the retainer (with inlets I<sub>1</sub> and I<sub>2</sub>, outlets O<sub>1</sub> and O<sub>2</sub>, and side ports S<sub>1</sub> and S<sub>2</sub> and contact pad holes C<sub>1-4</sub>), culture medium reservoirs (M<sub>1</sub>, M<sub>2</sub>), waste reservoirs (W<sub>1</sub>, W<sub>2</sub>), pneumatic controllers (P<sub>1</sub>, P<sub>2</sub>, P<sub>3</sub>), the flow rate sensor (Q<sub>1</sub>) and sterile syringe filters connected to the side ports S<sub>1</sub> and S<sub>2</sub> which are closed during the experiments. The main setup is positioned on top of an inverse microscope placed in a self-built incubator (red frame) using a thermo-controller coupled to a panel heater and heating foils and to a Pt-100 temperature sensor. (b) Actual image of the entire experimental setup. (c) Photo of the microsystem in the center surrounded by the peripheral flow control. (d) Zoom in of the microsystem system with the chip placed in the microfluidic holder .

**Figure S3.**



**Figure S3.** Fluorescence images of the microfluidic upper channel with sealing layer and integrated membranes at 100 mbar. (a) Chip with PDMS sealing layer (1:10) and integrated PET-membrane filled with DI-water. (b) Chip with PDMS sealing layer (1:10) and integrated PET-membrane filled with rhodamin B. (c) Chip with PDMS sealing layer (1:10) and integrated Si<sub>3</sub>N<sub>4</sub>—membrane (dashed line) filled with rhodamin B. (d) Chip with PDMS sealing layer (1:10) and integrated Si<sub>3</sub>N<sub>4</sub>--membrane (dashed line) flushed with DI-water after it was filled with rhodamin B.