



Supplementary Material

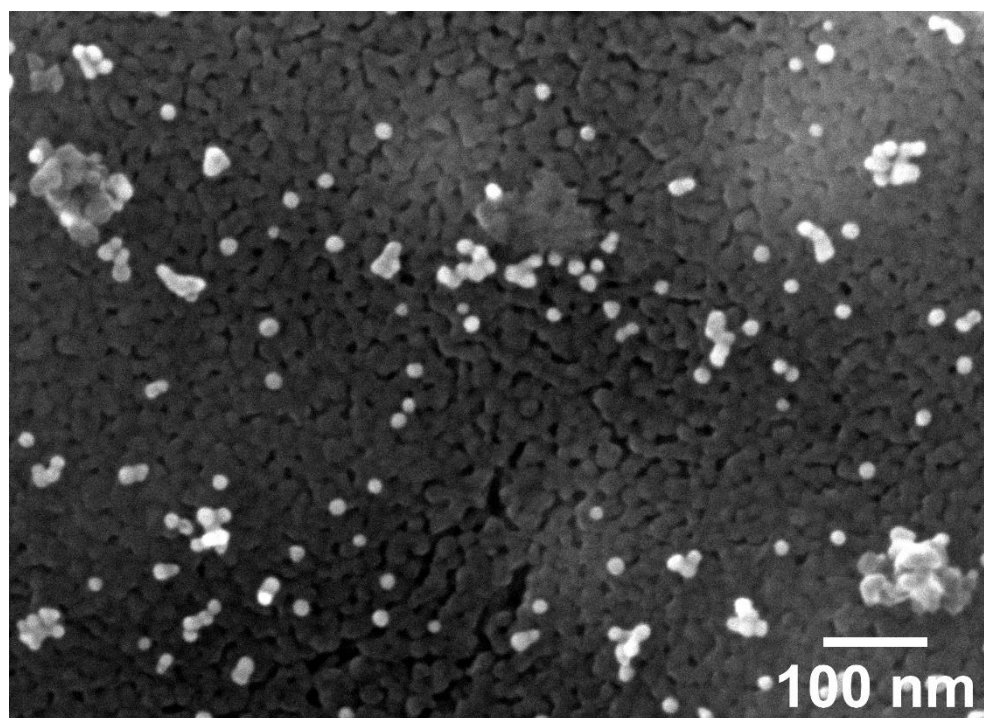
# Development of Gold Nanoparticle-Based SERS Substrates on TiO<sub>2</sub>-Coating to Reduce the Coffee Ring Effect

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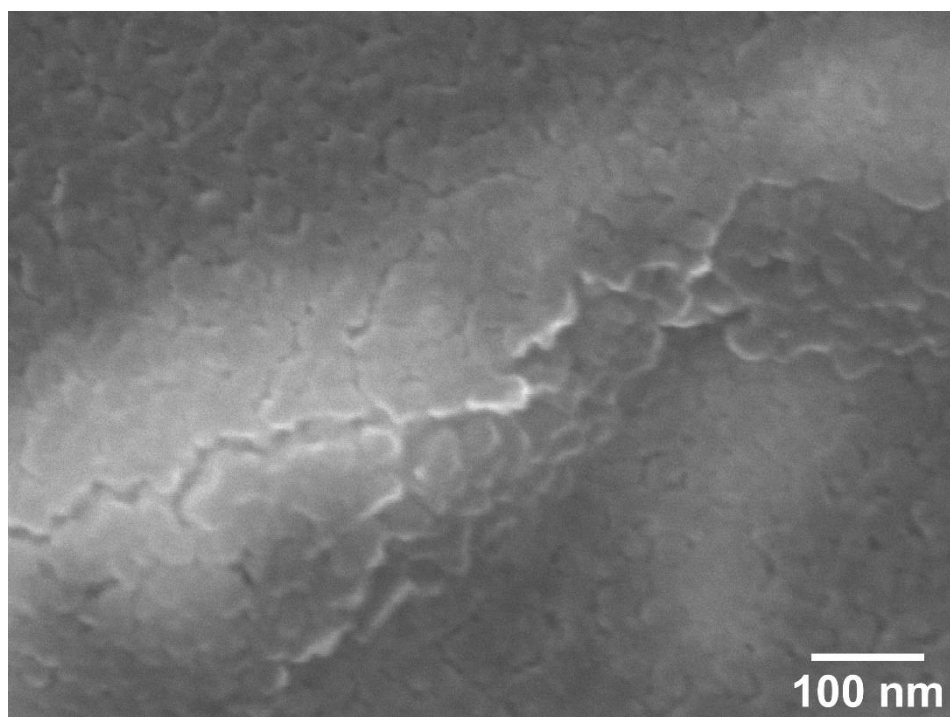
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**Figure S1.** High resolution SEM image of AuNPs onto a SERS-substrate coated with 4% TTIP and sintered at 600 °C a 100,000× magnification with a WD 6 mm and 15 kV acceleration voltage.



**Figure S2.** High resolution SEM image of a substrate with 4% TTIP and sintered at 600 °C without AuNPs at 100,000 $\times$  magnification with a WD 7,5 mm and 15 kV acceleration voltage.