





Proceedings Investigation of the Salt Concentration Dependence of Water-Gated Field Effect Transistors (WG-FET) Using 16-nm-Thick Single Crystalline Si Film ⁺

Ozan Ertop *, Bedri Gurkan Sonmez and Senol Mutlu

Department of Electrical & Electronics Engineering, Bogazici University, 34342 Istanbul, Turkey; gurkan.sonmez@boun.edu.tr (B.G.S.); senol.mutlu@boun.edu.tr (S.M.)

* Correspondence: ozan.ertop@boun.edu.tr; Tel.: +90-212-359-7368

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Abstract: This paper presents the effect of NaCl concentration on the operation of a water-gated field effect transistor (WG-FET) that uses 16-nm-thick single crystalline silicon (Si) film. In WG-FET, electrical double layer (EDL) formed at the water/silicon interface behaves as gate dielectric and this fluidic interface makes WG-FET a suitable device for sensing applications. Characteristics of EDL and the threshold voltage of WG-FET depend on the molarity of solution. Increasing the molarity of NaCl solution from 0.5 to 65 mM changes the threshold voltage from 360 to 465 mV. Accordingly, drain current of the WG-FET device changes with NaCl concentration.

Keywords: water-gated transistor; microfluidic interface; electrical double layer; salt sensor

1. Introduction

Water-gated field effect transistors (WG-FET) that use 16-nm-thick mono-Si films as channel layers promise a new platform where chemical/biological sensors and their read-out circuits can be integrated together. By using electrical double layer capacitance (EDLC) as gate insulator, high channel control can be achieved at low gate voltages with cheap and easy device fabrication. Ultrathin and high mobility characteristics of the channel layer result in high surface sensitivity in sensor applications and on-site amplification in their read-out circuits.

Applied gate potential in this device induces a voltage across the EDL capacitance, which causes accumulation of holes on the surface of p-type thin Si film as shown in Figure 1. This accumulation type device contains no p-n junctions, which reduces the fabrication cost and light sensitivity. Otherwise, electron-hole pairs can be created in these junctions in the presence of light and introduce noise to the system. IDS current of the WG-FET is determined by EDL capacitance and the threshold voltage. Similar to Si nanowire FETs [1] and ion-sensitive FETs (ISFET) [2], both threshold voltage and EDL capacitance, hence IDS, change with pH and the ion concentration of the solution as given by equations in Section 3. In this work, the effect of NaCl concentration on the operation of these WG-FETs is investigated for the first time.

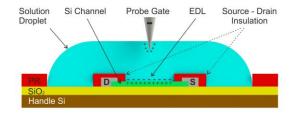


Figure 1. Operation principle of the WG-FET device.

2. Materials and Methods

WG-FET device is fabricated using a silicon-on-insulator (SOI) wafer. Fabrication steps are illustrated in Figure 2. First, 16 nm thick Si device layer is patterned to form the active region of the transistor on top of the 145-nm-thick SiO₂. Then, 200-nm-thick aluminum (Al) is thermally evaporated and patterned to build source and drain electrodes. After thermal annealing at 500 °C, ohmic contacts are obtained between Al and Si films. SiO₂ in the field regions are insulated with photoresist (PR) for repeatability enhancement [3]. Source and drain electrodes are also insulated to prevent galvanic corrosion of Al in NaCl solution.

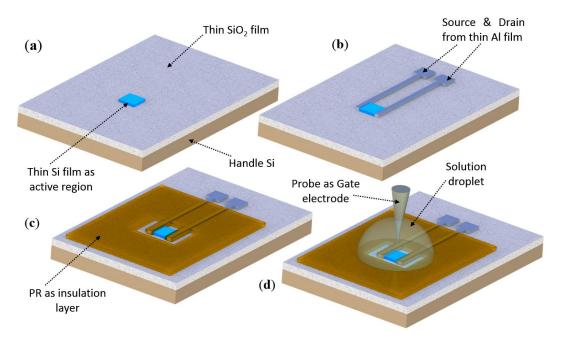


Figure 2. Fabrication steps of WG-FET: (**a**) Wet etching of thin film silicon to pattern the active region; (**b**) Thermal evaporation and patterning of 200-nm-thick Al film as drain and source electrodes; (**c**) Spin-coating and patterning of photoresist (PR) layer as insulation for the SiO₂ film and source & drain electrodes; (**d**) Placement of the solution droplet and immersion of the probe gate electrode.

A solution droplet is placed on top of the active region and a probe is immersed as the gate electrode. A device under test is shown in Figure 3. Contact pads are connected to the wires using silver epoxy paste.

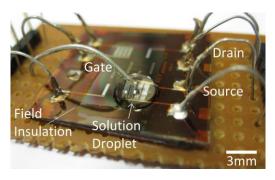


Figure 3. Device under test.

3. Results and Discussion

Characteristic *I*_{DS}-*V*_{DS} curves of the WG-FET with de-ionized (DI) water are presented in Figure 4, showing the typical transistor characteristics of the WG-FET used in this work. Applied voltages are limited to 0.6 V to eliminate any electrochemical reaction in NaCl solution, which keeps EDL operations in non-Faradaic region. Transfer characteristic curves of the transistor (*I*_{DS}-*V*_{GS}) are

measured at $V_{DS} = V_{DD}$ for different molarities, as shown in Figure 5a. Increasing NaCl concentration reduces the on-current of the WG-FET.

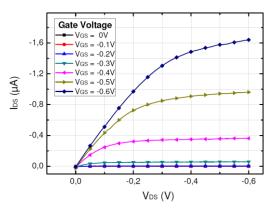


Figure 4. IDS VS. VDS graph of the WG-FET for various VGS voltages with DI-Water as the water droplet.

Threshold voltages for each NaCl molarity are extracted from the *IDS-VGS* curves and plotted in Figure 5b. These results can be explained as below with the complex dependence of EDL capacitance and threshold voltage on the ion concentration of the solution.

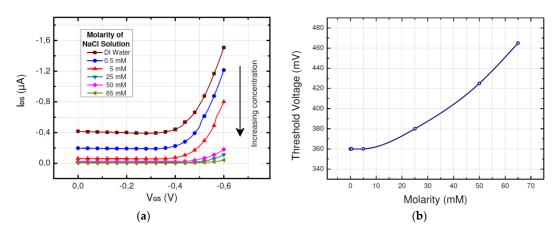


Figure 5. *IDS* vs. *VGS* graphs of WG-FET for various NaCl molarities of the solution droplet (**a**); Threshold voltage of WG-FET vs. molarity of the NaCl solution (**b**).

The WG-FET contains two EDL capacitances, one at the surface of probe gate electrode and the other on active channel. These are connected in series, hence when a potential is applied to gate electrode, only some portion of it drops across the EDL of the active channel, which we refer as the effective gate voltage V_{GS_eff} . The current passing through the WG-FET in the saturation region can be characterized by the equation

$$I_{DS} = \frac{1}{2} \frac{W}{L} \mu_{p} C_{edl} (V_{GS_eff} - V_{T})^{2},$$
(1)

where *W* is the channel width, *L* is the channel length, μ_p is the mobility of holes in thin-Si, *C*_{edl} is the EDL capacitance on the active channel and *V*_T is the threshold voltage. *C*_{edl} is a function of ion concentration. *V*_T depends on both ion concentration and pH as shown in Equations (2)–(7). These complex dependences explain the change of the WG-FET current with different solutions. EDL capacitance can be given as [4]

$$C_{\rm edl} = \sqrt{\frac{2z^2 q^2 \varepsilon \varepsilon_0 n^0}{kT}} \cosh(\frac{z q \Psi_0}{2kT}), \tag{2}$$

where *z* is the signed charge of ion, *q* is the unit charge, ε is the relative permittivity of the solution, ε_0 is the permittivity of air, n^0 is the number concentration of ion, *k* is the Boltzmann's constant, T is absolute temperature and Ψ_0 is the electrostatic potential at the surface with respect to bulk solution. This equation simplifies to Equation (3) for dilute aqueous solutions of NaCl at 25 °C where |z| = 1 [4].

$$C_{edl} = 228 \sqrt{I_0} \cosh(19.5\Psi_0), \tag{3}$$

where Io is the ion concentration. Threshold voltage of WG-FET can be expressed as [2]

$$V_{\rm T} = \Psi_{\rm bulk} - \Psi_0 + \chi^{\rm sol} - \frac{\Phi_{\rm Si}}{q} + \frac{Q_{\rm acc}}{C_{\rm edl}}$$
(4)

where Ψ_{bulk} is the potential of the bulk solution, χ^{sol} is the surface dipole potential, φ_{Si} is the work function of Si and Q_{acc} is the accumulation charge density. The change of the surface potential Ψ_0 with respect to the bulk pH is given in Equation (5) [5].

$$\Delta \Psi_0 = -\Delta p H_B \, 2.3 \frac{kT}{q} \alpha \,, \tag{5}$$

where α is a dimensionless sensitivity parameter varying between 0 and 1, which is [5]

$$\alpha = \left(\frac{2.3 \text{kTC}_{\text{edl}}}{q^2 \beta_{\text{int}}} + 1\right)^{-1} \tag{6}$$

where β_{int} is the intrinsic buffer capacity given as [5]

$$\beta_{\rm int} = 2.3a_{Hs} + N_s \frac{K_b a_{Hs}^2 + 4K_a K_b a_{Hs} + K_a K_b^2}{\left(K_a K_b + K_b a_{Hs} + a_{Hs}^2\right)^2}$$
(7)

where a_{Hs^+} is the activity of H⁺ at the active silicon layer surface related to the bulk activity, Ns is the number of sites per unit area, K_a and K_b are dimensionless dissociation constants.

Our results can be explained with the complex dependence of EDL capacitance and threshold voltage on the ion concentration of the solution as discussed above [4]. The changes in the threshold voltages of our devices with NaCl molarity agree with the study of Bergveld on the effect of NaCl concentration on ISFETs, which are depletion/inversion mode bulk Si devices with large reference electrodes, and have SiO₂ insulation [2].

4. Conclusions

It is concluded that concentration of NaCl solution used in WG-FET effects its characteristics by changing threshold voltage and EDL capacitance on the surface of thin Si film. WG-FETs can be both chemical/biological sensors and transistors, leading to an integrated platform where sensors and their read-out circuits are together.

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Conflicts of Interest: The authors declare no conflict of interest.

References

- 1. Chen, Y.; Wang, X.; Erramilli, S.; Mohanty, P.; Kalinowski, A. Silicon-based nanoelectronic field-effect pH sensor with local gate control. *Appl. Phys. Lett.* **2006**, *89*, 223512.
- 2. Bergveld, P. Thirty years of ISFETOLOGY: What happened in the past 30 years and what may happen in the next 30 years. *Sens. Actuators B Chem.* **2003**, *88*, 1–20.
- 3. Sonmez, B.G.; Ertop, O.; Mutlu, S. Improved repeatability in planar water-gated field effect transistor (WG-FET) with 16-nm-thick single crystalline Si film. *Procedia Eng.* **2016**, *168*, 1739–1742.
- 4. Bard, A.J.; Faulkner, L.R. Double-Layer Structure and Adsorption. In *Electrochemical Methods Fundamentals and Applications*, 2nd ed.; John Wiley & Sons, Inc.: New York, NY, USA, 2001; pp. 534–579.

5. Van Hal, R.E.G.; Eijkel, J.C.T.; Bergveld, P. A novel description of ISFET sensitivity with the buffer capacity and double-layer capacitance as key parameters. *Sens. Actuators B Chem.* **1995**, 24–25, 201–205.



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