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# Selected Harmonic Elimination for Cascaded Multilevel Inverter Based on Photovoltaic with Fuzzy Logic Control Maximum Power Point Tracking Technique

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Abstract: This paper describes the work performed on a single phase 9-level cascaded H-Bridge multilevel inverter (CHB-MLI) for photovoltaic (PV) power generation, using two methods of maximum power point tracking (MPPT)—namely fuzzy logic controller (FLC) and perturbation & observation (P&O). The MPPT scheme is used to extract maximum power from solar PV cells. PV cell temperature and solar irradiation are input quantities and the two used methods estimate the optimum duty cycle to ensure DC-DC boost converter maximum output. This paper also compares the results obtained by the work performed on CHB-MLI with DC-DC boost converter using FLC based MPPT controller to that of results obtained with the conventional P&O method. The fuzzy logic controller works with imprecise inputs, it does not need an accurate mathematical model and it can handle nonlinearity well. Besides, fuzzy is more robust as compared to the conventional non-linear controller. The genetic algorithm (GA) based Selective Harmonic Elimination (SHE) technique is also applied to CHB-MLI for the elimination of harmonics from the output voltage. In the work reported in this paper on PV inverter making use of the Fuzzy MPPT technique, the obtained total harmonic distortion (THD) of the output voltage without filter circuit and with filter circuit are within IEEE standard 519.

Keywords: photovoltaic; maximum power point tracking; fuzzy logic controller; cascaded H-Bridge multilevel inverter; genetic algorithm

## 1. Introduction

In recent years, renewable energy sources have emerged as a major alternative to conventional sources of energy. Among the renewable energy sources, photovoltaic (PV) cell energy has distinct features of lower carbon emissions and low maintenance [1–3].

Voltage output obtained from PV arrays is considerably less as compared to the voltage rating of most other networks, hence a DC-DC boost converter is used to improve the voltage output of PV cells. In this paper, an intelligent control method fuzzy logic controller (FLC) is applied to maximum power point tracking (MPPT) controller to control the duty cycle of DC-DC boost converter and the obtained results are compared with those of the conventional perturbation & observation (P&O) method. The boosted output voltage from the DC-DC boost converter needs to be converted to AC for commercial purposes because most of the loads are AC loads.

Various multilevel inverter (MLI) topologies have been introduced and studied in the literature for DC to AC conversion such as diode clamped, flying capacitor and cascaded H-Bridge multilevel inverter (CHB-MLI) [4–7]. Diode clamped (neutral point inverter) needs more number of diodes and the number of capacitors required is more in the flying capacitor inverter because of voltage balancing



requirement. On the other hand, CHB-MLI generates less harmonic components in output voltage. Increasing the number of output levels in CHB-MLI increases the voltage output quality step by step. In the application of PV arrays, CHB-MLIs are more suitable because in CHB-MLI each PV panel has separate DC voltage source and all H-bridge cells can be in a single module [7,8].

To extract maximum power from PV array, usually an MPPT controller is required [1,2,4]. In recent years, various type of MPPT methods are reported in literature [2,3]. Among these techniques P&O is broadly used owing to easy execution and thus resulting effortlessness [4]. P&O is an iterative method [9] in which operating point at regular interval is perturbed and it thus oscillates around the point dP/dV = 0 that is, maximum power point (MPP). On the other hand, FLC technique has also attracted attention of various researchers recently in the field of power electronics application. The FLC is easy to apply with imprecise inputs also [10,11]. In this paper, performance comparison is also presented for the work performed on single phase 9-level CHB-MLI for PV power generation using MPPT methods P&O and FLC for cases of without boost converter, with boost converter, without filter and with filter. In this work, the membership function shapes of the FLC are adjusted in terms of gap between MPP and operating point.

One of the main problems while attempting to reduce lower order harmonics is that output voltage is staircase and is not purely sinusoidal. It contains a number of odd order harmonics. The higher order harmonics can be reduced by using filters but the lower order dominant harmonics are difficult to reduce. Elimination of harmonics from cascaded multilevel inverters is one of the main challenges in applications like drives and smart grid. For this, researchers have proposed many traditional control schemes such as Newton-Raphson sequential quadratic programming, mathematical theory of resultants, pulse width modulation (sinusoidal/space vector/selective harmonic elimination) and so forth [5,12–15]. The conventional iterative method like Newton-Rephson requires an initial guess of switching angles close to the exact solution [5,12,15]. However, optimized selected harmonic elimination based control schemes also have emerged as the feasible alternative to the traditional control schemes. Thus, bio inspired optimization methods such as GA (Genetic Algorithm) and PSO (Practical Swarm Optimization) based selected harmonic elimination may also be used to increase the system robustness [12].

In three-phase system applications like the induction motor, the triple harmonics get cancelled by themselves [12]. The authors of [16,17] have considered the design of single-phase system with the consideration that in the application of interest of three-phase system, the triplen harmonics in each phase need not be canceled as they automatically cancel in the line-to-line voltages. A topology for single phase H-bridge inverter is also reported by [15] and mentioned therein that in three-phase systems the self-suppressing feature of zero-sequence harmonics gives a computational advantage as no additional measure with respect to these harmonic components is necessary. Transcendental equations can be solved subsequently through applying an appropriate optimization algorithm to obtain desired solutions. However, triplen harmonics and some other odd harmonics depending on level of inverter can also be considered for elimination besides 5th, 7th and 9th harmonics. In this paper, work on selected harmonic elimination (SHE) of lower order harmonics (5th, 7th and 9th) from the output voltage of CHB-MLI is reported using Genetic Algorithm, by obtaining optimized switching angles of 9-level CHB-MLI targeting minimum of total harmonic distortion (THD) [5].

The organization of this paper is as follows: Section 2 describes the structure of PV solar cell and its modelling. Section 3 describes DC-DC boost converter configuration. PV MPPT methods P&O and FLC are described in Sections 4 and 5, respectively. PV based 9-level CHB-MLI configuration and switching schemes are described in Section 6. Optimization problem for selective harmonic elimination is described in Section 7, which involves fitness function (total harmonic distortion), constraints of the problem and other intermediate mathematical equations for computation of switching angles of PV based CHB-MLI. GA based optimization procedure used in the work reported in this paper to obtain the optimal values of switching angles by selective elimination of harmonics (5th, 7th and 9th) is described in Section 8. Results of comparison obtained using two methods of MPPT that

is, P&O and FLC for single phase PV based 9-level CHB-MLI without boost converter, with boost converter, without filter and with filter cases are presented in Section 9. The results are obtained using MATLAB software (8.0.0.783, R2012b). Section 10 presents the conclusion.

#### 2. Modeling of Photovoltaic Solar Cell

The structure of the PV solar cell is mainly silicon p-n junction and directly converts absorbed solar energy into electrical energy [8–10,18]. The PV array has number of PV cells allied in series and/or parallel [11]. When it is exposed to light, it releases electrons across the closed electric circuit. The symbolic structure of a PV solar cell is depicted in Figure 1. When electrons collide with photons, cells attain higher levels of energy and electrons become free to move across the junction to produce current. The photo current  $I_{pv}$  produced through the PV cell is dependent on solar illumination level [10,18].



Figure 1. Solar cell circuit model.

Applying Kirchhoff's current law in Figure 1, PV cell output current ( $I_{pv}$ ) can be expressed as under [18]:

$$I_{pv} = I_{ph} - I_D - I_{sh} \tag{1}$$

Generated current through photon ( $I_{ph}$ ) is proportional to the solar irradiation,  $I_D$  is diode current and  $I_{sh}$  is shunt resistor current. The PV module characteristic equation can be described as:

$$I_{pv} = C_s I_{ph} - I_D - I_{sh} \tag{2}$$

$$C_p I_{ph} - C_p I_o \left[ \exp\left(\frac{q\left(V_{pv} + R_s I_{pv}\right)}{C_s Z K T}\right) - 1 \right] - \frac{V_{pv} + R_s I_{pv}}{R_{sh}}$$
(3)

where, saturation current is  $I_o$ , PV module voltage output is  $V_{pv}$ , series resistance is  $R_s$ , shunt resistance is  $R_{sh}$ , no. of cells in series is  $C_s$ , no. of cells in parallel is  $C_p$ , electron charge is q, identity factor of diode is Z, Boltzmann constant is K and T is the cell temperature.

 $I_{ph}$  and  $I_o$  are expressed in Equations (4) and (5), respectively as under:

$$I_{ph} = I_{scc} + K_i (T - T_r) \frac{G}{G_r}$$
(4)

$$I_o = I_{sc} \left(\frac{T}{T_r}\right)^3 \exp\left(\frac{qE_{go}}{C_s AZT} \left(\frac{1}{T_r} - \frac{1}{T}\right)\right)$$
(5)

where  $I_{scc}$  is PV module standard test short circuit current at conduction (STC) 25 °C with 1000 W/m<sup>2</sup>, short circuit current coefficient temperature is  $K_i$ , reference temperature is  $T_r$  in Kelvin, G is the solar irradiation,  $G_r$  is reference solar irradiation,  $I_{sc}$  is the current saturated at 25 °C and  $E_{go}$  is the band gap.

#### 3. DC-DC Boost Converter Configuration

A DC-DC boost converter is used between the PV array and inverter to supply required DC voltage and maintain load voltage constant [1]. The input-output relation of voltage for continuous conduction mode can be defined as under [18,19]:

$$\frac{V_o}{V_{in}} = \frac{1}{1 - D} \tag{6}$$

where, *D* is duty cycle,  $V_{in}$  and  $V_o$  are input and output voltages of boost converter. When *D* increases from 0 to 1, output becomes higher than input voltage. DC-DC boost converter arrangement with PV is depicted in Figure 2.



Figure 2. System configuration for photovoltaic (PV) based DC-DC Boost converter.

In PV array with increase in the duty cycle of boost converter, average current of PV array increases and a result voltage of PV decreases. Thus, raising duty cycle results in movement of the operational point to the left of the PV array *V-I* haracteristic. Likewise decrease in the duty cycle results in decrease in PV array average current and PV array output voltage increases. It results in operating point shifting to the right of PV array *V-I* characteristic. P&O and FLC are used to automatically vary the duty cycle of DC-DC converter to obtain constant DC voltage at CHB-MLI terminal.

## 4. Perturbation and Observation (P&O) MPPT

The MPPT method automatically finds the current or voltage at which a PV array should work to extract the maximum output power under given temperature and irradiance conditions [20]. The P&O algorithm is very common and suitable for practical applications [19,21,22]. This algorithm is based on the system perturbation by the decreasing or increasing duty cycle and the results are seen on PV output panel. The operational voltage of the PV module is perturbed with a small increment and the consequential change in power ( $\Delta P$ ) is observed. If  $\Delta P$  is positive, it is considered that operating point is reaching towards the MPP. If the  $\Delta P$  is negative, the operating point moves away from the MPP. As a result, the reverse perturbation direction is needed in the next perturbation cycle. The process is repetitive and duty cycle is generated to feed to the DC-DC boost converter. This algorithm is especially suitable during constant or slow-varying atmospheric conditions [2,8]. However, FLC helps to reduce the operating voltage response time, consequently minimizing the loss of power inside the PV system [19]. FLC based MPPT is discussed in the next section.

#### 5. Fuzzy Logic Control (FLC) Based MPPT

Conventional method P&O may give error in results that is, when solar irradiance is increased, the algorithm moves in the direction of high power and it fixes the operating point, which is not maximum power point (MPP) [22]. The FLC method is applied in this work in pursuit of improving the control performance of MPP through simulation and modelling based fuzzy logic

rule [8]. FLC technique controller response is faster and also improves the system stability while attempting to reach MPP [8,19]. The search process of MPP based on FLC is a heuristic rule and results in being adaptive stepwise, sensorless with respect to sunlight, and convergence is quick with a change in temperature [8,10,11,23]. Figure 3 represents the basic concept of FLC based MPPT controller.



Figure 3. Flow chart of fuzzy logic controller (FLC) controller.

The inputs to FLC MPPT are usually an error *E* and a change in error  $\Delta E$ . Both are defined in this section. The FLC consists of a functional block, that is, fuzzification, a fuzzy controller algorithm rule and defuzzification. Description of these is presented below:

*Fuzzification*: The FLC requires input variables for control rules in the form of fuzzy set information with linguistic labels. Actual voltage of the PV array (V) and current (I) are measured constantly to calculate power. The control basis is two input variables that is, error E and change of this error (*CE*) at the sampling time K [7]. The expressions of variables E and *CE* are as follows:

$$E(K) = \frac{P(K) - P(K-1)}{I(K) - I(K-1)}$$
(7)

$$CE(K) = E(K) - E(K - 1)$$
 (8)

Here P(K) and I(K) are the power and current for the PV array, respectively. By using fuzzification of the input variables in Figure 4a,b and thus obtaining the output of Figure 4c, they can be compared with pre-defined target values from the fuzzy toolbox. If at the instant *K* the input E(K) is the operating point located on the left or on the right of the MPP of the *P-I* characteristic, the input error CE(K) expresses route of displacement of operating point. The use of the DC-DC converter is to modify the duty ratio delta-*D* as the output of the projected controller. Consequently, according to slope E(K), by varying the duty ratio control the point is brought back to the point where the slope is zero.

Algorithm Rules (Inference Engine): Inference engine consists of fuzzy rule base and sub-block of fuzzy implication. In this process fuzzifed inputs are fed to the interface engine to apply fuzzy rules. The rule settings of FLC MPPT, different number of subset has been used block view of the fuzzy logic algorithm in Simulink window. In this paper five linguistic variable are used such as negative big (NB), negative small (NS), zero (ZE), positive small (PS) and positive big (PB) at MATLAB fuzzy toolbox. Membership functions of basic five subsets of fuzzy input and output variables are shown in Figure 4. Table 1 shows the control rules of fuzzy associative memory, where the matrix entries are fuzzy sets of error (*E*), change of error (*CE*) and change in duty ratio ( $\Delta D$ ) to the DC-DC boost converter.

Table 1. Control rules of fuzzy associative memory.

CE/E	NB	NS	ZO	PS	PB
NB	ZO	ZO	NB	NB	NB
NS	ZO	ZO	NS	NS	NS
ZO	NS	ZO	ZO	ZO	PS
PS	PS	PS	PS	ZO	ZO
PB	PB	PB	PB	ZO	ZO



Figure 4. Membership function for inputs and outputs.

In general, fuzzy control uses methods such as Max-Prod, Somme-Prod and Max-Min interface technique [7,11]. In this paper, the work on the fuzzy technique is performed using the Mamdani inference method, which is an under max-min interface technique of fuzzy combination. The fuzzy control surface for 25 fuzzy control rules on graphical form is presented in Figure 5.



Figure 5. Fuzzy control surface for MPPT.

The output rule viewer of FLC system in MATLAB window is shown in Figure 6, where the value of error (*E*), change in error (*CE*) and duty ratio are 0.04, 10.2 and 0.0183, respectively.

Rule Viewer:	PF11		
ile Edit Vi	ew Options		
E =	0.04	CE = 10.2	Delta-D = 0.0183
1 2			
4 5			
67	<u></u>		
8 4			
13			
15	$\geq$		
18			
20			
22			
25			
iput: [0.04;10	18]	Plot points: 101	Move: left right down up
)pened system	PF11, 25 rules		Help Close

Figure 6. Rule viewer of fuzzy logic controller (FLC) system.

*Defuzzification*: After evaluation of rules, the fuzzy control algorithm calculates the crisp output of the fuzzy controller by performing defuzzification. In this paper, center of gravity method is used for defuzzification [7,8]. The center of gravity defuzzification method in a system of rules is formally given by:

$$\Delta D = \frac{\sum_{i=1}^{n} w_i (\Delta D_i) \cdot \Delta D_i}{\sum_{i=1}^{n} w_i (\Delta D_i)}$$
(9)

where *n* is the maximum number of effective rules and  $w_i$  is the weighting factor. The output of fuzzy controller  $\Delta D(K)$  is obtained from defuzzification process by Equation (9). Actual duty ratio D(K) is obtained as under by making use of gain  $SG_{\Delta D}$ .

$$D(K) = D(K-1) + SG_{\Lambda D} \times \Delta D(K)$$
(10)

Using the steps mentioned above, the fuzzy controller is implemented for MPPT. Generated output pulse of FLC is used to manage the insulated-gate bipolar transistor (IGBT) switch of DC-DC boost converter.

#### 6. Photovoltaic Based CHB-MLI Configuration and Switching Scheme

CHB-MLI consists of series combination of two or more single phase H-Bridge inverters, where H-bridge is arrangement using switches such as IGBT, metal-oxide-semiconductor field-effect transistor (MOSFET) and so forth [24–29]. CHB-MLI has the benefits of "one converter per panel", such as enhanced operation per PV module, mixing capability of different sources and redundancy of the system. Through the separate DC links voltage control of multilevel inverter is possible independently. As a result, individual MPPT control of each PV panel can be achieved and the harvested energy from PV panel can be maximized. Thus, the modularity and low cost of CHB-MLI makes it a major contender for its proficient, robust and reliable application in solar power electronics [26]. In the work reported in this paper on single phase 9-level CHB-MLI for PV power

generation using MPPT methods, this paper addresses the issues of requirement of individual MPPT to solve mismatch issues of PV panels and in doing so a control scheme with intelligent MPPT (FLC) is also proposed. If each PV panel is not controlled separately, the overall efficiency of PV system will be decreased [26]. A single phase 9-level CHB-MLI with PV system and DC-DC boost converter configuration is shown in Figure 7.



Figure 7. Single phase 9-level CHB-MLI configuration integrated with PV panel.

The major advantages of the CHB-MLI are that the regulation of the DC buses is easy and it has modularity control [30–32]. Table 2 presents the switching schemes of PV system based 9-level single phase CHB-MLI output voltage making use of 16 switches ( $T_1$  to  $T_{16}$ ).

Inverter having equal steps was considered in the work of [15]; however, some investigators may try to provide an optimization of voltage levels of unequal magnitude as shown in Figure 8 as per reference [12]. Output waveform pattern with unequal DC source of single phase 9-level CHB-MLI is shown in Figure 8. The entire output voltage of MLI is specified by  $V = V_{dc} + 2V_{dc} + 3V_{dc} + 4V_{dc}$ .

Output Voltage	Combination	Combination of Switching
$+4V_{dc}$	$V_{dc} + 2V_{dc} + 3V_{dc} + 4V_{dc}$	T <sub>1</sub> , T <sub>2</sub> , T <sub>5</sub> , T <sub>6</sub> , T <sub>9</sub> , T <sub>10</sub> , T <sub>13</sub> , T <sub>14</sub>
$+3V_{dc}$	$V_{dc} + 2V_{dc} + 3V_{dc}$	T <sub>1</sub> , T <sub>2</sub> , T <sub>5</sub> , T <sub>6</sub> , T <sub>9</sub> , T <sub>10</sub>
$+2V_{dc}$	$V_{dc} + 2V_{dc}$	T <sub>1</sub> , T <sub>2</sub> , T <sub>5</sub> , T <sub>6</sub>
$+V_{dc}$	$V_{dc}$	T <sub>1</sub> , T <sub>2</sub>
0	$0V_{dc}$	T <sub>1</sub> , T <sub>3</sub>
$-V_{dc}$	$-V_{dc}$	T <sub>3</sub> , T <sub>4</sub>
$-2V_{dc}$	$-(V_{dc}+2V_{dc})$	T <sub>3</sub> , T <sub>4</sub> , T <sub>7</sub> , T <sub>8</sub>
$-3V_{dc}$	$-(V_{dc}+2V_{dc}+3V_{dc})$	T <sub>3</sub> , T <sub>4</sub> , T <sub>7</sub> , T <sub>8</sub> , T <sub>11</sub> , T <sub>12</sub>
$-4V_{dc}$	$-(V_{dc} + 2V_{dc} + 3V_{dc} + 4V_{dc})$	$T_3, T_4, T_7, T_8, T_{11}, T_{12}, T_{15}, T_{16}$

Table 2. PV system based 9-level single phase CHB-MLI switching scheme.



Figure 8. Output waveform pattern with non-equal DC source of single phase 9-level CHB-MLI.

Four switching angles  $\alpha_1$ ,  $\alpha_2$ ,  $\alpha_3$  and  $\alpha_4$  are calculated by solving non-linear equating by GA to minimize the THD and resulting output waveform pattern with non-equal DC source of single phase 9-level CHB-MLI is shown in Figure 8 [12].

#### 7. Optimization Method for Selected Harmonic Elimination (SHE)

Currently SHE technique is being prominently used to synthesize output voltage of multilevel inverters [5,27,33]. By applying Fourier series analysis, the staircase output voltage of cascaded multilevel inverter with non-equal DC sources can be described as [15]:

$$V(\omega t) = \sum_{n=1}^{\infty} V_n \sin(n\omega t)$$
(11)

where  $V_n$  is the magnitude of the *n*th harmonic. The even order components are equal to zero for such quarter-wave symmetrical signals. Therefore, Equation (11) can be expanded based on the magnitude of each harmonic order as follows. So, the new equation of output voltage becomes as mentioned below:

$$V_n = \begin{cases} \frac{4V_{dc}}{n\pi} \sum_{i=1}^{\infty} K_i \cos(n\alpha_i), & \text{for odd } n\\ 0, & \text{for even } n \end{cases}$$
(12)

where  $V_{dc}$  is the base voltage and  $K_i$  is the ratio of  $V_{dci}$  to  $V_{dc}$ . For example,  $V_{dc1} = V_{dc} \times K_1$ .

Low-order harmonics are generally the targets to reduced using GA technique is concerned. For given voltage levels and switching angles, *F* unknown variables should be solved to settle the equation set. Fundamental component forms the Equation (11) as indicated in (13)

$$M = \frac{1}{F} \left[ K_1 \cos \alpha_1 + K_2 \cos \alpha_2 + K_3 \cos \alpha_3 + K_4 \cos \alpha_4 + \dots + K_L \cos \alpha_L \right]$$
(13)

where *M* is defined as under:  $M = \frac{V_1}{4FV_{dc}/\pi} (0, M < 1)$ .

Consideration of elimination of triplen harmonics has been described in Section 1. In this work, targeting the 5th, 7th & 9th harmonics, the four non-linear equations for the solution of the problem are taken as under:

$$\begin{split} &\frac{1}{4} [K_1(\cos \alpha_1) + K_2 \cos(\alpha_2) + K_3 \cos(\alpha_3) + K_4 \cos(\alpha_4)] = M \\ &K_1(\cos(5\alpha_1) + K_2 \cos(5\alpha_2) + K_3 \cos(5\alpha_3) + K_4 \cos(5\alpha_4) = 0 \\ &K_1(\cos(7\alpha_1) + K_2 \cos(7\alpha_2) + K_3 \cos(7\alpha_3) + K_4 \cos(7\alpha_4) = 0 \\ &K_1(\cos(9\alpha_1) + K_2 \cos(9\alpha_2) + K_3 \cos(9\alpha_3) + K_4 \cos(9\alpha_4) = 0 \end{split}$$

By solving above non-linear equations are targeted to eliminate the 5th, 7th and 9th order harmonics. When conventional technique such as Newton Raphson (NR), resultants methods and so forth are tried to eliminate 5th, 7th and 9th order harmonics, it is known that they have disadvantage of involving high computation time in solving complicated non-linear equation. In this paper, GA approach is used to obtain the optimal value of switching angles of PV based 9-level CHB-MLI considering the total harmonic distortion (THD) an objective function by elimination of 5th, 7th and 9th order harmonics at different value of switching angles. Switching angles are in the range under:

$$0 < \alpha 1 < \alpha 2 < \alpha 3 < a_4 < \frac{\pi}{2} \tag{14}$$

To check the quality of voltage waveform, THD can be defined and calculated as follows

Minimize, %THD = 
$$\left[\frac{1}{V_1^2} \sum_{K=2}^{\infty} (V_K)^2\right]^{\frac{1}{2}} \times 100$$
 (15)

where,  $V_K$  is the voltage of particular harmonics.

#### 8. Genetic Algorithm (GA) Based Optimization Technique for Calculation of Switching Angle

GA is generally used to produce high quality results to complex search problems. The GA based is on relying bio-inspired processes such as crossover, mutation and selection [34,35]. It is capable of solving constrained and unconstrained optimization problems based on proper selection of initial population and frequently modifying this individual population for solutions. By selection of individual solutions from each generation, over successive generation multiple individual solutions in this case (switching angle) are selected from the existing inhabitants and the selected solutions are taken in next iteration of the algorithm. The search ends when a final set of solutions for the problem by consecutive generations from a large population is achieved, based on some terminating criteria. Four switching angles are calculated in the present work to achieve the minimum THD keeping the harmonics within the limits as mentioned in Equation (15). In the GA toolbox, graphical user interface allows using GA at MATLAB platform without working at the command line [5,34]. The nonlinear equations involved in the present problems are solved with the help of GA toolbox where fitness function, four variables with their range, non-linear constraints and terminating criteria are required to solve the problem [36]. The detail of these is given below:

In the work reported in this paper for the design of PV based 9-level CHB-MLI, four switching angles are taken. The best fitness and number of variables obtained using GA toolbox is shown in Figure 9.



Figure 9. Result obtained of best fitness and number of variable by genetic algorithm (GA) toolbox.

Switching angles of IGBTs are applied through pulse generators. In this GA work, no. of iterations as 100 are selected at GA toolbox. Results of switching angles obtained from GA toolbox are shown in Table 3.

	No. of Iteration in Use		Switching Angle (Degree)			Mean Fitness (%)	Best Fitness (%)
GA Results	100	$\alpha_1$	α2	α3	$\alpha_4$	0.0836291	0.120302
		20.557	37.679	59.161	78.801		

Table 3. Results of switching angle obtained from GA toolbox.

#### 9. Results and Analysis

Simulation was performed for two cases viz. for PV based boost converter with P&O MPPT incorporated CHB-MLI and for PV based boost converter with FLC MPPT CHB-MLI. Results for both cases are compared for changed temperature and irradiation level. The FLC based algorithm performs fairly well when climate changes. Also, the DC output of each PV panel has less response time and is suitable for input CHB-MLI.

Each solar panel is connected to a separately DC-DC boost converter in order to evaluate the performance of the P&O and FLC MPPTs under changing temperature and irradiation. FLC method can rapidly track the MPP under quickly varying irradiation and temperature with small change in oscillation, while the P&O fails to reach MPP under fast environmental changes. They suffer from high oscillation around MPP and more power loss [21]. FLC MPPT technique based solar PV system gives much more output voltage as compared to P&O MPPT. Some results of comparison between output voltage and current for each of four panels without boost converter using P&O MPPT and FLC technique are in Figure 10a–d.

The comparison of output power of panel<sup>1</sup> without boost converter is shown in Figure 11. The average power of panel<sup>1</sup> without boost converter obtained from both MPPT techniques are approximately equal but FLC based technique produces less oscillation and less power loss as compared to P&O technique.

To increase the PV output voltage boost converter is used. Thus, the obtained boosted output voltage from the solar PV arrays is suitable for network voltage rating. The comparison of output voltage for each PV panel using boost converter obtained from P&O and FLC technique is shown in Figure 12. It can be observed that stability of system is high and output voltage produced is more with less response time (convergence speed) using FLC based MPPT technique as compared to P&O MPPT technique using boost converter.



Figure 10. Cont.



**Figure 10.** Comparison of output voltage and output current without boost converter obtained using P&O MPPT (**left**) and FLC MPPT (**right**) methods for PV (**a**) panel<sup>1</sup>, (**b**) panel<sup>2</sup>, (**c**) panel<sup>3</sup> and (**d**) panel<sup>4</sup> respectively.



**Figure 11.** Comparison of output power obtained using (**a**) P&O MPPT and (**b**) FLC MPPT methods for PV panel<sup>1</sup> without boost converter.





**Figure 12.** Comparison of output voltage obtained using P&O MPPT (**left**) and FLC MPPT (**right**) methods for PV (**a**) panel<sup>1</sup>, (**b**) panel<sup>2</sup>, (**c**) panel<sup>3</sup> and (**d**) panel<sup>4</sup> respectively with boost converter.

The obtained output voltage of each panel with boost converter is given to 9-level CHB-MLI. Switching angles of IGBTs are applied through pulse generators. In performance optimization using GA to obtain the optimal values of switching angles number of iteration as 100 is selected as it was observed that in doing so with successive generations making use of applied GA method the optimal values were obtained. The comparison of CHB-MLI output voltage without filter, obtained with optimal switching angles fed to inverter IGBTs and using both MPPT methods P&O and FLC based boost converter is shown in Figure 13. A simple R-L load is connected to the inverter and the output voltage of PV based inverter with boost converter using FLC MPPT technique is more and has less oscillation as compared to P&O MPPT based inverter.



**Figure 13.** Overall output voltage of 9-level CHB-MLI without filter for (**a**) PV based boost converter obtained by P&O MPPT method (**b**) PV based boost converter obtained by FLC MPPT technique.

FFT analysis of THD present in the output voltage of CHB-MLI, using both methods, is shown in Figure 14. The % THD obtained from P&O MPPT technique is 9.52% and from FLC MPPT is 6.88%. Thus, it is observed that FLC MPPT based CHB-MLI technique shows improved performance.



**Figure 14.** Output voltage FFT analysis of THD present in of 9-level CHB-MLI without filter using by (a) PV based boost converter obtained by P&O MPPT (b) PV based boost converter obtained by FLC MPPT technique.

From Figure 14 it can be observed that lower order harmonics are almost eliminated for both PV based boost converter by P&O MPPT technique as well as PV based boost converter with FLC MPPT technique.

To obtain the sinusoidal output voltage waveform from multilevel inverter use of filter circuit was also attempted in this work. Resistor-capacitor (RC) filter circuit was used to obtain output voltage with less distortion for both MPPT techniques based multilevel inverter. Figure 15 shows the comparison of output voltage waveform with filter circuit obtained for PV based boost converter using P&O MPPT and using FLC MPPT methods.

Figure 16a,b shows the comparison of FFT analysis of output voltage THD with filter circuit corresponding to cases of Figure 15a,b.



**Figure 15.** Overall output voltage of PV based 9-level CHB-MLI obtained using with filter and using (a) P&O MPPT (b) FLC MPPT technique.



**Figure 16.** The comparison of Fast Fourier Transform (FFT) analysis of THD of output voltage with filter for PV based boost converter obtained using (**a**) P&O MPPT technique (**b**) FLC MPPT technique.

Thus, it is observed that for the 9-level PV based CHB-MLI boost converter with FLC MPPT technique the output voltage is more and THD is less, as compared to that obtained using the P&O MPPT technique. A comparison of CHB-MLI performance obtained using P&O and FLC MPPT techniques is shown in Table 4.

**Table 4.** Comparison of performance obtained using P&O and FLC MPPT techniques for 9-level PVbased CHB-MLI.

S. No.	Property	P&O	FLC
1	Convergence speed	Medium	Fast
2	Oscillation close to MPP	Medium	Less
3	Converter duty cycle	Fixed step size	Variable step
4	Implementation complexity	Easy	Medium
5	Power loss	More	Less
6	Overall output voltage of PV based 9-level CHB-MLI with filter circuit	Less (Figure 15a)	More (Figure 15b)
7	THD of output voltage PV based CHB-MLI without filter circuit	9.52% (Figure 14a)	6.88% (Figure 14b)
8	THD of output voltage of PV based CHB-MLI with filter circuit	3.37% (Figure 16a)	1.84% (Figure 16b)

Therefore, the % THD of the output voltage with filter using P&O technique is 3.37% and using FLC based inverter % THD is less i.e., 1.84%, satisfying the IEEE 519 standard.

#### **10. Conclusions**

This paper presents the work performed on single-phase 9-level PV based CHB-MLI using two MPPT techniques that is, P&O and FLC. Results of comparison obtained using P&O and FLC methods without boost converter, with boost converter, without filter and with filter cases are also presented. By used filter circuit the staircase output voltage waveform of CHB-MLI is improved. Using boost converter with regard to voltage output oscillations, P&O method seems to have just little edge over FLC method; however, FLC method controller response time (convergence speed) for voltage output continued to be lower as compared to P&O method both with & without boost converter. Thus, this work describes that in similar environmental conditions, FLC MPPT based CHB-MLI is capable of increasing the output voltage, reducing power loss, reducing response time of voltage output and reducing THD.

Genetic algorithm is also used in this work targeting selective elimination of harmonics (5th, 7th and 9th) from output voltage to obtain the optimal values of switching angles of single-phase 9-level CHB-MLI corresponding to the minimum of THD. From the obtained results from GA it can be concluded that selective harmonic elimination can be useful for PV based CHB-MLI. The results are obtained using MATLAB software.

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