

Editorial

Circuit and Signal Processing Section of Electronics: Editorial 2023

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What follows is a brief history of the Circuit and Signal Processing Section of *Electronics*: In 2018, this Section was divided from *Electronics*. In April 2020, Prof. Dr. Flavio Canavero was appointed Section Editor-in-Chief of this Section. In March 2021, Prof. Dr. Costas Psychalinos was appointed Section Editor-in-Chief of this Section. This Section has 61 Editorial Board Members.

The aim of this Section is to host significant and original contributions and state-of-the-art review articles in this field of research. Its scope is broad, ranging from mathematical foundations to practical engineering applications, measurement techniques, compliance verification, and reliability studies.

Topics of interest for this Section include, but are not limited to, the following:

- Circuits and systems for the Internet of Things (IoTs);
- Circuits and systems for 5G networks;
- Circuits and systems of Radio Frequency (RF) applications;
- Analog, digital, and mixed-signal processing building blocks;
- Non-linear circuits and systems;
- Amplifiers, filters, oscillators;
- Circuits and systems for biomedical applications;
- Power and energy circuits and systems;
- Multimedia systems and signal processing.

More detail can be found at the following link:

https://www.mdpi.com/journal/electronics/sections/Circuit_Signal_Processing (accessed on 26 December 2023)

With the end of 2023, I am pleased to share the Annual Statistical Data of the Circuit and Signal Processing Section:

- Papers Published: 261;
- Median Publication Time: 38 days;
- Special Issue setup: 62;
- Rejection rate: 31.24%.

During 2024, we intend to further enhance our Section by including fresh topics, such as:

- Artificial intelligence-based learning for circuits and systems with low power consumption;
- Artificial intelligence applications to typical integrated circuits (including Analog/Digital, RF, mmWave);
- Memristor-based architectures based on artificial intelligence/machine learning techniques;
- Memristor-based neuromorphic computing systems;
- Energy harvesting circuits for wearable and implantable devices;
- Architectures for neural network applications;
- Circuits for stochastic computing systems.



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We look forward to receiving your valued contributions to make this Section a significant resource of knowledge for researchers in the engineering field. Both original research articles and comprehensive review papers are welcome.

Finally, I would like to thank all our authors, reviewers, readers, and members of the Editorial Board and Editorial Offices for their confidence and continuous support.

Conflicts of Interest: The authors declare no conflict of interest.

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