

Article

Large-Signal Stabilization Method for Islanded DC Microgrids Considering Battery and Supercapacitor Hybrid Energy Storage Systems

Xinbo Liu * [,](https://orcid.org/0000-0002-0948-4449) Yongbing Suo, Xiaotong Song, Jinghua Zhou and Yaxin Qu

Abstract: Islanded DC microgrids composed of distributed generators (DGs), constant power loads (CPLs), parallel converters, batteries and supercapacitors (SCs) are typical nonlinear systems, and guaranteeing large-signal stability is a key issue. In this paper, the nonlinear model of a DC microgrid with a hybrid energy storage system (HESS) is established, and large-signal stability criteria are obtained. The HESS consists of batteries and SCs. The derived criteria reveal the influences of the filter parameters, CPL power, DG power and the proportional control parameters of the battery converter and the SC converter on the system large-signal stability. Furthermore, important large-signal stabilization methods for regulating the HESS converter's control parameters can easily achieve the large-signal stabilization of islanded DC microgrids without extra equipment. The paper is summarized as follows: First, the topology of and control strategy for a DC microgrid with an HESS and CPLs are proposed. Then, according to the characteristics of the HESS, the DGs and the CPLs, the system is equivalently simplified. Finally, the nonlinear model and large-signal stability criteria are both derived using the mixed potential theory, and a large-signal stabilization design method for the HESS converter's control parameters is proposed. The experimental and simulation results show the effectiveness of the proposed large-signal stabilization method.

Keywords: large-signal stability; islanded DC microgrids; hybrid energy storage system (HESS); mixed potential function; constant power loads (CPLs); parallel converters

1. Introduction

In DC microgrids, reactive power, harmonics, frequency and phase are not involved, as only DC voltage is included and considered. DC microgrids integrate distributed generators (DGs), different loads, energy storage systems (ESSs) and various converters and are increasingly utilized [\[1–](#page-17-0)[3\]](#page-17-1). Unfortunately, the inertia of islanded DC microgrids is very small, and the DC bus voltage is extremely sensitive to disturbances. Furthermore, large power variations of DGs and loads commonly exist in DC microgrids; consequently, maintaining DC bus voltage stability becomes the key issue [\[4–](#page-17-2)[8\]](#page-17-3).

The ESS is used to eliminate the DC bus voltage fluctuations of DC microgrids, and the appropriate control of the ESS could enhance the system stability $[9-11]$ $[9-11]$. Batteries are widely adopted in ESSs; however, frequent charging and discharging processes significantly decrease their work life. Consequently, DC microgrids mostly use hybrid energy storage systems that combine high-energy-density batteries with high-power-density supercapacitors [\[12,](#page-17-6)[13\]](#page-17-7). The batteries absorb or release low-frequency-power fluctuations, while high-frequency-power fluctuations are stabilized by the SC. The parallel converters of batteries and SCs introduce great difficulties for HESS control and DC microgrids' stability [\[14\]](#page-17-8). Furthermore, research on HESSs mostly focuses on control strategies and power-allocation optimizations. Intelligent algorithms such as PI control, model predictive control, sliding mode control and fuzzy control have been widely used in HESS converters [\[15\]](#page-17-9). The lowpass-filtering method and wavelet packet decomposition method are utilized to achieve

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School of Electrical and Control Engineering, North China University of Technology, Beijing 100144, China ***** Correspondence: liuxinbo@ncut.edu.cn

power allocation [\[16\]](#page-17-10). However, there are few studies about the quantitative stability analysis of HESS converters' control parameters on islanded DC microgrids.

On the other hand, in DC microgrids, most of the loads connected to the DC bus through closed-loop control converters are equivalent to constant power loads [\[17\]](#page-17-11). The negative incremental impedances of the CPLs significantly deteriorate system stability during large disturbances, and the CPLs' influences on islanded DC microgrids is extremely strong [\[18,](#page-17-12)[19\]](#page-17-13). Consequently, many methods have been proposed to compensate the characteristics of the CPLs.

In [\[20](#page-18-0)[,21\]](#page-18-1), active or passive damping is added to the source converters to compensate the CPLs. In order to increase the system damping, the authors of [\[22\]](#page-18-2) increase the DC bus capacitance, while the authors of [\[23\]](#page-18-3) propose the virtual impedance method. The authors of [\[24\]](#page-18-4) and [\[25\]](#page-18-5) propose a sliding-mode duty-ratio controller, backstepping and a nonlinear disturbance observer for a DC-DC converter with CPLs. However, these studies mainly focus on improving system stability based on specific control or analysis methods and are applicable for single DC-DC converters feeding CPLs. The authors of [\[26\]](#page-18-6) propose a control method based on DC current feedforward, and the authors of [\[27\]](#page-18-7) present a Takagi–Sugeno fuzzy-based model predictive control strategy to improve the stability of DC microgrid systems under the influence of CPLs. Unfortunately, there are few references that consider the negative influences of the CPLs and simultaneously also consider the positive influences of HESS converters' control parameters.

Generally speaking, small-signal stabilization and large-signal stabilization are the main methods for the stability analysis of DC microgrids [\[28\]](#page-18-8). Based on linearization techniques at steady-state equilibrium operating points, small-signal stability guarantees DC bus voltage stability during small disturbances and is not suitable for large disturbances [\[29](#page-18-9)[,30\]](#page-18-10). Unfortunately, the DC microgrid is a typical nonlinear system, and steadystate equilibrium operating points are constantly changing; consequently, small-signal stability methods cannot ensure DC microgrid stability under large power variations of DGs and loads [\[31,](#page-18-11)[32\]](#page-18-12).

Large-signal stability analysis usually uses the Lyapunov function [\[33](#page-18-13)[,34\]](#page-18-14). However, constructing Lyapunov functions is very difficult. Furthermore, mixed potential theory is proposed to build Lyapunov-type functions and derive stability criteria [\[35\]](#page-18-15). Refs. [\[19](#page-17-13)[,36\]](#page-18-16) present a mixed potential function for cascaded BUCK/BOOST converters and analyze the control parameters' influences on system stability. Reference [\[37\]](#page-18-17) proposes a mixed potential function of DC/DC bidirectional converters and achieves proper controller limiting to improve large-signal stability. Refs. [\[38,](#page-18-18)[39\]](#page-18-19) study the large-signal stability of DC microgrids considering battery energy storage systems. Additionally, voltage-controlled converters and current-controlled converters are both taken into account in [\[39\]](#page-18-19). In [\[40](#page-18-20)[,41\]](#page-18-21), a large-signal stability criterion for DC microgrids is derived using a Lyapunov function, and the stable operation range and maximum load power of the system are analyzed. Reference [\[42\]](#page-18-22) proposes a large-signal stability criterion for DC microgrids with multiple converters and analyzes the relationship between stable operation range and load power. Reference [\[43\]](#page-18-23) improves the stability of DC microgrids considering fault and distributed energy power allocation. Unfortunately, it is not easy to obtain the large-signal models of complicated systems using mixed potential functions. Consequently, the large-signal stability analysis of DC microgrids integrating an HESS, CPLs and DGs is rarely reported, and simultaneously, the stability influences of the control parameters and the powers are not considered.

This paper attempts to fill the gap through the following research study, and the contributions are as follows:

(1) The topological structure of and control strategy for islanded DC microgrids containing CPLs, DGs and an HESS are proposed. A nonlinear model of the system is obtained. Based on the mixed potential function theory, a large-signal model and a large-signal stability criterion for DC microgrids are obtained;

- (2) The presented large-signal stability criterion reveals the relationships among the filtering parameters, DG power, CPL power, the proportional control parameter (k_{ip1}) of the inner current loop for the battery converter, the proportional control parameter of the inner current loop for the battery converter, the proportional control parameter (k_{vp2}) of the outer power loop for the SC converter and the stability of the islanded DC microgrids. Moreover, simultaneously, important large-signal stabilization design methods for the HESS converter's control parameters for DC microgrids are provided; methods for the HESS converter's control parameters for DC microgrids are ϵ) The presented large-signal stability criterion reveals the relationships among the relationship
- (3) Regulating the proportional control parameter (k_{ip1}) of the inner current loop for the battery converter and the proportional control parameter (k_{vp2}) of the outer power loop for the SC converter can optimize a control strategy for islanded DC microgrids and enhance system stability without extra equipment. and enhance system stability with $\frac{1}{2}$

2. Modeling of and Control Strategy for DC Microgrids

Typical islanded DC microgrids are constituted of DGs, CPLs and an HESS, as shown in Figure [1.](#page-2-0) Most HESSs are connected to the DC bus through bidirectional BUCK/BOOST converters. Closed-loop-controlled motors and other loads are considered CPLs. DGs are converters. Closed-loop-controlled motors and other loads are considered CPLs. DGs are equivalent to the current source. The simplified structure of an islanded DC microgrid with an HESS, CPLs and parallel converters is shown in Figure [2.](#page-2-1) with an HESS, CPLs and parallel converters is shown in Figure 2. in Figure 1. Most HESSs are connected to the DC bus through bidirectional BUCK/BOOST

Figure 1. Figure 1. Structure of typical islanded DC microgrid. Structure of typical islanded DC microgrid.

Figure 2. Simplified structure of islanded DC microgrid with HESS, CPLs and DGs. **Figure 2.** Simplified structure of islanded DC microgrid with HESS, CPLs and DGs.

2.1. Modeling $\it odding$

The bidirectional DC/DC converter of an HESS can be operated in buck mode and boost mode. When the HESS absorbs energy, the DC/DC converter works in buck mode, as boost mode. When the HESS absorbs energy, the DC/DC converter works in buck mode, shown in Figure [3.](#page-3-0) When the HESS outputs energy, the DC/DC converter works in boost mode, as shown [in](#page-3-1) Figure 4. T and T a The bidirectional DC/DC converter of an HESS can be operated in buck mode and boost mode. When the HESS absorbs energy, the DC/DC converter works in buck mode, as boost mode, as shown in Figure 4.

Figure 3. Principle diagram of bidirectional DC/DC converter working in buck mode.

Figure 4. Principle diagram of bidirectional DC/DC converter working in boost mode.

The bidirectional DC/DC converter of the battery is described as: The bidirectional DC/DC converter of the battery is described as:

$$
v_{dc} = \alpha v_{bat} - \alpha Ri_L - \alpha L \frac{di_L}{dt}
$$
 (1)

 (1) In (1), *vdc* is the DC bus voltage, *vbat* is the battery voltage, *i^L* is the inductor current, *R* is the battery equivalent resistance, and α is the battery charging and discharging factor.

Moreover, the bidirectional DC/DC converter of batteries adopts an inductor current
inner lags and a DC by explanation at the particular the latter and DC/DC convention are equivalent to a voltage source in series with the equivalent resistor and inductor, as shown in Figure 5. inner loop and a DC bus voltage outer loop; therefore, the battery and DC/DC converter shown in Figure [5.](#page-3-2)

Figure 5. Equivalent model of battery and bidirectional DC/DC converter. **Figure 5.** Equivalent model of battery and bidirectional DC/DC converter.

The bidirectional DC/DC converter of a SC utilizes a power outer loop and a current inner loop; consequently, the SC and the DC/DC converter are equivalent to a controlled power source whose power is P_{sc} , as shown in Figure 6. The bidirectional DC/DC converter of a SC utilizes a power outer loop and a current

Figure 6. Equivalent model of SC and bidirectional DC/DC converter. **Figure 6.** Equivalent model of SC and bidirectional DC/DC converter.

The power relationship of the SC and DC/DC converter is: The power relationship of the SC and DC/DC converter is: The power relationship of the SC and DC/DC converter is:

power source whose power is *Psc*, as shown in Figure 6.

$$
i_{sc}v_{dc} = i_{SC}v_{SC} = P_{sc}
$$
 (2)

 (2) and the realized (2) In (2), $i_{\rm sc}$ is the output current of the DC/DC converter, and $v_{\rm SC}$ and $i_{\rm SC}$ are the voltage
current of the SC, respectively. and current of the SC, respectively.

In a DC microgrid, the loads are connected in parallel to the DC bus through converters. Due to the closed-loop control, when the DC bus voltage increases, the input current of these loads decreases, and the power remains constant. These loads are regarded as CPLs, and the voltage–current characteristics of the CPLs are shown in (3). The equivalent model
of the CPLs is shown in Figure 7 of the CPLs is shown in Figure [7.](#page-4-1) P_{corr}

$$
i_{CPL} = \frac{P_{CPL}}{v_{CPL}}\tag{3}
$$

Figure 7. Equivalent model of CPLs. **Figure 7.** Equivalent model of CPLs.

In (3), *iCPL*, *PCPL* and *vCPL* are the input current, the power and the input voltage of the CPLs, respectively.

DGs usually adopt the constant current mode and are equivalent to a power source whose power is P_{DG} .

In (3), *i*CPL, *PCPL* and *iCPL* and *pc* are the input voltage of the power and the input voltage of the input volt *Consequently, the nonlinear model of a DC microgrid with an HESS is established as* shown in Figure [8.](#page-4-2)

Figure 8. Nonlinear model of a DC microgrid. **Figure 8.** Nonlinear model of a DC microgrid.

In Figure 8, *vbat* is the battery voltage, *ibat* is the battery current, *R*1 is the equivalent internal resistance of the battery, *L*¹ is the inductance of the bidirectional DC/DC converter, and DG and SC are equivalent to the power sources whose powers are P_{DG} and P_{sc} , respectively. The output currents of DGs and the SC are i_{DG} and i_{sc} , respectively. C_{dc} is the P_s , respectively. The output current solution of DGs and P_s , $\frac{1}{2}$ is the power of the CDIsc, P_s filter capacitor, and its voltage is v_{dc} , while P_{load} is the power of the CPLs. In Figure [8,](#page-4-2) *vbat* is the battery voltage, *ibat* is the battery current, *R*¹ is the equivalent

2.2. Control Strategy

2.2. Control Strategy

According to the power of DGs and CPLs, the reference power of the HESS is derived. The power of the HESS is divided into high-frequency power and low-frequency power
using a low pass filtor (LPE), as shown in Figure 9. The power relationships are: using a low-pass filter (LPF), as shown in Figure [9.](#page-5-0) The power relationships are:

$$
P_{DG} - P_{load} = P_{bat-ref} + P_{sc-ref} \tag{4}
$$

Figure 9. Control block diagram of low-pass filter (LPF). **Figure 9.** Control block diagram of low-pass filter (LPF).

In (4), $P_{bat-ref}$ and P_{sc-ref} are the reference powers for the battery and for the supercator respectively. pacitor, respectively.

The battery converter maintains constant DC bus voltage. Therefore, the battery converter adopts the outer voltage loop and the inner current loop to control the charging or discharging. The voltage outer loop calculates the reference value for the current inner loop control. The current inner loop makes the actual current (I_{bat}) of the battery unit track the current reference value (*I_{bat-ref}*) through the PI controller to eliminate the steady-state error of current tracking. Finally, the complementary control signals are output by the PWM generator.

On the other hand, the supercapacitor (SC) converter utilizes the outer power loop and inner current loop. The power outer loop calculates the reference value of the current inner control loop. The current inner loop makes the actual SC current (*I_{sc}*) track the current reference value (*I_{sc-ref}*) through the PI controller to eliminate the steady-state error of current tracking. Finally, the complementary control signals are output by the PWM generator.

The control strategy for HESS converters is shown in Figure 10.

Figure 10. Control block diagram of HESS converters. **Figure 10.** Control block diagram of HESS converters.

In Figure 10, V_{ref} is the reference DC bus voltage, $I_{bat-ref}$ is the reference current of In Figure 10, V_{ref} is the reference DC bus voltage, $I_{bat-ref}$ is the reference current of the battery converter, and V_{dc} and I_{bat} are the actual output voltage and current of the converter. *Pbat-ref* and *Psc-ref* are the reference power for the battery and for the battery converter. *Pbat-ref* and *Psc-ref* are the reference power for the battery and for the supercapacitor, respectively. The LPF adopts the first-order low-pass filter, and its transfer function is shown in (5):

$$
H(s) = \frac{1}{Ts + 1} \tag{5}
$$

According to (4) and (5), the reference power of the battery and the SC are calculated, respectively, and shown as:

$$
P_{bat-ref} = \frac{1}{Ts+1} (P_{DG} - P_{load}) = \frac{1}{Ts+1} P_{HESS}
$$
 (6)

$$
P_{sc-ref} = \frac{Ts}{Ts+1}(P_{DG} - P_{load}) = \frac{Ts}{Ts+1}P_{HESS}
$$
\n⁽⁷⁾

In (6) and (7), *PHESS* is the total reference power of the HESS. Based on (6) and (7), (8) and (9) are derived and shown as:

$$
T\frac{\mathrm{d}P_{bat-ref}(t)}{\mathrm{d}t} + P_{bat-ref}(t) = P_{HESS}(t)
$$
\n(8)

$$
T\frac{\mathrm{d}P_{bat-ref}(k+1) - \mathrm{d}P_{bat-ref}(k)}{T_s} + P_{bat-ref}(k+1) = P_{HESS}(k+1) \tag{9}
$$

In (8) and (9), *T* is the low-pass-filtering coefficient, and *T^s* is the sampling period. $P_{bat-ref}(k + 1)$ is the battery reference power for the next sampling period, and $P_{sc-ref}(k + 1)$ is the SC reference power for the next sampling period.

Based on (6) , (8) and (9) , $P_{bat-ref}$ $(k + 1)$ are obtained and shown as:

$$
P_{bat-ref}(k+1) = \frac{T}{T+T_s} P_{bat-ref}(k) + \frac{T_s}{T+T_s} P_{HESS}(k+1)
$$
\n(10)

Similarly, P_{sc-ref} ($k + 1$) are obtained on the basis of (7)–(9), and the derivation process is as follows:

$$
T\frac{\mathrm{d}P_{sc-ref}(t)}{\mathrm{d}t} + P_{sc-ref}(t) = T\frac{\mathrm{d}P_{HESS}(t)}{\mathrm{d}t}
$$
\n(11)

$$
T\frac{dP_{sc-ref}(k+1) - dP_{sc-ref}(k)}{T_s} + P_{sc-ref}(k+1) = T\frac{dP_{HESS}(k+1) - dP_{HESS}(k)}{T_s}
$$
(12)

$$
P_{sc-ref}(k+1) = \frac{T}{T+T_s}(P_{sc-ref}(k) + P_{HESS}(k+1) - P_{HESS}(k))
$$
\n(13)

In (13), when P_{HESS} ($k + 1$) – P_{HESS} (k) = ΔP_{HESS} , the following is obtained.

$$
P_{sc-ref}(k+1) = \frac{T}{T+T_S}(P_{sc-ref}(k) + \Delta P_{HESS})
$$
\n(14)

3. Large-Signal Stabilization Method

The mixed potential function is widely used in nonlinear systems containing inductance, capacitance, resistance and negative-impedance characteristic components. Compared with other stability analysis methods, the mixed potential function derives the quantitative stability criterion. Mixed potential function P is expressed as:

$$
P(i, v) = -A(i) + B(v) + (i, \gamma v - \alpha)
$$
\n(15)

where *A*(*i*) is the current potential function, *B*(*v*) is a voltage potential function and (*i*, γv -*α*) varies with the system topology.

The general mixed potential function model is established as follows:

- (1) The potential function of non-energy storage components in the system is obtained;
- (2) The energy absorbed by the capacitor element in the system is calculated;
- (3) The formulas obtained are written in standard form.

For the established mixed potential function, (16) is used to verify its accuracy:

$$
\begin{cases}\nL\frac{di_{\rho}}{dt} = \frac{\partial P(i,v)}{\partial i_{\rho}} \\
C\frac{dv_{\sigma}}{dt} = -\frac{\partial P(i,v)}{\partial v_{\sigma}}\n\end{cases}
$$
\n(16)

In (16), i_{ρ} and v_{σ} are the current and voltage variables of the mixed potential function. The third stability theory of mixed potential functions is often used to analyze the large-signal stability. The following are derived and shown as:

$$
A_{ii}(i) = \frac{\partial^2 A(i)}{\partial i^2} \tag{17}
$$

$$
B_{vv}(v) = \frac{\partial^2 B(v)}{\partial v^2}
$$
\n(18)

In (17), *Aii*(*i*) is the second-order partial derivative of the current potential function to the current variable, and in (18), $B_{vv}(v)$ is the second-order partial derivative of the voltage potential function to the voltage.

If the system satisfies:

$$
\mu_1 + \mu_2 \ge \delta, \delta > 0 \tag{19}
$$

In (19), μ_1 is the minimum eigenvalue of matrix $L^{-1/2}A_{ii}(i)L^{-1/2}$, and μ_2 is the minimum eigenvalue of matrix $C^{-1/2}B_{uu}(u)C^{-1/2}$.

Simultaneously, when $|i| + |u| \rightarrow \infty$, if the system satisfies:

$$
P^*(i,v) = \frac{\mu_1 - \mu_2}{2} P(i,v) + \frac{1}{2} (P_i, L^{-1}P_i) + \frac{1}{2} (P_v, C^{-1}P_v) \to \infty
$$
 (20)

There is a convergence region that makes the system trajectory asymptotically converge to the steady-state equilibrium point.

Large-Signal Stabilization Method for DC Microgrids with HESS

On the basis of the simplified model of the HESS DC microgrid in Figure [8,](#page-4-2) the large-signal model of the system is established using the mixed potential function.

The current potential function of the battery equivalent model and bidirectional DC/DC converter is:

$$
P_i = \alpha v_{bat} i_{bat} - \frac{1}{2} \alpha i_{bat}^2 R_1
$$
\n(21)

The voltage potential functions for the equivalent models of the DGs and the SC are:

$$
P_v = P_{DG} - \int\limits_{0}^{v_{dc}} \frac{P_{DG}}{v} dv + P_{sc} - \int\limits_{0}^{v_{dc}} \frac{P_{sc}}{v} dv \qquad (22)
$$

The voltage potential function for the CPLs is:

$$
P_v = -P_{load} + \int\limits_{0}^{v_{dc}} \frac{P_{load}}{v} dv
$$
\n(23)

The energy stored on capacitor *Cdc* is:

$$
P_E = v_{dc} \left(\frac{P_{load}}{v_{dc}} - \frac{P_{DG}}{v_{dc}} - \frac{P_{sc}}{v_{dc}} - i_{bat} \right)
$$
 (24)

Based on (21) to (24), the mixed potential function of a DC microgrid is expressed as:

$$
P(i,v) = \alpha v_{bat} i_{bat} - \frac{1}{2} \alpha i_{bat}^2 R_1 - \int\limits_{0}^{v_{dc}} \frac{P_{DG}}{v} dv - \int\limits_{0}^{v_{dc}} \frac{P_{sc}}{v} dv + \int\limits_{0}^{v_{dc}} \frac{P_{load}}{v} dv - v_{dc} i_{bat} \tag{25}
$$

The current potential function and voltage potential function in (25) are shown as:

$$
A(i) = \begin{bmatrix} -\alpha v_{bat}i_{bat} + \frac{1}{2}\alpha i_{bat}^2 R_1 & 0\\ 0 & 0 \end{bmatrix}
$$
 (26)

$$
B(v) = \begin{bmatrix} -\int_{0}^{v_{dc}} \frac{P_{DG}}{v} dv - \int_{0}^{v_{dc}} \frac{P_{sc}}{v} dv + \int_{0}^{v_{dc}} \frac{P_{load}}{v} dv & 0\\ 0 & 0 & 0 \end{bmatrix}
$$
(27)

Equation (16) is applied to verify the verification of the mixed potential function model in (25):

$$
\begin{cases}\n\frac{\partial P(i,v)}{\partial i_{bat}} = \alpha v_{bat} - i_{bat} R_1 - v_{dc} = \alpha L \frac{di_{bat}}{dt} \\
\frac{\partial P(i,v)}{\partial v_{dc}} = -(\frac{P_{DG}}{v_{dc}} + i_{bat} + \frac{P_{sc}}{v_{bat}} - \frac{P_{load}}{v_{bat}}) = -C \frac{dv_{dc}}{dt}\n\end{cases}
$$
\n(28)

Obviously, (28) and (16) are consistent. Thus, the mixed potential function in (25), the voltage potential function and the current potential function are correct.

Based on (25), the large-signal stability criterion for DC microgrids is derived. Firstly, the quadratic partial derivatives of the current potential function and the voltage potential function are derived separately and shown as:

$$
A_{ii} = \begin{bmatrix} -\frac{\partial \alpha v_{bat}}{\partial i_{bat}} + \alpha R_1 & 0\\ 0 & 0 \end{bmatrix}
$$
 (29)

$$
B_{vv} = \begin{bmatrix} \frac{P_{DG}}{v_{dc}^2} + \frac{\partial i_{sc}}{\partial v_{dc}} - \frac{P_{load}}{v_{dc}^2} & 0\\ 0 & 0 \end{bmatrix}
$$
(30)

According to the double closed-loop control of the battery converter is obtained and shown as:

$$
\alpha v_{bat} = v_{dcref} + k_{ip1}(i_{baterf} - i_{bat}) + k_{ii1} \int (i_{baterf} - i_{bat}) dt
$$
\n(31)

$$
\frac{\partial \alpha v_{bat}}{\partial i_{bat}} = -k_{ip1} \tag{32}
$$

In (31) and (32), *kip*¹ and *kii*¹ are the proportional and integral control parameters of the inner current loop for the battery converter, *vdcref* is the rated DC bus voltage of DC microgrids, and *ibatref* is the rated current of the battery converter.

According to the power conservation principle and the control strategy for the SC converter, this is derived and shown as

$$
i_{L1} = i_{SCref} = k_{vp2}(P_{scref} - i_{sc}v_{dc}) + k_{vi2} \int (P_{scref} - i_{sc}v_{dc})dt
$$
\n(33)

$$
i_{sc}v_{dc} = i_{SC}v_{SC}
$$
 (34)

$$
\frac{\partial i_{sc}}{\partial v_{dc}} = \frac{\partial \frac{i_{SC}v_{SC}}{v_{dc}}}{\partial v_{dc}} = \frac{-k_{vp2}i_{sc}v_{SC}v_{dc} - i_{SC}v_{SC}}{v_{dc}^2}
$$
(35)

In (33) and (34), *kvp*² and *kvi*² are the proportional and integral control parameters of the outer power loop for the SC converter, *Pscref* is the reference power of the SC, and *vSC* and *iSC* are the actual voltage and current of the SC.

According to the third stability theorem, μ_1 is the minimum eigenvalue of $L^{-1/2}A_{ii}(i)$ *L*^{−1/2}, and $μ$ ₂ is the minimum eigenvalue of $C^{-1/2}B_{vv}(v)$ $C^{-1/2}$; $μ$ ₁ and $μ$ ₂ are obtained and shown as:

$$
\mu_1 = \frac{k_{ip1} + \alpha R_1}{\alpha L_1} \tag{36}
$$

$$
\mu_2 = \frac{P_{DG} - k_{vp2} i_{sc} v_{SC} v_{dc} - i_{SC} v_{SC} - P_{load}}{C_{dc} v_{dc}^2}
$$
(37)

Equation (14) is introduced into Equation (37) to obtain:

$$
\mu_2 = \frac{P_{DG} - \frac{T}{T + T_S} (P_{sc-ref}(k) + \Delta P_{HESS}) (k_{vp2} v_{SC} + 1) - P_{load}}{C_{dc} v_{dc}^2}
$$
(38)

According to the stability theorem in (19), the large-signal stability criterion for a DC microgrid with an HESS and CPLs is shown as:

$$
\frac{k_{ip1} + \alpha R_1}{\alpha L_1} + \frac{P_{DG} - \frac{T}{T + T_S} (P_{sc-ref}(k) + \Delta P_{HESS}) (k_{vp2} v_{SC} + 1) - P_{load}}{C_{dc} v_{dc}^2} > 0
$$
 (39)

To guarantee the large-signal stability of a DC microgrid, the criterion in (39) provides quantitative constraints on the CPL power, the filter inductor and capacitor, the DC bus voltage, the proportional control parameter (*kip*1) of the inner current loop for the battery converter and the proportional control parameter (*kvp*2) of the outer power loop for the SC converter.

In islanded DC microgrids, when the CPL power increases, the system stability significantly deteriorates. Based on (39), regulating *kip*¹ and *kvp*² of HESS converters during CPL power variations can easily achieve the large-signal stabilization of an islanded DC microgrid without extra equipment. The powers of the CPLs significantly vary during the working period and could also be regarded as dynamic loads at multiple time scales. The proposed large-signal stability criterion can analyze the transient state of dynamic loads.

Furthermore, when planning an islanded DC microgrid, the proposed large-signal stabilization method can improve the system large-signal stability; simultaneously, when the CPL power of an existing islanded DC microgrid significantly increases, the stabilization method can also guarantee large-signal stability.

The proposed procedure of the large-signal stability method for a DC microgrid with an HESS is expressed in Figure [11.](#page-9-0) Firstly, the control strategy for the HESS is proposed. The battery and the SC are controlled via PI double closed loops. The equivalent model of a DC microgrid is obtained according to the operating characteristics and control methods; then, the nonlinear model of a DC microgrid system is obtained based on mixed potential theory. The correctness of the mixed potential model is verified. When the mixed potential model is correct, the third stability theorem of the mixed potential theory is used to derive the large-signal stability criterion for DC microgrids. The control strategy for the HESS is considered in the criterion, and the control parameters are introduced into the large-signal stability criterion, which makes the criterion more accurate and comprehensive.

Figure 11. Procedure of large-signal stability method. Figure 11. Procedure of large-signal stability method.
 Figure 11. Procedure of large-signal stability method.

4. Large-Signal Stabilizations Method

The correctness of the large-signal stabilization method derived in (39) needs to be verified, and a typical islanded DC microgrid on the basis of Figure [2](#page-2-1) is constructed using Matlab software. The system parameters are shown in Table [1.](#page-10-0) The CPLs are implemented via a closed-loop controlled DC/DC converter and a resistor. The islanded DC microgrid only operates in off-grid mode. When the powers between distributed generation and load are unbalanced, the hybrid energy storage system balances the powers. The platforms built in the experiments and simulations of this paper are both in island mode and are not connected to the grid.

Table 1. Simulation parameters of an islanded DC microgrid.

1. In the simulation, the CPL power step causes a disturbance to the system. Two different groups of control parameters are designed and shown in Table [2.](#page-10-1) The control parameters of Group A satisfy (39), while the control parameters of Group B do not satisfy (39). The power variation of the CPLs is 2 kW to 12 kW. The load power variations are the same, while the control parameters of the hybrid energy storage system are different. Consequently, these groups are utilized to verify the stability influence of the control parameters of the hybrid energy storage system.

Table 2. Simulation parameters of A and B.

When Group A is utilized, the waveforms of the CPL power step, DC bus voltage, battery power and SC power are shown in Figure [12.](#page-11-0) Obviously, when the initial CPL power is 2 kW, the DC bus voltage is 400 V, and the battery charging power is 2 kW. The DC microgrid is stable. As the CPL power increases from 2 kW to 12 kW, the DC bus voltage recovers to 400 V after a short fluctuation, and the battery changes from the charging state to the discharging state. At the same time, the SC quickly releases high-frequency power. Figure [12](#page-11-0) indicates that the control parameters of Group A can guarantee the large-signal stability of a DC microgrid.

When Group B is utilized, the waveforms of the CPL power step, DC bus voltage, battery power and SC power are shown in Figure [13.](#page-11-1) Both CPL power and DC bus voltage strongly oscillate and cannot return to the steady state. The powers of the battery and SC are in a state of divergence. The battery cannot maintain a stable DC bus voltage. The DC microgrid is not stable during large disturbances. Figure [13](#page-11-1) indicates that the control parameters of Group B cannot guarantee the large-signal stability of a DC microgrid.

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The large-signal stability of a DC microgrid. Stability of a DC microgrid. Stability of a DC microgrid. Stabil

Figure 12. Simulation results when control parameters of Group A are used. (a) CPL power, (b) DC bus voltage, (**c**) battery power and (**d**) SC power. bus voltage, (**c**) battery power and (**d**) SC power.

Figure 13. Simulation results when control parameters of Group B are used. (a) CPL power, (b) DC bus voltage, (**c**) battery power and (**d**) SC power. bus voltage, (**c**) battery power and (**d**) SC power.

Figures [12](#page-11-0) and [13](#page-11-1) verify the validity of the derived large-signal stability criterion. The system is stable when the system control parameters satisfy the proposed criterion, otherwise the system is unstable. The results also illustrate that the large-signal stability of an islanded DC microgrid can be improved by regulating the proportional control parameters of the battery converter and SC converter.

2. Based on (39), the large-signal stability of an islanded DC microgrid also closely relates to CPL power. When the control parameters of the HESS converters are the same, two large power variations of the CPLs are introduced, as shown in Table [3.](#page-12-0) The control parameters of the energy storage system are the same, but the load power variations are different. The stability influence of the load power variations is verified, and the allowable load power variations are determined.

Table 3. Simulation parameters of Groups C and D.

When Group C is utilized, the waveforms of the CPL power step, DC bus voltage, battery power and SC power are shown in Figure [14.](#page-12-1) At 2 s, the CPL power increases from 2 kW to 8 kW; the DC bus voltage recovers to 400 V after a short decrease; and the battery changes from the charging state to the discharging state. Simultaneously, the SC quickly changes from the charging state to the discharging state. Simultaneously, the SC quickly releases high-frequency power. Figure [14](#page-12-1) indicates that the large-signal stability of a DC releases high-frequency power. Figure 14 indicates that the large-signal stability of a DC microgrid can be guaranteed during the large disturbance of Group C. microgrid can be guaranteed during the large disturbance of Group C.

Figure 14. Simulation results when control parameters of Group C are used. (a) CPL power, (b) DC bus voltage, (**c**) battery power and (**d**) SC power. bus voltage, (**c**) battery power and (**d**) SC power.

When Group D is utilized, the waveforms of the CPL power step, DC bus voltage, When Group D is utilized, the waveforms of the CPL power step, DC bus voltage, battery power and SC power are shown in Fig[ure](#page-13-0) 15. After the large disturbance, both battery power and SC power are shown in Figure 15. After the large disturbance, both CPL CPL power and DC bus voltage have serious oscillations and cannot return to the steady power and DC bus voltage have serious oscillations and cannot return to the steady state. The powers of the battery and SC are unstable. The battery cannot maintain a stable DC

bus voltage. The DC microgrids are not stable during large disturbances. It can be seen from Figure 15 that the large-signal stability of a DC microgrid cannot be guaranteed under the large disturbance of Group D.

Figure 15. Simulation results when control parameters of Group D are used. (a) CPL power, (b) DC bus voltage, (**c**) battery power and (**d**) SC power. bus voltage, (**c**) battery power and (**d**) SC power.

Figures 14 and 15 also verify the correctness of the derived large-signal stability Figures 14 and 15 also verify the correctness of the derived large-signal stability criterion and show that the large-signal stability of an islanded DC microgrid closely relates to large disturbances.

The simulation results indicate that the large-signal stabilization method for islanded The simulation results indicate that the large-signal stabilization method for islanded DC microgrids in (39) is correct and also show that regulating the proportional control DC microgrids in (39) is correct and also show that regulating the proportional control parameters of the SC converter and the battery converter can significantly improve the parameters of the SC converter and the battery converter can significantly improve the large-signal stability of islanded DC microgrids. large-signal stability of islanded DC microgrids.

5. Experimental Results 5. Experimental Results

The effectiveness of the proposed large-signal stability method for islanded DC micro-grids is verified. This paper builds the experimental platform based on Figure [2,](#page-2-1) as shown shown in Figure 16. The experimental platform includes a DC power supply, the battery in Figure [16.](#page-14-0) The experimental platform includes a DC power supply, the battery and the bidirectional DC/DC converter, the SC and the bidirectional DC/DC converter, and the bidirectional DC/DC converter, and the converter. CPLs. The parasitic inductance of the resistor used for the load step is also measured, as \mathbb{R}^1 shown in Table [4.](#page-14-1) The experimental parameters are shown in Table [5.](#page-14-2) The supercapacitor is
 supercapacitor is an electric double-layer capacitor, which is composed of two supercupacitors connected in series. The capacity of each supercapacitor is 166 F, and the rated voltage is 24 V. an electric double-layer capacitor, which is composed of two supercapacitors connected in

Power device Load **Sampling devices**

Figure 16. DC microgrid experimental platform. **Figure 16.** DC microgrid experimental platform.

Table 5. Parameters of experimental platform. **Table 4.** Parasitic inductance value of resistive load.

20 0.28

Table 5. Parameters of experimental platform.

Large disturbances are introduced via power variations of the CPLs. Two groups with different control parameters are designed. The parameters of Group E satisfy (39), while the parameters of Group F do not satisfy (39), and shown in Table [6.](#page-14-3) The power variations of the CPLs are from 23 W to 80 W. In Group E and Group F, the load power variations are the same, and the control parameters of the energy storage system are different, to verify the stability of the influence of the control parameters of the hybrid energy storage system. gradually increases, the current of the SC tends to be zero. Obviously, the DC microgrid to be zero. Obviously

Table 6. Experimental parameters of Groups E and F.

When Group E is adopted, the waveforms of the DC bus voltage, CPL current, and battery and SC currents are shown in Figure [17.](#page-15-0) The CPL power increases from 23 W to 80 W, and the DC bus voltage slightly decreases. Simultaneously, the SC responds rapidly, and the output current of the SC instantaneously increases. While the battery current gradually increases, the current of the SC tends to be zero. Obviously, the DC microgrid maintains stable operation during large disturbances.

Figure 17. Control parameters of Group E: DC bus voltage, CPL current, battery current and SC current of a DC microgrid.

When Group F is utilized, the DC bus voltage, CPL current, and battery and SC currents are shown in Figure [18.](#page-15-1) After the same CPL power variation, the DC bus voltage slightly decreases; the battery current violently fluctuates; and the SC current also fluctuates near zero. Figure [18](#page-15-1) shows that the DC microgrid is unstable during large disturbances. disturbances. When Group F is utilized, the DC bus voltage, CPL current, and battery and SC when Group F is utilized, the DC bus voltage, CPL current, and battery and SC

Figure 18. Control parameters of Group F: DC bus voltage, CPL current, battery current and SC **Figure 18.** Control parameters of Group F: DC bus voltage, CPL current, battery current and SC current of a DC microgrid. current of a DC microgrid.

The comparison between Figures 17 and 18 illustrates that when the control The comparison between Figures [17](#page-15-0) and [18](#page-15-1) illustrates that when the control parameters satisfy the derived large-signal stabilization method in (39), the DC microgrid can remain stable under large disturbances. Conversely, when the control parameters do not satisfy (39), the DC microgrid cannot achieve large-signal stability. The experimental results verify the effectiveness of the proposed criterion. The results also illustrate that the proposed large-signal stability method for islanded DC microgrids can be improved by regulating the proportional control parameters of the battery converter and SC converter.

Similarly, based on (39), the large-signal stability of an islanded DC microgrid closely Similarly, based on (39), the large-signal stability of an islanded DC microgrid closely relates to CPL power. In Group G and Group H, the control parameters of the hybrid energy storage system are the same, but the load power steps are different, as shown in energy storage system are the same, but the load power steps are different, as shown in Tabl[e 7](#page-15-2). The disturbances of Group G are from 23 W to 80 W, satisfying (39), while the Table 7. The disturbances of Group G are from 23 W to 80 W, satisfying (39), while the disturbances of Group H are from 23 W to 115 W, not satisfying (39). disturbances of Group H are from 23 W to 115 W, not satisfying (39).

Table 7. Experimental parameters of G and H.

When Group G is used, the DC bus voltage, CPL current, battery current and SC 23 W to 80 W, the DC bus voltage slightly decreases. At the same time, the SC rapidly current of the DC microgrid are shown in Figure [19.](#page-16-0) When the CPL power increases from discharges, and the battery current slowly increases. Obviously, after the power step of the \overline{CD} CPLs, the DC microgrid maintains a new stable state.

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Figure 19. Control parameters of Group G: DC bus voltage, CPL current, battery current and SC current of a DC microgrid. current of a DC microgrid. current of a DC microgrid.

When Group H is applied, the DC bus voltage, CPL current, battery current and SC current of the DC microgrid are shown in Figure [20.](#page-16-1) Unfortunately, after the CPL power increases, the DC bus voltage significantly drops; the battery current seriously fluctuates from 0.4 A to 6 A, and the SC current also fluctuates in a small range. Consequently, the DC microgrid is unstable during large disturbances. DC microgrid is unstable during large disturbances. DC microgrid is unstable during large disturbances.

Figure 20. Control parameters of Group H: DC bus voltage, CPL current, battery current and SC current of a DC microgrid. current of a DC microgrid.

 $\mathcal{F}_{\mathcal{A}}$ and 20 also verify the derived large-signal stability the derived large-signal stability of Figures [19](#page-16-0) and [20](#page-16-1) also verify the correctness of the derived large-signal stability criterion. When the load power variations satisfy the criterion, the system is stable, otherwise the system is unstable. This shows that the criterion proposed in this paper can constrain the variation of load power.

In the experimental results shown in Figures [17–](#page-15-0)[20,](#page-16-1) the green waveform is the current waveform of the supercapacitor. It can be concluded that the supercapacitor quickly charges and discharges to absorb or release high-frequency-power fluctuations.

The experimental results indicate that under the same disturbance, regulating the proportional control parameters of the battery converter and SC converter can significantly improve the large-signal stability of an islanded DC microgrid. The simulation results coincide with the experimental results, and all results indicate that the large-signal stabilization method for islanded DC microgrids in (39) is correct.

6. Conclusions 6. Conclusions 6. Conclusions

For an islanded DC microgrid constituted of an HESS, CPLs and DGs, this paper presents a large-signal stabilization method. Firstly, the topological structure of and control strategy for a DC microgrid is proposed. According to the dynamic characteristics of the DGs, the HESS and the CPLs, the nonlinear model of the system is obtained. Then, the large-signal model and large-signal stability criterion for a DC microgrid are obtained using mixed potential functions and the stability theorem. A large-signal stabilization method is

method is proposed, and the CPL power, the filter inductor and capacitor, the DC bus

proposed, and the CPL power, the filter inductor and capacitor, the DC bus voltage, the proportional control parameter (k_{ip1}) of the inner current loop for the battery converter and the proportional control parameter (k_{vp2}) of the outer power loop for the SC converter are all considered. Furthermore, the regulating control parameters of HESS converters can easily achieve the large-signal stabilization of an islanded DC microgrid without extra equipment. The experimental and simulation results verify the correctness of the proposed large-signal stabilization method for islanded DC microgrids.

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