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An Improved Proposed Single Phase Transformerless Inverter with Leakage Current Elimination and Reactive Power Capability for PV Systems Application [†]

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Abstract: Single-phase transformerless inverters are broadly studied in literature for residential-scale PV applications due to their great advantages in reducing system weight, cost and elevating system efficiency. The design of transformerless inverters is based on the galvanic isolation method to eliminate the generation of leakage current. Unfortunately, the use of the galvanic isolation method alone cannot achieve constant common mode voltage (CMV). Therefore, a complete elimination of leakage current cannot be achieved. In addition, modulation techniques of single-phase transformerless inverters are designed for the application of the unity power factor. Indeed, next-generation PV systems are required to support reactive power to enable connectivity to the utility grid. In this paper, a proposed single-phase transformerless inverter is modified with the clamping method to achieve constant CMV during all inverter operating modes. Furthermore, the modulation technique is modified by creating a new current path in the negative power region. As a result, a bidirectional current path is created in the negative power region to achieve reactive power generation. The simulation results show that the CMV is completely clamped at half the DC link voltage and the leakage current is almost completely eliminated. Furthermore, a reactive power generation is achieved with the modified modulation techniques. Additionally, the total harmonic distortion (THD) of the grid current with the conventional and a modified modulation technique is analyzed. The efficiency of the system is enhanced by using wide-bandgap (WBG) switching devices such as SiC MOSFET. It is observed that the efficiency of the system decreased with reactive power generation due to the bidirectional current path, which leads to increasing conduction losses.

Keywords: transformerless inverter; photovoltaic (PV) power system; reactive power; leakage current; wide-bandgap (WBG); silicon carbide (SiC)

1. Introduction

Several transformerless inverter topologies have been presented and published [1–7]. One of the drawbacks of PV transformerless inverters is the generating of leakage current due to the absence of a transformer. High generation of leakage current increases system losses, total harmonic distortion (THD), electromagnetic interference (EMI) and can cause personal safety issues [8–11]. The literature cites various modulation techniques that have been used to eliminate or minimize leakage current.

Many transformerless inverter topologies with unipolar modulation technique have been introduced to increase system efficiency and reduce the leakage current by disconnecting the AC and DC sides during the freewheeling modes; this is known as galvanic isolation. Many topologies have been derived and developed based on this method, including a highly efficient and reliable inverter concept (HERIC), the H5 inverter and the H6 topology [12,13]. However, complete elimination of leakage current cannot be achieved with the galvanic isolation method alone because common mode voltage (CMV) during freewheeling periods cannot be identified by the switching state, which means that it is not constant. Therefore, modulation strategies and converter structures must be modified so that CMV becomes constant during all inverter operating modes.

Most of the modulation techniques are designed for the application of a unity power factor. Indeed, next-generation PV inverters are required to support reactive power to allow for the high penetration of PV inverters to be connected to the utility grid [14–19]. Several international standards have been reviewed to achieve reactive power support. As reported by VDE-AR-N4105, PV grid connected inverters must have the ability to generate reactive power.

Considering all this, a traditional bipolar modulation technique is proposed as a possible candidate to be used in next-generation PV inverters, because it has the ability to generate reactive power and eliminate leakage current. Nevertheless, using bipolar modulation techniques to generate reactive power will increase switching losses and degrade system efficiency as it is a two-level modulation.

The Si MOSFET-based transformerless inverter is highly efficient in generating reactive power, but the inverter design must be optimized to address the fact that the Si MOSFET's body diode has poor reverse recovery performance, which may lead to device failure [20,21]. These inverters are based on the idea of avoiding the conduction of an anti-parallel diode, which will result in a more complicated and complex inverter that in turn increases costs. On the other hand, the SiC MOSFET's body diode has superior reverse recovery performance because it has a shorter minority carrier lifetime [22–30]. Therefore, reactive power generation with SiC MOSFET can be achieved without any alteration to the converter structure or adding any additional freewheeling diodes (FWD).

In this paper, WBG power devices such as SiC MOSFET are used to achieve reactive power generation without altering or modifying the structure of the circuit. Furthermore, the total losses (switching and conduction) are significantly reduced with SiC MOSFET due to its superior characteristics. additionally, high efficiency and low THD is achieved when operating the system at high switching frequency of 100 kHz with SiC MOSFET.

2. Proposed Topology with Leakage Current Elimination and Reactive Power Generation

The proposed H6 topology is derived from the conventional H6 topology where the sixth switch is repositioned and connected to the A terminal [31,32]. The proposed topology and its switching pattern are presented in Figures 1 and 2. The proposed topology has low conduction loss compared to traditional topologies, such as H5 and H6, because the number of conducting switches is reduced from six switches to five switches.

On the other hand, only using galvanic isolation will not maintain the desired constant CMV during the freewheeling period. Since the inverter output terminals A and B are floating during the freewheeling periods with respect to the DC side, this means that CMV cannot be determined from the switching states. Accordingly, the generation of leakage current cannot be fully controlled with the galvanic isolation method. Therefore, the proposed topology is improved by incorporating the clamped method technique to eliminate the leakage current and achieve constant CMV during the freewheeling period, as shown in Figure 3. The DC link capacitor is divided into two series capacitors to achieve the voltage divider. In the clamping method, an extra active switch is added to the proposed topology. This switch is known as a clamping switch and is placed between the midpoint of the freewheeling switches and the midpoint of the DC link capacitors.



Figure 1. Circuit diagram of the proposed transformerless inverter topology.



Figure 2. Modulation strategy of the proposed transformerless inverter.



Figure 3. Modified proposed transformerless inverter.

The generation of reactive power cannot be accomplished in single-phase transformerless inverter topologies because the existing modulation techniques are not adopted for a freewheeling path in the negative power region. To use unipolar PWM in the negative power region, the line to line voltage (VAB) must have three values, +VDC, –VDC and zero. It is observed that zero voltage states cannot be achieved in the negative power regions because there is no current path established, as illustrated in Figure 4. The modulation method is modified to achieve leakage current elimination and reactive power generation, as shown in Figure 5.



Figure 4. Operation modes of the negative power region during freewheeling periods: (**a**) V_g is positive and I_g is negative; (**b**) V_g is negative and I_g is positive.



Figure 5. Modified modulation strategy for leakage current elimination and reactive power generation.

3. Modulation Technique for Reactive Power Generation

The proposed PWM technique is divided into four regions and eight modes, as shown in Figure 5. In Regions II and IV, the power is positive where I_g and V_g have the same polarity. The power is negative in Regions I and III where I_g and V_g have an opposite sign. The operation modes are described as follows.

Mode-I is the active mode in the positive power region (both I_g and V_g are positive). In this mode, S1, S6 and S4 are turned ON, and the other switches are turned OFF, as shown in Figure 6a. The inductor current is flowing through S4 and S6. Even though S3 is turned ON, there is no current flowing through it, which means that the S1 switch has no conduction loss in this mode. The common mode voltage in Mode-I is calculated as follows:

$$v_{AN} = U_{PV} \tag{1}$$

$$v_{BN} = 0 \tag{2}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{3}$$

Mode-II is the freewheeling mode in the positive power region (both I_g and V_g are positive). In this mode, S1, S3 and S7 are turned ON and the other switches are turned OFF, as shown in Figure 6b. The inductor current is flowing through S1 and the antiparallel diode of S3. The common mode voltage in Mode-II is given as:

$$v_{AN} = v_{BN} = 0.5 U_{PV} \tag{4}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{5}$$



Figure 6. Operation modes of the proposed topology: (a) I_g and V_g are positive; (b) the zero state during this interval.

Mode-III is the active mode in the positive power region (both I_g and V_g are negative). In this mode, S5, S3 and S2 are turned ON and the other switches are turned OFF, as shown in Figure 7a.

The inductor current is flowing through S2, S3 and S5. The common mode voltage in Mode-III is expressed as:

$$v_{AN} = 0 \tag{6}$$

$$v_{BN} = U_{PV} \tag{7}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{8}$$

Mode-IV is the freewheeling mode in the positive power region (both I_g and V_g are negative). In this mode, S1, S3 and S7 are turned ON, and the other switches are turned OFF, as shown in Figure 7b. The inductor current is flowing through S3 and the antiparallel diode of S1. The common mode voltage in Mode-IV is calculated as follows:

$$v_{AN} = v_{BN} = 0.5 U_{PV} \tag{9}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{10}$$



Figure 7. Operation modes of the proposed topology: (**a**) I_g and V_g are negative; (**b**) the zero state during this interval.

Mode-V is the active mode in the negative power region (I_g is positive and V_g is negative). In this mode S4 and S6 are OFF, while S2, S5 and S3 are ON; thus, inductor current freewheels through the anti-parallel diodes of S2, S5 and S3, as given in Figure 8a where $V_{AN} = 0$ and $V_{BN} = V_{DC}$; thus, V_{CM} and V_{DM} are determined as:

$$v_{DM} = v_{AB} = -U_{PV} \tag{11}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{12}$$

Mode-VI is the freewheeling mode in the negative power region (I_g is positive and V_g negative). In this mode, S1 commutates complementarily to S2 and S5 at the switching frequency instead of being OFF for the whole negative period as in the conventional unipolar PWM. In this arrangement, a new current path is created to generate zero-voltage by turning OFF S2, S4, S5 and S6 and turning ON S1, S3 and S7. Thus, the inductor current passes over S1 and the antiparallel diode of S3 as depicted in Figure 8b, where $V_{AN} = V_{DC}/2$ and $V_{BN} = V_{DC}/2$; thus, V_{CM} and V_{DM} are determined as:

$$v_{DM} = v_{AB} = 0 \tag{13}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{14}$$



Figure 8. Operation modes of the proposed topology: (**a**) *Vg* is positive and *Ig* is negative; (**b**) the zero state during this interval.

Mode-VII is the active mode in the negative power region (*Ig* is negative and *Vg* is positive). In this mode, S2, S5 and S3 are OFF, while S4, S6 and S1 are ON; thus, inductor current freewheels through the anti-parallel diodes of S4 and S6, as shown in Figure 9a, where $V_{AN} = V_{DC}$ and $V_{BN} = 0$; thus, V_{CM} and V_{DM} are determined as:

$$v_{DM} = v_{AB} = U_{PV} \tag{15}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{16}$$

Mode VIII is the freewheeling mode in the negative power region (*Ig* is negative and *Vg* is positive). In this mode, S3 commutates complementarily to S4 and S6 at the switching frequency instead of being OFF for the whole positive period, as in the conventional unipolar PWM. In this arrangement, a new current path is created to generate zero-voltage by turning OFF S2, S4, S5 and S6 and turning ON S1, S3 and S7. Thus, the inductor current passes over S3 and the antiparallel diode of S1 as depicted in Figure 9b, where $V_{AN} = V_{DC}/2$ and $V_{BN} = V_{DC}/2$; thus, V_{CM} and V_{DM} are determined as:

$$v_{DM} = v_{AB} = 0 \tag{17}$$

$$v_{CM} = \frac{v_{AN} + v_{BN}}{2} = 0.5 U_{PV} \tag{18}$$



Figure 9. Operation modes of the proposed topology: (a) V_g is negative and I_g is positive; (b) the zero state during this interval.

By implementing the proposed modulation, a new current path is created in Regions I and III to achieve a zero-voltage state. Therefore, the three output voltages are generated $+V_{DC}$, zero and $-V_{DC}$, which satisfy the principle operation of the unipolar PWM technique. The main concept of the proposed modulation technique is to create a bidirectional current path of S1 and S3 during the negative power region. As a result, reactive power generation can be achieved with the proposed modulation technique. Moreover, the proposed modulation technique maintains a constant common mode voltage at $V_{DC}/2$ over all the operation modes, which will result in eliminating leakage current. Consequently, the proposed H6 topology with the modified unipolar PWM technique is suitable for the next generation of PV transformerless inverter applications.

4. Simulation Results

PSIM and MATLAB/SIMULINK simulation software programs were used to design the proposed topology to evaluate its performance. The parameters of the system design are shown in Table 1. The simulation included two switching frequencies: 16 kHz and 100 kHz.

Parameter	Value
Input Voltage	800 V
Grid Voltage	120 V
Grid Frequency	60 Hz
Switching Frequency	16 kHz and 100 kHz
DC Bus Capacitor (C_{DC})	970 μF
Stray Parasitic Capacitance	300 nF
Output Power	3 kW

Table 1. System design parameters.

The CM characteristics of the proposed topology, with only galvanic isolation, are given in Figure 10.



Figure 10. Common mode (CM) characteristics of the proposed topology.

It is observed that the voltage waveforms of V_{AN} and V_{BN} are oscillating. Furthermore, the common mode voltage V_{CM} is oscillating and not constant at 400 V. Therefore, there is a flow of leakage current that cannot be eliminated. To resolve this problem, the proposed topology is modified

with a clamping method and the CM characteristics, such as V_{AN} , V_{BN} , V_{CM} and leakage, that are shown in Figure 11. It is also noted that the voltage waveforms of V_{AN} and V_{BN} are smooth and complementary to each other. As a result, the V_{CM} is totally clamped to 400 V over the whole period, which will lead to a total elimination of leakage current.



Figure 11. CM characteristics of the modified topology.

The simulated waveform of the proposed H6 inverter at unity power factor with the traditional and proposed unipolar PWM is presented in Figure 12. Both modulation techniques are expected to perform properly for purely active power of a unity power factor. On the other hand, with the non-unity power factor (reactive power generation), the traditional unipolar PWM does not work correctly.

Figure 13 shows a current distortion in the negative power region because there is no current path to realize a zero-voltage through the negative power region. However, the issue of current distortion with conventional unipolar PWM can be resolved with the proposed unipolar PWM. The proposed modulation technique works optimally with a lagging or leading power factor where the simulated waveform has free current distortion as depicted in Figure 14. Hence, the proposed modulation technique supports the proposed H6 inverter for reactive power generation.



Figure 12. V_g and I_g for the proposed H6 inverter with the unity power factor: (**a**) traditional unipolar PWM is positive; (**b**) proposed unipolar PWM.



Figure 13. Grid current for the proposed inverter with conventional unipolar PWM.



Figure 14. Grid current for the proposed inverter with modified conventional unipolar PWM.

It is noted that the proposed topology with the modified modulation method succeeded in handling reactive power with 3.15% THD, as shown in Figure 15. However, the proposed topology incorporating the conventional modulation method generates high THD that does not comply with the IEEE 1547 Standard (THD < 5%) due to the absence of current path in the negative power region. The THD is reduced by 82% with the modified modulation. The efficiency of the proposed topology with and without reactive power capability is measured with SiC MOSFET switching devices. The efficiency is determined according to the California Energy Commission (CEC), as shown in the following:

$$\eta_{CEC} = 0.04\eta_{10\%} + 0.05\eta_{20\%} + 0.12\eta_{30\%} + 0.21\eta_{50\%} + 0.53\eta_{75\%} + 0.05\eta_{100\%}$$
(19)



Figure 15. Total harmonic distortion (THD) of the proposed topology with the conventional and modified modulation technique.

Two switching frequencies using 16 kHz and 100 kHz and the calculated efficiency are given on Tables 2 and 3. The efficiency of the proposed inverter without reactive power capability at 16 kHz and 100 kHz is 98% and 97.6%, respectively. On the other hand, the efficiency of the proposed inverter with reactive power capability at 16 kHz and 100 kHz is 96.8% and 95.6%, respectively. These items are presented in Figures 16 and 17. Therefore, the cost of adding reactive power capability to the proposed inverter requires a reduction in system efficiency of about 1.2% and 2% for 16 kHz and 100 kHz, respectively. This reduction of system efficiency is due to the bidirectional flow of the current during reactive power generation, which leads to an increase in conduction loss.

Table 2. SCEC efficiency of proposed topology with SiC MOSFET at 16 kHz.

Output Power	300 W	600 W	900 W	1500 W	2250 W	3000 W	CEC
Proposed Topology (without reactive power capability)	99.7%	99.5%	99.3%	99%	98.4%	98%	98.7%
Proposed Topology (with reactive power capability)	99.2%	99%	98.6%	98.2%	97.5%	96.8%	97.7%

Table 3. SCEC efficiency of proposed topology with SiC MOSFET at 100 kHz.

Output Power	300 W	600 W	900 W	1500 W	2250 W	3000 W	CEC
Proposed Topology (without reactive power capability)	99.4%	99.3%	99%	98.6%	98.1%	97.6%	98.3%
Proposed Topology (with reactive power capability)	98%	97.8%	97.5%	96.9%	96.3%	95.6%	96.6%



Figure 16. Efficiency of the proposed topology with SiC MOSFET at 16 kHz.



Figure 17. Efficiency of the proposed topology with SiC MOSFET at 100 kHz.

5. Conclusions

When operating at high switching frequency, the stray capacitors will be charged and discharged by the common mode voltage (CMV). As a result, a resonant circuit is formed that includes PV stray capacitors, grid impedance and output filter inductances. Therefore, the resonant circuit will be excited and the leakage current will flow through the parasitic capacitances between the PV panel and the ground. There are many disadvantages associated with the circulating of leakage current in the system, such as increased system losses, total harmonic distortion (THD) and serious safety and radiated interference problems. In this paper, a proposed topology is modified with an extra switch to achieve constant common mode voltage and eliminating leakage current. Moreover, the modulation method is modified so that reactive power generation can be achieved. Three voltages' states must be generated in the negative power region to satisfy the requirement of unipolar PWM. However, conventional unipolar PWM cannot generate a zero-voltage in the negative power region. The proposed modulation technique helps to create a new current path in the negative power region where a zero-voltage state can be achieved. To this end, the proposed modulation technique enables the proposed inverter with reactive power generation without any changes to the inverter structure. Moreover, the proposed modulation achieved a constant common mode voltage that leads to eliminating leakage current. The results show that the proposed topology with the galvanic isolation method alone cannot completely eliminate leakage current. On the other hand, the common mode voltage is completely clamped at 400 V during the entire period, and a complete elimination of leakage current is achieved with the modified proposed topology. Moreover, there is current distortion in the negative power region with conventional PWM, while with the proposed modulation, a current path is created and the current distortion is eliminated. Furthermore, THD is reduced by 82% with the modified modulation method. The efficiency of the system is measured with SiC MOSFET to enhance system performance, especially with reactive power capability, because the increase in conducting switches also increases loss. In brief, the proposed modulation technique satisfies the requirement for a next-generation, single-phase, PV grid tied inverter where reactive power control is required.

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