



Article High-Quality SiO₂/O-Terminated Diamond Interface: Band-Gap, Band-Offset and Interfacial Chemistry

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Abstract: Silicon oxide atomic layer deposition synthesis development over the last few years has open the route to its use as a dielectric within diamond electronics. Its great band-gap makes it a promising material for the fabrication of diamond–metal–oxide field effects transistor gates. Having a sufficiently high barrier both for holes and electrons is mandatory to work in accumulation and inversion regimes without leakage currents, and no other oxide can fulfil this requisite due to the wide diamond band-gap. In this work, the heterojunction of atomic-layer-deposited silicon oxide and (100)-oriented p-type oxygen-terminated diamond is studied using scanning transmission electron microscopy in its energy loss spectroscopy mode and X-ray photoelectron spectroscopy. The amorphous phase of silicon oxide was successfully synthesized with a homogeneous band-gap of 9.4 eV. The interface between the oxide and diamond consisted mainly of single- and double-carbon-oxygen bonds with a low density of interface states and a straddling band setting with a 2.0 eV valence band-offset and 1.9 eV conduction band-offset.

Keywords: silicon oxide; diamond; interface; band-gap; band-offset; EELS; XPS

1. Introduction

Ultra-wide band-gap semiconductors provide the material basis for building more efficient electronic devices thanks to their superior properties. Among them, diamond stands out with its outstanding breakdown field (up to 10 MV/cm [1]) and its high electron and hole mobilities, which ensure excellent performances [2]. Importantly, it also possesses the best thermal conductivity and an enormous radiation hardness. These properties make diamond devices interesting for high-temperature, high-power, and harsh environments applications [3–5].

Concerning diamond transistors, remarkable progress has been shown in the last two decades as a result of intensive research. The majority of the successful diamond transistors are built thanks to the 2D hole gas that is present on the hydrogen-terminated diamond surface. These H-terminated Field effect transistors (FETs) show good on-state characteristics as this approach solves the low carrier density at room temperature challenge present in diamond caused by the deep dopant levels available [6–9]. However, thermal and time stability remain the main challenges to overcome, even if different solutions have already been proposed [10,11]. Alternatively, transistors such as MESFETs [12–14], JFETs [15,16], and depletion MOSFETs [17–20] based on p-type diamond are also an interesting



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Copyright: © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). approach. High temperature stability and radiation hardness are their main advantage over H-terminated FETs [21]. Lastly, a low power demonstration of diamond inversion-based transistors [22] has been achieved, but a high power implementation is still lacking due to the still immature technology.

With the opposite behaviour to the hydrogen termination, the oxygen termination of diamond displays an insulating character. It is thermally stable up to 700 K and can be useful for passivation, as it depletes holes away from the surface [23–25]. Transistor gates based on this termination can be very useful to acquire a normally off behaviour in both surface [7] and bulk [14] conducting transistors. However, several oxides such as alumina, zirconia, or hafnia have been investigated on oxygen-terminated diamond, showing leakage currents, hysteresis, staggered band setting, and Fermi level pinning [26–32]. The most-promising oxide in this respect is silicon oxide, where the accumulation regime and low density of interface states were found in our previous work [33] and a straddling band setting was predicted by calculations [3]. Additionally, normally off operation has been achieved by MOSFETs based on this oxide [34,35].

In this work, atomic layer deposition (ALD) of SiO_2 is performed on p-type (100)oriented O-terminated diamond in order to study the interfacial properties responsible for its low density of interface states [33]. A combined experimental study using scanning transmission electron microscopy in its energy loss spectroscopy mode (STEM-EELS) and X-ray photoelectron spectroscopy (XPS) to probe the band-gap of SiO_2 and the band alignment and interfacial chemistry between diamond and SiO_2 is presented.

STEM-EELS is a very strong tool to investigate the chemistry and band-gap at nanometre resolution, which is a necessity in the context of thin films. Nonetheless, the measurement of a material band-gap with valence EELS (VEELS) requires precise operation conditions and spectrum treatment to avoid an incorrect assessment. The single scattering distribution of electrons is directly related to the band-gap; however, multiple effects such as the Cherenkov effect or the zero loss peak are also present in the VEELS spectrum. To reduce the Cherenkov effect, which particularly affects the band-gap estimation, low acceleration voltages of 60 kV are needed for an accurate measurement of the band-gap [31,36-38]. XPS is an accessible, but key tool to study the interface bonds, band-gap, and band setting of heterostructures. It has already been used to quantify band-offsets and interfacial chemistry of different oxides and surface terminations of diamond heterostructures [11,32,39-44], but no contribution has yet reported the properties of SiO_2 on oxygen-terminated diamond.

2. Materials and Methods

Two samples consisting of O-terminated CVD-grown (100)-oriented p-type diamond with SiO_2 layers grown by ALD were used in this work. First, a p-type diamond layer of about a 1 micron thickness was grown by MPCVD with a nominal boron concentration of about 10^{16} cm⁻³ using $CH_4/H_2 = 1\%$, $O_2/H_2 = 0.25\%$, and B/C = 60 ppm at 900 °C on a (100) Sumitomo Ib substrate. The pressure used was 33 Torr, and the microwave power was 240 W. After the epitaxy, an ozone plasma treatment was carried out with the objective of oxidizing the sample surface [24,45]. The treatment consisted of a 120 min ozone plasma at 500 mbar achieved by using a Xenon EXCIMER UV lamp at 172 nm. Subsequently, 2 nm and 40 nm of SiO_2 were grown by ALD on each of the samples. The ALD conditions were similar to those used in our previous contribution [33], where the close-to-ideal electric behaviour of this interface was shown. A full scheme of the samples together with a TEM image of the diamond– SiO_2 interface for the 40 nm sample are presented in Figure 1.



Figure 1. Scheme of the two analysed samples. The samples consist of silicon oxide layers of 2 nm and 40 nm deposited by ALD on boron-doped MPCVD diamond layers grown on High pressure high temperature (HPHT) Ib diamond substrates. A transmission electron microscopy image of the thicker 40 nm silicon oxide layer is also presented.

The resultant SiO_2 layer was studied by scanning transmission electron microscopy in electron energy loss mode. Concerning the lamella preparation, the sample was nanomachined with Ga+ in a Helios Nanolab 650 SEM-FIB. The specimen was characterized by using an FEI-TITAN THEMIS double-aberration-corrected electron microscope. An accelerating voltage of 60 kV was used in order to avoid the Cherenkov effect. A 0.12 eV FWHM zero loss peak (ZLP) was acquired by using a monochromator. A convergence semi-angle of 16.00 mrad and a collection semi-angle of 20.02 mrad were used to obtain the low-loss spectra at the diamond– SiO_2 interface. Concerning the XPS study, all the measurements were carried out using high-resolution monochromatic Al-K α radiation (hv = 1486.7 eV). The spectra were recorded using a 0.1 eV step and a 60 eV pass energy after a mild Ar+ cleaning with low energy (0.5 kV) and an oblique angle (57°). The peak contributions were extracted by combining a Lorentzian and a Gaussian function (Voigt profile). The background was subtracted using the Tougaard background model function [46].

3. Results

The VEELS spectrum recorded on the lamella of the 40 nm ALD-grown SiO_2 sample is presented in Figure 2 in blue. The VEELS spectrum is generally composed of a variety of phenomena such as the ZLP (0–2 eV), Cherenkov effect (4–6 eV), or plasmonic peaks, which can impede the assessment of the electronic transitions and band-gap determination. In this case, the ZLP contribution was deconvoluted using a logarithmic tail fit to the range 2 eV to 5 eV of the spectrum, leaving a plateau in the 5 to 8 eV region, as no contribution was expected at these energies. No Cherenkov-effect-related contribution was observed in the spectrum due to the low beam energy used (60 keV) and the relatively low dielectric constant of the SiO_2 layer. The ZPL-deconvolved spectrum is presented in red in Figure 2. The spectrum presents three clearly distinguishable inter-band transitions peaks at 10.7 eV, 14.8 eV, and 18.2 eV and a plasmon peak at 22.5 eV [47].

The single scattering distribution at the band-gap edge is proportional to a $(E - E_{gap})^n$ curve. For the case of direct gap materials, this curve is proportional to the density of states, and so, an $(E - E_{gap})^{1/2}$ curve was observed in the spectrum. Thus, by fitting an $(E - E_{gap})^n$ curve, the value and the nature of the band-gap can be investigated. In Figure 2, the measurement of the *SiO*₂ band-gap can be observed for both the PEELS and VEELS spectra. The VEELS spectrum was accurately fit by a direct band-gap $(E - E_{gap})^{1/2}$ curve,

showing a band-gap value of 9.4 (±0.2) eV, which is in perfect agreement with reported values [47,48]. The small, but non-negligible contribution before the band-gap edge is associated with intra-gap transitions related to the surface of the lamella and, thus, is not representative of the thin film, but rather an artefact created by the preparation. The band-gap remained constant through the whole layer. The good agreement of the measurement within literature values together with the reproducibility among the layer ensures the good quality of the SiO_2 layer grown over the O-terminated (100)-oriented diamond.

In a similar fashion, the band-gap can be measured as well from the energy loss of the emitted photoelectrons when performing an XPS measurement. In Figure 2, the O(1s) photoelectron energy loss spectrum (PEELS) measured on the 40 nm SiO_2 sample is represented in green. The maximum of the O(1s) peak was set as the energy origin. The PEELS spectrum presents inter-band transitions peaks at 10.7 eV, 12.5 eV, 15.0 eV, and 17.9 eV and the plasmon peak at 22.2 eV. The similarities between the PEELS and VEELS spectra are remarkable with only the exception of the 12.5 eV peak not being distinguishable in the VEELS spectrum. The differences can be attributed to the higher surface sensitivity present in the PEELS measurement, as the collected scattered photoelectrons' interaction region is roughly the 2–3 nm beneath the surface [49]. The PEELS spectrum was as well fit by a direct band-gap $(E - E_{gap})^{1/2}$ curve, showing a band-gap value of 9.0 (±0.2) eV. Both techniques give compatible values within their error margins, although the VEELS measurement is considered more precise and reliable due to the reproducibility of the band-gap among spectra taken at different depths through the layer.



Figure 2. Scanning transmission electron microscopy—Valence energy loss spectroscopy (STEM-VEELS) spectrum measured on the 40 nm SiO_2 lamella represented as recorded (in blue) and with the zero loss peak deconvoluted (in red). The O(1s) X-ray photoelectron spectroscopy—photoelectron energy loss spectroscopy (XPS-PEELS) spectrum recorded on the 40 nm SiO_2 sample is represented in green with the O(1s) peak maximum as the energy origin. An $(E - E_{gap})^{1/2}$ curve is used to fit the band-gap edge of both spectra and is represented with a black discontinuous curve. The fitting of the spectra edge is represented as well in the inset of the figure, yielding a 9.4 ± 0.2 eV band-gap for the VEELS spectrum and 9.0 ± 0.2 eV for the PEELS spectrum.

3.1. Band Adjustment and Interface Chemistry Determination 3.1.1. Bulk SiO₂

The representative VB, Si(2p), and O(1s) XPS spectra from the measurement performed in the 40 nm sample are represented in Figure 3a. Since the inelastic mean free path of a photoelectron ejected from SiO_2 at ~ 1 keV is about ~ 3 nm [49], all the signal is unambiguously attributed to SiO_2 . The lower energy range spectrum is attributed to the SiO_2 VB. The first three peaks at 6.4 eV, 10.3 eV, and 13 eV are characteristic of the amorphous phase of SiO_2 . The valence band maximum (VBM) was determined by the widely used linear fit method [32], yielding a value of 3.6 eV. The peak centred at 102.3 eV is attributed to the Si (2p) spectrum. The whole Si(2p) area is attributed to Si bonded to oxygen (Si-O). Lastly, the O (1s) spectrum centred at 531.7 eV is shown. The summary of all the XPS peaks is presented in Table 1. Analogously, the whole area of this peak is attributed to oxygen bonded to silicon atoms (O-Si). The distance from Si(2p) to O(1s) is 429.4 eV, which is similar to reported values for the amorphous phase of SiO_2 . By using the relative sensitivity factor (RSF), the relation between oxygen and silicon core levels provides a stoichiometry for the silicon oxide layer of 32.7% for silicon and 67.3 % for oxygen. This is very close to the 2:1 stoichiometry expected for the layer.



Figure 3. (a) XPS spectra of the VB, Si(2p), and O(1s) recorded on the 40 nm SiO_2 layer sample. (b) XPS spectra of the VB, Si(2p), C(1s), and O(1s) recorded on the 2 nm SiO_2 layer sample. The VB and peak heights are scaled for better comprehension of the reader in (a,b). The factors used to scale the VB and peak heights are displayed as well in (a,b) referenced to the Si(2p) height in each measurement. (c) XPS deconvoluted spectrum of the C(1s) peak recorded on the 2 nm SiO_2 layer sample.

Table 1. Summary of the XPS peaks' energy and full-width at half-maximum (FWHM) for the two studied samples (2 nm and 40 nm of SiO_2). The C(1s) deconvoluted interfacial components' (measured on the 2 nm SiO_2 sample) energies and relative areas' (RA) are also shown at the bottom of the table. The RA is calculated as the ratio of the area between one of the interfacial component over the sum of the areas of all of them.

| Spectrum | | 2 nm <i>SiO</i> ₂ | 40 nm <i>SiO</i> ₂ |
|-----------------------------|--------|------------------------------|-------------------------------|
| VBM | Energy | 1.7 eV | 3.6 eV |
| Si(2p) | Energy | 102.4 eV | 102.3 eV |
| | FWHM | 1.7 eV | 1.6 eV |
| O(1s) | Energy | 531.9 eV | 531.8 eV |
| | FWHM | 1.6 eV | 1.5 eV |
| C(1s) | Energy | 284.8 eV | - |
| | FWHM | 0.7 eV | - |
| C(1s) interfacial component | | Energy | Relative area |
| $C(1s)_{C-Si}$ | | 283.6 eV | 15% |
| $C(1s)_{C-O}$ | | 286.0 eV | 35% |
| $C(1s)_{C=O}$ | | 287.4 eV | 50% |

3.1.2. Diamond–*SiO*₂ Interface

The representative VB, Si(2p), O(1s), and C(1s) XPS spectra from the measurement performed in the 2 nm sample are represented in Figure 3b. The lower-energy-range spectrum is attributed to the overlap of the SiO_2 and diamond VB. The peak at 14 eV is characteristic of diamond [32]. The VBM was determined again using the linear fit method [39], and the VBM was estimated as 1.7 eV. Thanks to the bigger contribution of diamond to the VB spectrum and the big valence band-offset between the two materials, the edge of the VB is attributed solely to diamond. The peak centred at 102.4 eV is attributed to the Si(2p) spectrum, while the peak centred at 531.9 eV to the O(1s) spectrum. Although an interfacial contribution is expected (whether it is C-O or Si-C), the whole Si(2p) and O(1s) areas are again attributed to Si-O bonding as there is no clear contribution of interfacerelated peaks in the spectra. An oxygen-rich stoichiometry of 28.8% of silicon and 71.2% oxygen was calculated with the relation of the peak areas by means of the RSF. The absence of clear interfacial peaks is explained by their proximity in energies to the main Si-O component in the Si(2p) and O(1s) spectra. The non-deconvoluted oxygen from the interface contributes positively to the extracted oxygen-rich silicon oxide layer stoichiometry as an artefact. Even so, the deviation from the 1:2 stoichiometry is most probably meaningful and related to the SiO_2 nucleation process.

Lastly, the C(1s) spectrum was found centred at 284.8 eV with smaller peaks at higher binding energies. These smaller peaks are attributed to interfacial bonding between diamond and SiO_2 and are shown in more detail in Figure 3c. A deconvolution of the C(1s) peak shows the presence of C-O (286 eV), C=O (287.4 eV), and C-Si (283.6 eV) bonding at the interface. The summary of all the XPS peaks, as well as the ratio of the interfacial peak areas are presented in Table 1. The relation between the areas shows a prominent carbon-oxygen bonding consisting of single- and double-bonding with a scarce silicon-carbon bond percentage in contrast with the work [11].

The interface between diamond and SiO_2 was also studied by STEM-EELS on the lamella of the 40 nm ALD-grown SiO_2 sample. In Figure 4a, an annular dark field (ADF) image of the interface displaying the analysis regions for EELS is presented. The EELS spectra of the O K-edge and the Si L-edge are presented in Figure 4b,c respectively. The O K-edge and Si L-edge areas' ratio is constant through the whole layer, indicating a constant stoichiometry. Even close to the interface, as in Spectrum 3, this ratio remains approximately constant. Spectrum 2, just at the interface, shows only features of oxygen, but no silicon, indicating C-O bonding, rather than C-Si bonding at the interface. Spectrum 1 is located at the diamond side, and both signals disappear as no oxygen or silicon was detected. These results support the conclusions extracted from the XPS analysis, pointing to a majority of C-O bonding at the interface, rather than C-Si bonds.

3.1.3. Band Setting

The valence band-offset (VBO) between SiO_2 and diamond can be calculated thanks to the XPS spectra of their representative core peaks and their VBM using the following formula [39]:

$$VBO = (Si(2p)_{SiO_2}^{40nm} - VBM_{SiO_2}^{40nm}) - (Si(2p)_{SiO_2}^{2nm} - C(1s)^{2nm}) - (C(1s)_{diam}^{O-diam} - VBM_{diam}^{O-diam})$$

where the first term is the distance between the Si(2p) core level and the VBM measured in the 40 nm-thick sample, the second term is the distance between the Si(2p) core level and the C(1s) core level measured in the 2 nm thin sample, and the third term is the distance between the C(1s) core level and the VBM measured in a O-terminated diamond sample. Applying the values from Table 1 and the most reliable C(1s)-VBM value reported in the literature of 282.8 eV [50], a value for the VBO of 1.7 eV was extracted. However, the great dispersion of values found in the literature for the C(1s)-VBM term encourages the estimation of the VBO using the following formula, as in our previous work [32]:

$$VBO = (Si(2p)_{SiO_2}^{40nm} - VBM_{SiO_2}^{40nm}) - (Si(2p)_{SiO_2}^{2nm} - VBM_{diam}^{2nm})$$
(1)

where the first term is the same as in the previous approach, but the second term is the distance between the Si(2p) core level and the (diamond) VBM directly measured in the 2 nm SiO_2 layer sample. This approach can be taken due to the big offset between diamond and silicon oxide, allowing the valence band maximum of diamond to be probed directly through the 2 nm of SiO_2 .



Figure 4. (a) ADF image of the SiO_2 -diamond interface displaying the analysis regions for EELS. EELS spectrum of (b) O K-edge and (c) Si L-edge in the near-interfacial region.

Using the data provided in the Table 1, a value of 2.0 eV was deduced, in relatively good agreement with the previously deduced value, thus validating this latter approach. Finally, applying the value of the SiO_2 layer band-gap measured by VEELS, a conduction band-offset (CBO) of 1.9 eV was extracted. Therefore, a straddling band setting was deduced between diamond and SiO_2 . A scheme of the band alignment and the interfacial chemistry is presented in Figure 5.

The theoretical VBO value reported between SiO_2 and O-terminated diamond deduced from their electron affinities is 2.7 eV [51], which is higher than the value extracted here. As the electron affinity approach does not take into account the formation of bonds at the interfaces and their associated charge redistribution, the difference between theoretical and experimental values can be explained as a result of the new bonding configuration and its resulting interface dipolar contribution to the band alignment [52]. Despite this, this measurement shows encouraging results as substantially big offsets are reported for the conduction and valence bands. Particularly noteworthy is the existence of a high barrier for electrons since no other oxide has been reported to have any, using similar experimental techniques to the one reported here, even though inversion regimes have been demonstrated on p-type diamond MOSFETs based on alumina [22]. These results open the route of the ALD SiO_2 –O-diamond interface for fabricating the gate of diamond transistors, whether if it is for inversion-based MOSFETs, due to the measured straddling band setting with a large electron barrier, or normally off H-MOSFETs, due to the low interface states' density [33] and the fabrication ease of this interface.





4. Conclusions

A detailed investigation of the properties of the ALD-grown SiO_2 and oxygenterminated p-type (100)-oriented diamond heterojunction was presented in this work. The band-gap of the amorphous SiO_2 is reported to be constant among the layers on a nanometric level with a value of 9.4 eV. The oxide bonding with diamond mainly consists of double- and single-C-O bonds, and the heterojunction presents a straddling band setting with 2.0 eV VBO and 1.9 eV CBO. These results show the potential of SiO_2 for the successful fabrication of diamond MOSFET gates where barriers both for holes and electron are present in contrast with any oxide ever investigated with similar techniques.

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References

- 1. Hiraiwa, A.; Kawarada, H. Figure of merit of diamond power devices based on accurately estimated impact ionization processes. *J. Appl. Phys.* **2013**, *114*, 034506. [CrossRef]
- Volpe, P.; Pernot, J.; Muret, P.; Omnès, F. High hole mobility in boron doped diamond for power device applications. *Appl. Phys.* Lett. 2009, 94, 092102. [CrossRef]
- Donato, N.; Rouger, N.; Pernot, J.; Longobardi, G.; Udrea, F. Diamond power devices: state of the art, modelling, figures of merit and future perspective. J. Phys. D Appl. Phys. 2019, 53, 093001. [CrossRef]

- 4. Umezawa, H. Recent advances in diamond power semiconductor devices. *Mater. Sci. Semicond. Process.* **2018**, *78*, 147–156. [CrossRef]
- 5. Araujo, D.; Suzuki, M.; Lloret, F.; Alba, G.; Villar, P. Diamond for Electronics: Materials, Processing and Devices. *Materials* **2021**, 14, 7081. [CrossRef] [PubMed]
- 6. Oi, N.; Inaba, M.; Okubo, S.; Tsuyuzaki, I.; Kageura, Y.; Onoda, S.; Hiraiwa, A.; Kawarada, H. Vertical-type two-dimensional hole gas diamond metal oxide semiconductor field-effect transistors. *Sci. Rep.* **2018**, *8*, 10660. [CrossRef]
- Kitabayashi, Y.; Kudo, T.; Tsuboi, H.; Yamada, T.; Xu, D.; Shibata, M.; Matsumura, D.; Hayashi, Y.; Syamsul, M.; Inaba, M.; et al. Normally-Off C-H Diamond MOSFETs With Partial C-O Channel Achieving 2-kV Breakdown Voltage. *IEEE Electron Device Lett.* 2017, 38, 363–366. [CrossRef]
- 8. Sasama, Y.; Kageura, Y.; Komatsu, K.; Moriyama, S.; Inoue, J.i.; Imura, M.; Watanabe, K.; Taniguchi, T.; Uchihashi, T.; Takahide, Y. Charge-carrier mobility in hydrogen-terminated diamond field-effect transistors. *J. Appl. Phys.* **2020**, *127*, 185707. [CrossRef]
- 9. Tsunoda, J.; Iwataki, M.; Oi, N.; Morishita, A.; Hiraiwa, A.; Kawarada, H. (111) vertical-type two-dimensional hole gas diamond MOSFETs with hexagonal trench structures. *Carbon* **2021**, *176*, 349–357. [CrossRef]
- Kawarada, H.; Yamada, T.; Xu, D.; Tsuboi, H.; Kitabayashi, Y.; Matsumura, D.; Shibata, M.; Kudo, T.; Inaba, M.; Hiraiwa, A. Durability-enhanced two-dimensional hole gas of C-H diamond surface for complementary power inverter applications. *Sci. Rep.* 2017, *7*, 42368. [CrossRef]
- 11. Zhu, X.; Bi, T.; Yuan, X.; Chang, Y.; Zhang, R.; Fu, Y.; Tu, J.; Huang, Y.; Liu, J.; Li, C.; et al. C-Si interface on SiO₂/(111) diamond p-MOSFETs with high mobility and excellent normally off operation. *Appl. Surf. Sci.* **2022**, *593*, 153368. [CrossRef]
- 12. Umezawa, H.; Matsumoto, T.; Shikata, S.I. Diamond Metal–Semiconductor Field-Effect Transistor with Breakdown Voltage Over 1.5 kV. *IEEE Electron Device Lett.* **2014**, *35*, 1112–1114. [CrossRef]
- 13. Driche, K. Diamond Unipolar Devices: Towards Impact Ionization Coefficients Extraction. PhD Thesis, Thèse de Doctorat Dirigée par Gheeraert, Etienne Nano Electronique et nano Technologies Université Grenoble Alpes, Grenoble, France, 2018.
- 14. Cañas, J.; Pakpour-Tabrizi, A.C.; Trajkovic, T.; Udrea, F.; Eon, D.; Gheeraert, E.; Jackman, R.B. Normally-OFF Diamond Reverse Blocking MESFET. *IEEE Trans. Electron Devices* **2021**, *68*, 6279–6285. [CrossRef]
- 15. Iwasaki, T.; Yaita, J.; Kato, H.; Makino, T.; Ogura, M.; Takeuchi, D.; Okushi, H.; Yamasaki, S.; Hatano, M. 600 V Diamond Junction Field-Effect Transistors Operated at 200°. *IEEE Electron Device Lett.* **2013**, *35*, 241–243. [CrossRef]
- 16. Iwasaki, T.; Kato, H.; Makino, T.; Ogura, M.; Takeuchi, D.; Yamasaki, S.; Hatano, M. High-temperature bipolar-mode operation of normally off diamond JFET. *IEEE J. Electron Devices Soc.* **2016**, *5*, 95–99. [CrossRef]
- 17. Pham, T.T.; Rouger, N.; Masante, C.; Chicot, G.; Udrea, F.; Eon, D.; Gheeraert, E.; Pernot, J. Deep depletion concept for diamond MOSFET. *Appl. Phys. Lett.* **2017**, *111*, 173503. [CrossRef]
- Pham, T.T.; Pernot, J.; Perez, G.; Eon, D.; Gheeraert, E.; Rouger, N. Deep-Depletion Mode Boron-Doped Monocrystalline Diamond Metal Oxide Semiconductor Field Effect Transistor. *IEEE Electron Device Lett.* 2017, 38, 1571–1574. [CrossRef]
- Pham, T.T.; Pernot, J.; Masante, C.; Eon, D.; Gheeraert, E.; Chicot, G.; Udrea, F.; Rouger, N. 200 V, 4 MV/cm lateral diamond MOSFET. In Proceedings of the 2017 IEEE International Electron Devices Meeting (IEDM), San Francisco, CA, USA, 2–6 December 2017; pp. 25.4.1–25.4.4. [CrossRef]
- Masante, C.; Pernot, J.; Letellier, J.; Eon, D.; Rouger, N. 175 V, >5.4 MV/cm, 50 mΩm. cm² at 250 °C Diamond MOSFET and its reverse conduction. In Proceedings of the 2019 31st International Symposium on Power Semiconductor Devices and ICs (ISPSD), Shanghai, China, 19–23 May 2019; pp. 151–154. [CrossRef]
- Umezawa, H.; Ohmagari, S.; Mokuno, Y.; Kaneko, J.H. Characterization of X-ray radiation hardness of diamond Schottky barrier diode and metal-semiconductor field-effect transistor. In Proceedings of the 2017 29th International Symposium on Power Semiconductor Devices and IC's (ISPSD), Sapporo, Japan, 28 May–1 June 2017; pp. 379–382. [CrossRef]
- Matsumoto, T.; Kato, H.; Oyama, K.; Makino, T.; Ogura, M.; Takeuchi, D.; Inokuma, T.; Tokuda, N.; Yamasaki, S. Inversion channel diamond metal-oxide-semiconductor field-effect transistor with normally off characteristics. *Sci. Rep.* 2016, *6*, 31585. [CrossRef]
- 23. Rutter, M.J.; Robertson, J. Ab initio calculation of electron affinities of diamond surfaces. *Phys. Rev. B* 1998, 57, 9241–9245. [CrossRef]
- 24. Navas, J.; Araujo, D.; Piñero, J.C.; Sánchez-Coronilla, A.; Blanco, E.; Villar, P.; Alcántara, R.; Montserrat, J.; Florentin, M.; Eon, D.; et al. Oxygen termination of homoepitaxial diamond surface by ozone and chemical methods: An experimental and theoretical perspective. *Appl. Surf. Sci.* **2018**, *433*, 408–418. [CrossRef]
- 25. Teraji, T.; Koide, Y.; Ito, T. Schottky barrier height and thermal stability of p-diamond (100) Schottky interfaces. *Thin Solid Films* **2014**, 557, 241–248. [CrossRef]
- Chicot, G.; Maréchal, A.; Motte, R.; Muret, P.; Gheeraert, E.; Pernot, J. Metal oxide semiconductor structure using oxygenterminated diamond. *Appl. Phys. Lett.* 2013, 102, 242108. [CrossRef]
- Loto, O.; Florentin, M.; Masante, C.; Donato, N.; Hicks, M.L.; Pakpour-Tabrizi, A.C.; Jackman, R.B.; Zuerbig, V.; Godignon, P.; Eon, D.; et al. Gate Oxide Electrical Stability of p-type Diamond MOS Capacitors. *IEEE Trans. Electron Devices* 2018, 65, 3361–3364.
 [CrossRef]
- 28. Pham, T.T.; Maréchal, A.; Muret, P.; Eon, D.; Gheeraert, E.; Rouger, N.; Pernot, J. Comprehensive electrical analysis of metal/Al2O3/O-terminated diamond capacitance. *J. Appl. Phys.* **2018**, *123*, 161523. [CrossRef]

- Soto, B.; Cañas, J.; Villar, M.; Araujo, D.; Pernot, J. Transport mechanism in O-terminated diamond/ZrO2 based MOSCAPs. *Diam. Relat. Mater.* 2021, 121, 108745. [CrossRef]
- Kovi, K.K.; Vallin, Ö.; Majdi, S.; Isberg, J. Inversion in Metal–Oxide–Semiconductor Capacitors on Boron-Doped Diamond. *IEEE Electron Device Lett.* 2015, 36, 603–605. [CrossRef]
- Cañas, J.; Piñero, J.; Lloret, F.; Gutierrez, M.; Pham, T.; Pernot, J.; Araujo, D. Determination of alumina bandgap and dielectric functions of diamond MOS by STEM-VEELS. *Appl. Surf. Sci.* 2018, 461, 93–97. [CrossRef]
- Cañas, J.; Alba, G.; Leinen, D.; Lloret, F.; Gutierrez, M.; Eon, D.; Pernot, J.; Gheeraert, E.; Araujo, D. Diamond/γ-alumina band offset determination by XPS. *Appl. Surf. Sci.* 2021, 535, 146301. [CrossRef]
- Cañas, J.; Dussarrat, C.; Teramoto, T.; Masante, C.; Gutierrez, M.; Gheeraert, E. High quality SiO₂/diamond interface in O-terminated p-type diamond MOS capacitors. *Appl. Phys. Lett.* 2022, 121, 072101. [CrossRef]
- 34. Huang, B.; Bai, X.; Lam, S.K.; Tsang, K.K. Diamond FinFET without Hydrogen Termination. Sci. Rep. 2018, 8, 3063. [CrossRef]
- 35. Fei, W.; Bi, T.; Iwataki, M.; Imanishi, S.; Kawarada, H. Publisher's Note: "Oxidized Si terminated diamond and its MOSFET operation with SiO₂ gate insulator". *Appl. Phys. Lett.* **2020**, *116*, 269901. [CrossRef]
- Stöger-Pollach, M.; Franco, H.; Schattschneider, P.; Lazar, S.; Schaffer, B.; Grogger, W.; Zandbergen, H. Čerenkov losses: A limit for bandgap determination and Kramers–Kronig analysis. *Micron* 2006, *37*, 396–402. [CrossRef]
- Stöger-Pollach, M.; Schattschneider, P. The influence of relativistic energy losses on bandgap determination using valence EELS. Ultramicroscopy 2007, 107, 1178–1185. [CrossRef]
- Kimoto, K.; Kothleitner, G.; Grogger, W.; Matsui, Y.; Hofer, F. Advantages of a monochromator for bandgap measurements using electron energy-loss spectroscopy. *Micron* 2005, 36, 185–189. [CrossRef]
- 39. Maréchal, A.; Aoukar, M.; Vallée, C.; Rivière, C.; Eon, D.; Pernot, J.; Gheeraert, E. Energy-band diagram configuration of Al2O3/oxygen-terminated p-diamond metal-oxide-semiconductor. *Appl. Phys. Lett.* **2015**, *107*, 141601. [CrossRef]
- Maréchal, A.; Kato, Y.; Liao, M.; Koizumi, S. Interfacial energy barrier height of Al₂O₃/H-terminated (111) diamond heterointerface investigated by X-ray photoelectron spectroscopy. *Appl. Phys. Lett.* 2017, 111, 141605. [CrossRef]
- Liu, J.W.; Liao, M.Y.; Imura, M.; Koide, Y. Band offsets of Al₂O₃ and HfO₂ oxides deposited by atomic layer deposition technique on hydrogenated diamond. *Appl. Phys. Lett.* 2012, 101, 252108. [CrossRef]
- 42. Zhang, F.; Sun, G.; Zheng, L.; Liu, S.; Liu, B.; Dong, L.; Wang, L.; Zhao, W.; Liu, X.; Yan, G.; et al. Interfacial study and energy-band alignment of annealed Al₂O₃ films prepared by atomic layer deposition on 4H-SiC. *J. Appl. Phys.* **2013**, *113*, 044112. [CrossRef]
- 43. Ren, Z.; Lv, D.; Xu, J.; Zhang, J.; Zhang, J.; Su, K.; Zhang, C.; Hao, Y. High temperature (300 °C) ALD grown Al₂O₃ on hydrogen terminated diamond: Band offset and electrical properties of the MOSFETs. *Appl. Phys. Lett.* **2020**, *116*, 013503. [CrossRef]
- Takahashi, K.; Imamura, M.; Hirama, K.; Kasu, M. Electronic states of NO₂-exposed H-terminated diamond/Al₂O₃ heterointerface studied by synchrotron radiation photoemission and X-ray absorption spectroscopy. *Appl. Phys. Lett.* 2014, 104, 072101. [CrossRef]
- 45. Teraji, T.; Garino, Y.; Koide, Y.; Ito, T. Low-leakage p-type diamond Schottky diodes prepared using vacuum ultraviolet light/ozone treatment. *J. Appl. Phys.* **2009**, *105*, 126109. [CrossRef]
- Tougaard, S.; Sigmund, P. Influence of elastic and inelastic scattering on energy spectra of electrons emitted from solids. *Phys. Rev. B* 1982, 25, 4452–4466. [CrossRef]
- Garvie, L.; Rez, P.; Alvarez, J.; Buseck, P. Interband transitions of crystalline and amorphous SiO₂: An electron energy-loss spectroscopy (EELS) study of the low-loss region. *Solid State Commun.* **1998**, *106*, 303–307. [CrossRef]
- Weinberg, Z.A.; Rubloff, G.W.; Bassous, E. Transmission, photoconductivity, and the experimental band gap of thermally grown SiO₂ films. *Phys. Rev. B* 1979, 19, 3107–3117. [CrossRef]
- 49. Shinotsuka, H.; Tanuma, S.; Powell, C.J.; Penn, D.R. Calculations of electron inelastic mean free paths. X. Data for 41 elemental solids over the 50 eV to 200 keV range with the relativistic full Penn algorithm. *Surf. Interface Anal.* 2015, 47, 871–888. [CrossRef]
- 50. Koh, D.; Banerjee, S.; Brockman, J.; Kuhn, M.; King, S.W. X-ray photoelectron spectroscopy investigation of the valence band offset at beryllium oxide-diamond interfaces. *Diam. Relat. Mater.* **2020**, *101*, 107647. [CrossRef]
- Donato, N. Modelling and Design of Diamond Power Semiconductor Devices. PhD Thesis, University of Cambridge, Cambridge, UK, 2019.
- 52. Tung, R.T.; Kronik, L. Charge Density and Band Offsets at Heterovalent Semiconductor Interfaces. *Adv. Theory Simulations* **2018**, 1, 1700001. [CrossRef]