

Supplementary Material

Remarkably High-Performance Nanosheet GeSn Thin-Film Transistor

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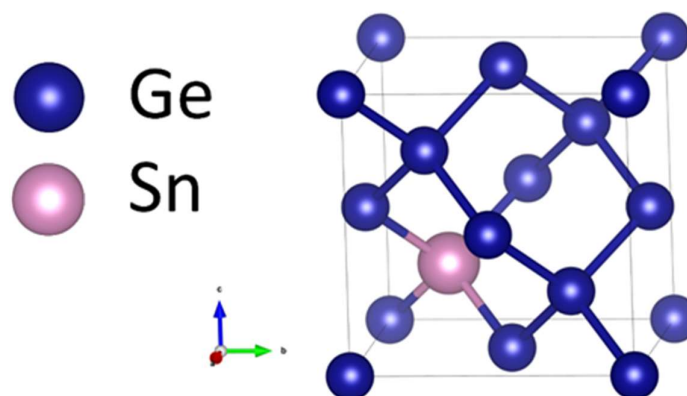


Figure S1. Optimized structure of the diamond cubic $\text{Ge}_{0.875}\text{Sn}_{0.125}$ model with the lattice constant of 5.763 Å, containing eight atoms by first-principles calculations.

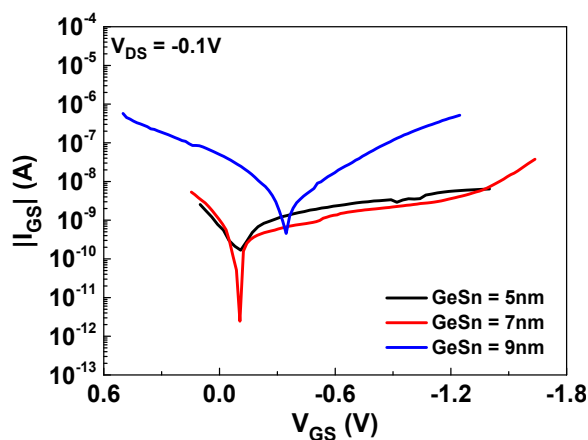


Figure S2. The $|I_{GS}|$ – V_{GS} characteristics of the GeSn/SiO₂/HfO₂ pTFTs with different channel thickness.

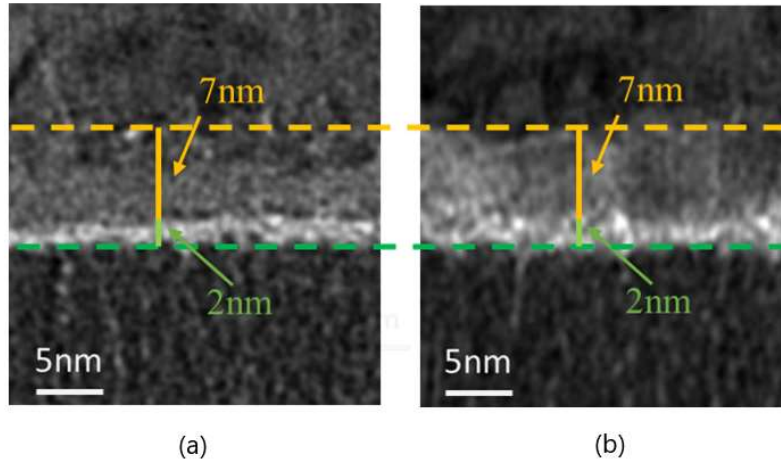


Figure S3. The cross-sectional TEM image of GeSn pTFT with GeSn annealed at (a) 300 and (b) 350 °C.

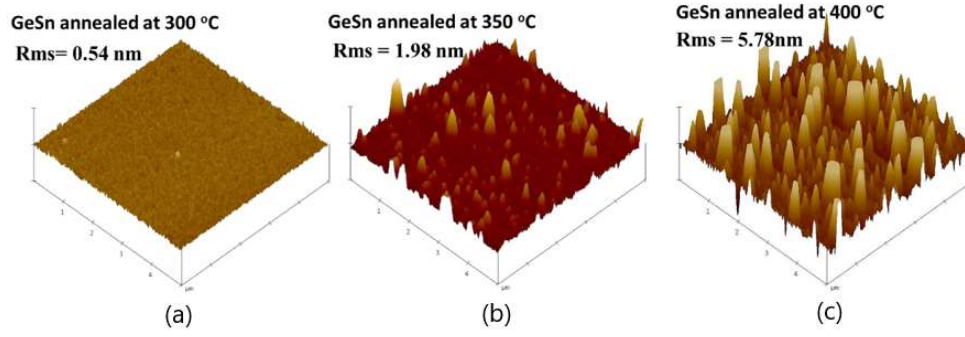


Figure S4. The surface roughness of GeSn films annealed at (a) 300 °C, (b) 350 °C and (c) 400 °C measured by AFM.

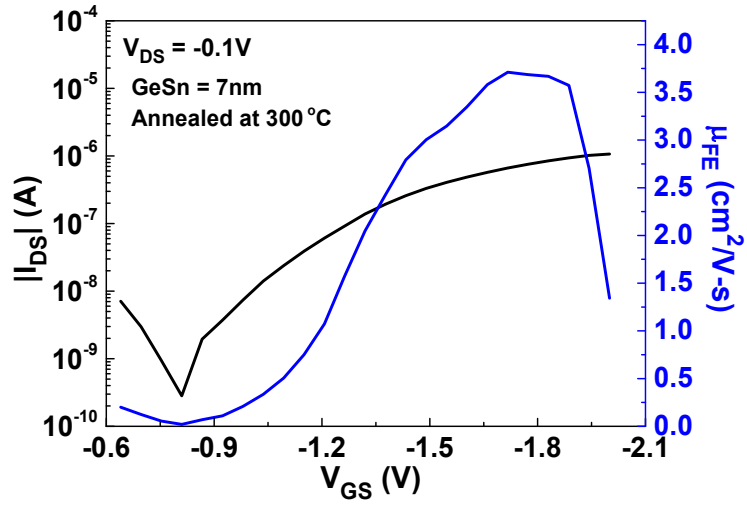


Figure S5. The $|I_{DS}|$ - V_{GS} and μ_{FE} - V_{GS} characteristics of the GeSn pTFTs with 7nm channel thickness and annealed at 300 °C.

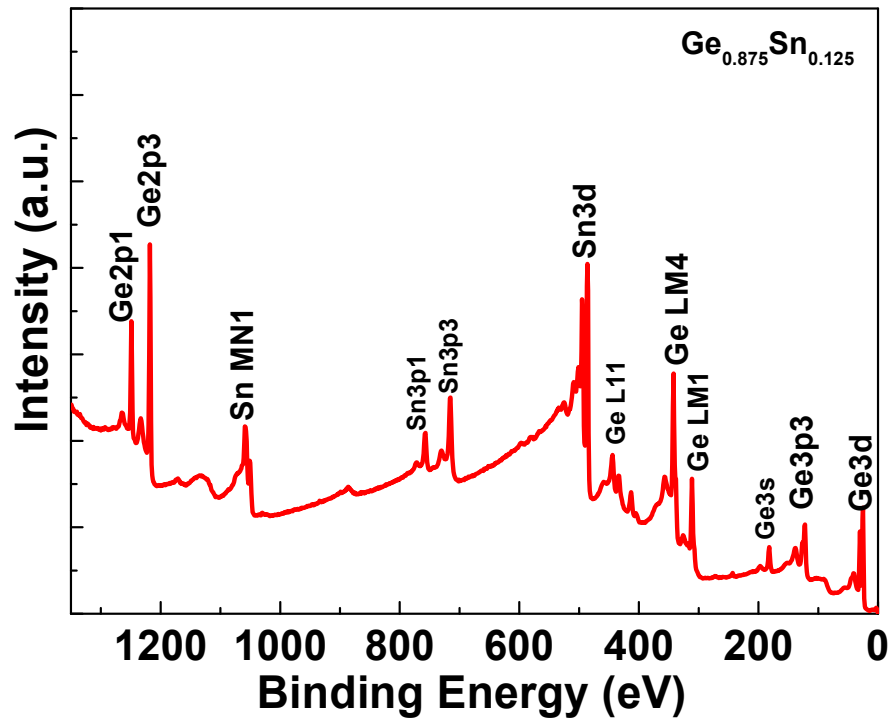


Figure S6. The XPS spectrum of $\text{Ge}_{0.875}\text{Sn}_{0.125}$.

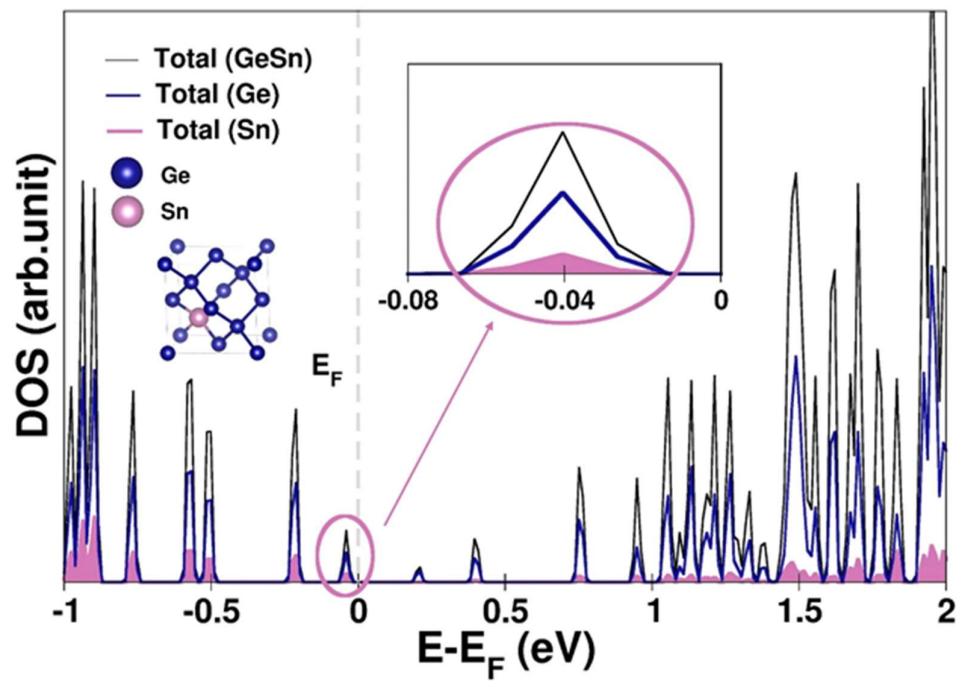


Figure S7. Total density of state (DOS) and projected DOS of Ge and Sn of $\text{Ge}_{0.875}\text{Sn}_{0.125}$ by first-principles calculations.