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Analysis of DC Characteristics in GaN-Based Metal-Insulator-Semiconductor High Electron Mobility Transistor with Variation of Gate Dielectric Layer Composition by Considering Self-Heating Effect



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Abstract: This study investigates metal-insulator-semiconductor high electron mobility transistor DC characteristics with different gate dielectric layer compositions and thicknesses, and lattice temperature effects on gate leakage current by using a two-dimensional simulation. We first compared electrical properties, including threshold voltage, transconductance, and gate leakage current with the self-heating effect, by applying a single Si₃N₄ dielectric layer. We then employed different Al₂O₃ dielectric layer thicknesses on top of the Si₃N₄, and also investigated lattice temperature across a two-dimensional electron gas channel layer with various dielectric layer compositions to verify the thermal effect on gate leakage current. Gate leakage current was significantly reduced as the dielectric layer was added, and further decreased for a 15-nm thick Al₂O₃ on a 5-nm Si₃N₄ structure. Although the gate leakage current increased as Al₂O₃ thickness increased to 35 nm, the breakdown voltage was improved.

Keywords: GaN; metal-insulator-semiconductor high electron mobility transistor; gate leakage current; two-dimensional electron gas; breakdown voltage

1. Introduction

GaN-based high electron mobility transistors (HEMTs) have been studied as promising power devices for various applications, such as a communication base transceiver station and military radar equipment, due to their superior material characteristics, including high breakdown voltage, high electron mobility, high thermal conductivity, and wide bandgap [1–3]. However, a gate leakage current degrades a device's electrical properties, power efficiency, and stability during operation. Generally, a gate leakage current is affected by a high lattice temperature and electric field and occurs mostly near the drain-side gate head edge [4–10]. When an insulator is placed between the gate metal and semiconductor, i.e., metal-insulator-semiconductor field-effect transistor, an electric field generated by the gate voltage is strongly formed in the insulator having a large resistance, such that the input impedance becomes very large and the gate leakage current can be reduced. Gate leakage current in GaN-based HEMTs can be reduced using structures similar to metal-insulator-semiconductor high electron mobility transistors (MIS-HEMTs), where high-k materials, such as Si₃N₄ and Al₂O₃, are deposited under the gate metal [11–18]. MIS-HEMTs have been studied by adding a high quality single

dielectric layer, such as Si_3N_4 , Al_2O_3 , SiO_2 , HfO_2 , Sc_2O_3 , GaO_3 , or ZrO_2 , to suppress current collapse, improve current flow, and reduce gate leakage current [14–17,19,20]. However, various dielectric layers deposited on top of a GaN-based HEMT cause an interface state trap and mechanical stresses, resulting in changes in threshold voltage and carrier concentration, respectively. Therefore, appropriate dielectric material and thickness selections are required. The Si_3N_4 dielectric layer causes compressive stress, but the Al_2O_3 dielectric layer deposited on the Si_3N_4 dielectric layer generates tensile stress. Since proper tensile stress helps to increase the carrier concentration compared to compressive stress, the combination of Si_3N_4 and Al_2O_3 layer thicknesses is very important [21].

This study investigated transfer I–V characteristics, gate leakage current, lattice temperature across two-dimensional electron gas (2-DEG) channel layer, and MIS-HEMT drain current variation by changing the dielectric layer compositions and thicknesses with the help of a two-dimensional simulator [22]. To begin with, we compared electrical properties, including threshold voltage, transconductance, and gate leakage current with the self-heating effect (SHE) by applying a single Si_3N_4 dielectric layer. We then employed different Al_2O_3 dielectric layer thicknesses on top of the Si_3N_4 and analyzed the lattice temperature across the 2-DEG channel layer to verify the thermal effect on the gate leakage current. Finally, we discuss an optimized MIS-HEMT structure as a normally-on device.

2. Materials and Methods

This study analyzed and compared MIS-HEMT transfer I–V characteristics for different dielectric layer compositions and thicknesses, as shown in Figure 1. Figure 1a,b shows schematic views of a basic AlGaN/GaN-based HEMT (BG-HEMT) and MIS-HEMT, respectively. Figure 1c represents the composition and thickness of distinct dielectric layers A, B, and C. Table 1 summarizes the BG-HEMT layer information, where the nominal Al composition of the AlGaN layer was 26%.



Figure 1. Basic AlGaN/GaN-based MIS-HEMT (**a**) without and (**b**) with a dielectric layer, and (**c**) dielectric layer composition and thickness for samples A, B, and C.

Parameter	Value	
(1) L_{SG}	1.0 μm	
$(2) L_G$	4.5 μm	
$(3) L_{GD}$	9.5 μm	
GaN Cap	2.0 nm	
AlGaN	21 nm	
AlN	1.0 nm	
UID GaN Channel	0.1 μm	
GaN Buffer	1.8 µm	

Table 1. Basic AlGaN/GaN-based HEMT geometrical parameters.

We used two gate leakage current models to compare the BG-HEMT and MIS-HEMT structures' gate leakage currents. Figure 2a depicts the Poole–Frenkel thermal emission (PF) mechanism principle. Crystalline mismatches between layers when fabricating AlGaN/GaN HEMTs cause trap and continuum energy states in the barrier material. Electrons captured in trap energy states move to continuum energy states via high electric fields and temperatures, and current is generated by the voltage drop in the high reverse gate voltages. Figure 2b shows the Fowler–Nordheim tunneling (FN) mechanism. The barrier becomes thinner under high electric fields, allowing more electrons to penetrate the barrier and generate a current at high forward gate voltages [23–25]. The interface trap between the insulator and semiconductor is inevitable while growing epitaxial layers and causes the 2-DEG concentration and threshold voltage to become unstable. Therefore, it is essential to consider the interface trap density [26,27]. To obtain more accurate data, we set the interface trap density at the dielectric layer/GaN cap interface and acceptor trap density in the GaN buffer layer. Figure 3a shows the interface trap density at the Si₃N₄/GaN cap interface = 2.1×10^{12} cm⁻², and Figure 3b shows acceptor trap density doping profile in GaN buffer layer = 2.09×10^{15} cm⁻³ at the unintentionally doped (UID) GaN/GaN buffer interface [28].



Figure 2. Mechanism of gate leakage current models: (**a**) Poole–Frenkel thermal emission, and (**b**) Fowler–Nordheim tunneling.



Figure 3. (a) Interface trap density at the Si_3N_4/GaN cap interface, and (b) GaN buffer layer acceptor trap density doping profile with respect to depth.

Since the thermal conductivity of the material affects the heat transfer during operation of the device and depends on the temperature, the lattice heat flow based on the thermal conductivity model is essential to considering SHE [29]. The thermal conductivity model is given by:

$$k(T_L) = (TC.CONST) / \left(\frac{T_L}{300}\right)^{TC.NPOW}$$
(1)

where *TC.CONST*, T_L , and *TC.NPOW* are the thermal conductivity constant at 300 K, local lattice temperature, and coefficient of temperature dependence of thermal conductivity, respectively. As shown in Table 2, thermal conductivity constants of GaN and AlGaN were applied. Then, the lattice heat flow model is applied by using the calculated thermal conductivity, and it can be expressed as

$$C\frac{\partial T_L}{\partial t} = \nabla(k\nabla T_L) + H \tag{2}$$

where C, k, H, and T_L are the heat capacitance per unit volume, thermal conductivity, heat generation, and local lattice temperature, respectively. k is calculated using Equation (1), and H is expressed as:

$$H = \frac{\left|\vec{J_n}\right|^2}{q\mu_n n} + \frac{\left|\vec{J_p}\right|^2}{q\mu_p p} - T_L\left(\vec{J_n}\nabla P_n\right) - T_L\left(\vec{J_p}\nabla P_p\right) + q(R-G) \left[T_L\left[\frac{\partial\varphi_n}{\partial T}\right]_{n,p} - \varphi_n - \left[\frac{\partial\varphi_p}{\partial T}\right]_{n,p} + \varphi_p\right]$$
(3)
$$-T_L\left[\left[\frac{\partial\varphi_n}{\partial T}\right]_{n,p} + P_n\right] divJ_n - T_L\left[\left[\frac{\partial\varphi_p}{\partial T}\right]_{n,p} + P_p\right] divJ_p$$

In the steady-state, the current divergence can be replaced by the net recombination. Equation (3) then simplifies to:

$$H = \left[\frac{\left|\vec{J_n}\right|^2}{q\mu_n n} + \frac{\left|\vec{J_p}\right|^2}{q\mu_p p}\right] + q(R - G)\left[\varphi_p - \varphi_n + T_L\left[P_p - P_n\right]\right] - T_L(\vec{J_n}\nabla P_n + \vec{J_p}\nabla P_p)$$
(4)

where
$$\left[\frac{\left|\vec{J_n}\right|^2}{q\mu_n n} + \frac{\left|\vec{J_p}\right|^2}{q\mu_p p}\right]$$
 is the Joule heating effect term, $q(R-G)[\varphi_p - \varphi_n + T_L[P_p - P_n]]$ is the heating and

cooling term considering recombination and generation, and $-T_L(J_n \nabla P_n + J_p \nabla P_p)$ accounts for the Peltier and Joule–Thomson effects. A simple and intuitive form of *H* that has been widely used in the past is:

$$H = (\vec{J_n} + \vec{J_p}) \cdot \vec{E}$$
(5)

where *H* is the generated heat, $\vec{J_n}$ and $\vec{J_p}$ are electron and hole current densities, respectively, and \vec{E} is the electric field. SHE can be estimated using either Equation (4) or (5) for steady-state calculations. By default, Equation (5) is used.

We set the work function of the gate, source, and drain metals for Schottky and ohmic contacts, respectively. However, in the case of MIS-HEMT, the work function changes because of dielectric layers, even if the work function of the gate metal is fixed. This is due to the high-*k* effective work function model, which is expressed as:

$$\Phi_{meff} = E_{highk} + S_{highk} (\Phi_m - E_{highk})$$
(6)

where Φ_{meff} is the effective gate work function, E_{highk} is the high-*k* material charge neutrality, S_{highk} is a slope parameter of the high-*k* material, and Φ_m is the metal work function in a vacuum ($\Phi_m = 3.73 \text{ eV}$ for a Schottky contact [30]).

We considered auger recombination, Schockley–Read–Hall recombination, strain piezoelectric polarization, Selberherr's impact ionization model, self-heating, thermal conductivity, the Fermi–Dirac distribution function, Poole–Frenkel thermal emission, the Fowler-Nordheim tunneling model, and the high-*k* effective work function model for the physical calculations [31–34]. Table 2 shows AlGaN and GaN material parameters.

Figure 4 shows the thermal conductivities of GaN, Al_2O_3 , and Si_3N_4 as a function of temperature. As temperature increases above 400 K, the thermal conductivity of Al_2O_3 becomes lower than that for Si_3N_4 .



Figure 4. Thermal conductivities for GaN, Al₂O₃, and Si₃N₄ with respect to temperature.

3. Results and Discussion

We first compared BG-HEMT and MIS-HEMT (sample A) transfer I–V characteristics for each drain voltage (V_{DS}) = 1, 5, 10, and 30 V. The sample A threshold voltage shifted negatively to reach approximately –7.5 V, and the maximum transconductance decreased compared with BG-HEMT, as shown in Figure 5. The sample A drain current was generally larger than that for BG-HEMT and became much larger as V_{DS} increased.



Figure 5. Transfer I–V characteristics for BG-HEMT and MIS-HEMT (sample A) at (**a**) $V_{DS} = 1$ V, (**b**) $V_{DS} = 5$ V, (**c**) $V_{DS} = 10$ V, and (**d**) $V_{DS} = 30$ V (drain current (filled dot), transconductance (empty dot)).

Figure 6 shows the measured gate leakage current with respect to the gate voltage (V_{GS}) for V_{DS} = 1, 5, 10, and 30 V. In the case of BG-HEMT, the gate leakage current decreased as V_{GS} increased at low V_{DS} = 1 and 5 V. However, at high V_{DS} = 10 and 30 V, the gate leakage current decreased with increasing V_{GS} , but increased again. The gate leakage current was affected by heat, and when V_{GS} was above the threshold voltage of about -3.7 V, the gate leakage current increased because heat was generated by the device operation. In contrast, the gate leakage current was significantly reduced in sample A.

Figure 7 shows the lattice temperature and electric field across the 2-DEG channel layer of the BG-HEMT and sample A at $V_{GS} = 0$ V and $V_{DS} = 30$ V. Sample A's lattice temperature in the 2-DEG channel layer was significantly higher than that for BG-HEMT, but sample A's electric field was lower than that for BG-HEMT. Since the increase of current density was larger (see Figure 5d) than the decrease of the electric field in sample A, as shown in Figure 7b, the overall lattice temperature of sample A across the 2-DEG channel layer was larger than that of BG-HEMT, as shown in Figure 7a (see Equation (5)). Although the gate leakage current can be affected by temperature, the dielectric layer effectively reduced the gate leakage current [21].



Figure 6. Gate leakage current for BG-HEMT and MIS-HEMT (sample A) with respect to V_{GS} at (a) $V_{DS} = 1 \text{ V}$, (b) $V_{DS} = 5 \text{ V}$, (c) $V_{DS} = 10 \text{ V}$, and (d) $V_{DS} = 30 \text{ V}$.

Parameter	GaN	AlGaN	
Bandgap energy (eV)	3.39	3.89	
Electron affinity (eV)	4	3.6	
Low field mobility ($cm^2 V^{-1} s^{-1}$)	990	300	
Relative permittivity	9.5	9.38	
Thermal conductivity constant (W cm ^{-1} K ^{-1})	1.3	0.4	
TC.NPOW	0.43	-	
High field mobility model	GANSAT saturation velocity model		
Electron Saturation velocity (cm s ⁻¹)	1.9×10^{7}	1.12×10^{7}	

Table 2. Material parameters used for simulations at temperature 300 K.

After confirming that sample A's gate leakage current was markedly reduced, the dielectric layer composition was changed, as shown in Figure 1c. We then compared the transfer I–V characteristics for these structures when $V_{DS} = 1, 5, 10$, and 30 V. Figure 8 shows the threshold voltages, maximum transconductances, and drain currents for samples A, B, and C. Sample B's threshold voltage shifted toward the positive direction as compared with sample A, and sample C shifted toward the negative direction [35]. Sample B and C's threshold voltages were about –6.2 and –9.4 V, respectively. Maximum transconductance was achieved for sample B, then C and A, in descending order. However, the drain current exhibited different tendencies, with sample C obtaining the highest value, then sample B and A, in descending order.



Figure 7. (a) Lattice temperature, and (b) electric field distributions across the 2-DEG channel layer with and without the Si₃N₄ dielectric layer at $V_{GS} = 0$ V and $V_{DS} = 30$ V.



Figure 8. Comparison of transfer I–V characteristics of MIS-HEMTs (samples A, B, and C) with respect to V_{GS} at (a) $V_{DS} = 1$ V, (b) $V_{DS} = 5$ V, (c) $V_{DS} = 10$ V, and (d) $V_{DS} = 30$ V (drain current (filled dot), transconductance (empty dot)).

Samples A, B, and C's gate leakage currents were simulated at high $V_{DS} = 10$ and 30 V. Figure 9 shows that sample B's gate leakage current was significantly reduced compared with sample A, even though the values were extremely low. Since the actual physical phenomena occurring in the device were not known, the results only represented the tendency of the gate leakage current depending on

the dielectric layer composition and thickness. Sample C's gate leakage current exceeded sample B's. Thus, Al_2O_3 deposition on Si_3N_4 effectively reduced the gate leakage current, but the gate leakage current increased with thicker Al_2O_3 on Si_3N_4 .



Figure 9. Gate leakage current for MIS-HEMTs (samples A, B, and C) with respect to V_{GS} at (a) $V_{DS} = 10 \text{ V}$ and (b) $V_{DS} = 30 \text{ V}$.

Figure 10a shows that sample C's lattice temperature across the 2-DEG channel layer was the highest, with samples A and B exhibiting similar values when $V_{CS} = 0$ V and $V_{DS} = 30$ V. The highest peaks occurred at the drain-side gate head edges with 441.94, 406.64, and 406.38 K for samples C, A, and B, respectively. However, the electric field distributions for the three structures were different. Sample A was the highest, followed by samples B and C at the drain-side gate head edge. As shown in Figure 5; Figure 7, sample C's lattice temperature across the 2-DEG channel layer was higher than sample B because sample C's effective current density was also higher, as shown in Figure 8. Figure 10b shows that sample C exhibited an even lower electric field at the drain-side gate edge, which resulted in an increasing gate leakage current, as shown in Figure 9. As the thickness of the insulator increased, the current due to the tunneling effect decreased. However, when the thickness of the insulator increased, the threshold voltage, stress, and heat transfer must be considered. In the case of samples B and C, the dielectric layer composition was a combination of Al_2O_3 and Si_3N_4 . Since the barrier height in samples B and C were larger than for A, the gate leakage current by FN tunneling was reduced. For sample C, the electric field was the lowest, but the current density was the highest at $V_{GS} = 0$ V and $V_{DS} = 30$ V, resulting in generating more heat. In addition, the thermal conductivity of the materials must be taken into account in the heat transfer. The thermal conductivity of Al₂O₃ was lower than that of Si₃N₄. The induced heat confined in the Al₂O₃ further lowered the barrier height and increased the gate leakage current.

Figure 11 shows the MIS-HEMT breakdown voltage characteristics. Depositing the Al_2O_3 on the Si_3N_4 dielectric layer improved the breakdown voltage. We set the breakdown voltage for the drain current = 1 mA/mm as a reference, and achieved breakdown voltage values that were about 94, 194, and 300 V at pinch-off voltages = -14, -12, and -12.6 V, respectively, considering each threshold voltage. Although the gate leakage current in sample C was larger than for sample B, it showed higher breakdown characteristics.



Figure 10. (a) Lattice temperature, and (b) electric field distributions across 2-DEG channel layer for MIS-HEMTs (samples A, B, and C) at $V_{GS} = 0$ V, and $V_{DS} = 30$ V.



Figure 11. Breakdown voltage characteristics for MIS-HEMTs (samples A, B, and C).

Figure 12 shows the MIS-HEMT saturation current characteristics with and without SHE for $V_{GS} = -2$ to 0 V. SHE decreased the drain current for all three structures and made the current unstable. Sample C was more affected by SHE and sample B was less affected. Table 3 shows the calculated level of drain current degradation for all three structures. Although sample C had a higher breakdown voltage and drain current, the gate leakage current was higher and the drain current was more affected by SHE. Therefore, an appropriate Al₂O₃ thickness on Si₃N₄ is crucial to operating an AlGaN/GaN HEMT as a normally-on device.



Figure 12. Saturation current characteristics for MIS-HEMT with and without the self-heating effect (SHE) as a normally-on device: (**a**) sample A, (**b**) sample B, and (**c**) sample C.

-	ΔI_D (mA/mm) (at V _{DS} = 40 V)					
Type	V_{GS} = -2.0 V	V_{GS} = -1.5 V	V_{GS} = -1.0 V	V_{GS} = -0.5 V	$V_{GS} = 0.0 V$	
Α	-45.17	-58.38	-73.23	-89.49	-107.41	
В	-44.49	-60.10	-77.99	-98.01	-119.92	
С	-138.17	-161.87	-187.07	-213.58	-241.27	

Table 3. Drain current variation due to the self-heating effect.

4. Conclusions

This study provides a simulation study of transfer I–V characteristics, gate leakage current, and saturation current characteristics for MIS-HEMT structures by considering SHE. The gate leakage current increased with increasing temperature, but the MIS-HEMT gate leakage current was significantly lower than the basic AlGaN/GaN-based HEMT, even when temperature was increased by applying the gate dielectric layer. The gate dielectric layer, consisting of Si₃N₄ and Al₂O₃, reduced the gate leakage current more than a single Si₃N₄ gate dielectric layer did. However, the threshold voltage, gate leakage current, and breakdown voltage all changed depending on the dielectric layer composition and thickness. The MIS-HEMT gate leakage current and breakdown voltage with Al₂O₃ on Si₃N₄ were significantly improved compared with the device incorporating only Si₃N₄. However, the saturation current characteristics became too unstable to operate as a normally-on device for thicker Al₂O₃ layers.

Future work will study MIS-HEMT structures in more detail, with wider ranges of dielectric layer compositions and thicknesses.

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