

Article

Complementary Cooperation of Dual Power Circuits to Drive Active Magnetic Bearings

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Abstract: A compact-design hybrid power amplifier for AMB (active magnetic bearing) units, named as dual cooperative drive circuit (DC²), is designed and verified by intensive experiments. DC² can operate under dual mode: either digital driving mode (DDM) or analog driving mode (ADM). By taking advantages of the complementary cooperation between DDM and ADM, the proposed DC² manifests its superiorities mainly in three aspects: (i) Compared to a traditional 2-level PWM (pulse width modulation) drive circuit, the improvement degree of reducing the amplitudes of current ripples is up to 62%. (ii) By taking advantages of mutually complementary cooperation between DDM and ADM, the steady-state errors of output current, which is exerted to the coils of AMBs, can be much reduced by DC². (iii) DC² can provide a high-current slew rate so that the response of AMBs is fast and quick enough to regulate the high-speed rotor back to the neutral position within a very short time period below 0.40 ms. In addition, the proposed DC² is particularly suitable to be compliant with digital controllers and DSPs (digital signal processors) since the commands or drive sequences are all in DC (direct current) manner.

Keywords: active magnetic bearing; rotor position regulation; drive circuit; pulse width modulation

1. Introduction

Active magnetic bearings (AMBs) are able to levitate the interested objects in a fashion of no friction and no mechanical wear. Therefore, they are increasingly applied to the industries for which high speed and high precision are both required, such as high-speed compressors, turbo-machineries, high-precision machine tools, and flywheel energy storage systems. One of the most important components of a superior AMB unit is its drive circuit. Most of the drive circuits applied to AMBs are of analog type [1,2]. Analog drive circuits possess the merits of high linearity and low electromagnetic interference (EMI). However, the energy efficiency of analog drive circuits is inherently poor because a great percentage of the energy power is converted into heat at the corresponding driver integrated circuits (ICs). By contrast, the power loss is relatively limited for the digital drive circuits. As a consequence, a large number of studies on digital drive circuits for AMBs were proposed during the past decades [3–9]. The main shortcoming of digital drive circuits to AMBs is: severe current ripple induced. The most popular technique for current ripple reduction is the so called three-level pulse width modulation (PWM) scheme [8,9]. Nevertheless, the current ripple problem by digital drive circuits is still more serious than that by analog drive circuits.

Due to analog drive circuits and digital drive circuits having their own irreplaceable advantages, many types of switch–linear hybrid (SLH) power amplifiers were proposed recently [10–14]. The SLH amplifier is mainly composed by a set of linear power amplifier and a set of two-level switching power supply. However, the switching frequency of the power supply has to be much higher than the bandwidth of the linear power amplifier. As well known, high switching frequency results in serious

switching loss [11]. Therefore, a multilevel voltage supply, namely a multilevel converter, is proposed to replace the two-level switching power supply to improve the overall energy efficiency [11–14]. Unfortunately, “multilevel” implies it requires a large number of electronic components. The reliability of the overall drive circuit would be accordingly reduced. Besides, the more complex the circuit, the larger the impact of time delay which certainly downgrades the quality of rotor dynamic control. To overcome the aforementioned shortcomings, a compact-design hybrid power amplifier for AMB units, named as dual cooperative drive circuit (DC²), is proposed by this paper. The proposed DC² is able to conduct under dual operation modes: digital driving mode (DDM) and analog driving mode (ADM). Taking advantage of the complementary characteristics of interchange between DDM and ADM, the proposed DC² manifests its superiorities on structure compactness, high energy efficiency, fast response, low tracking error and fairly mild current ripples. This work is absolutely aimed at drive circuit design. Regarding AMB position control on high-speed rotors, please refer to the previous works of the authors [15–17].

2. Design of Dual Cooperative Drive Circuit (DC²)

The conceptual design of the proposed drive circuit stems from the two-quadrant H-bridge drive circuit [18]. That is, only positive coil current is provided. The configuration of the proposed dual cooperative drive circuit (DC²) for AMBs is shown in Figure 1. Compared with the H-bridge drive circuit, merely two essential electronic components, i.e., a digital to analog converter (DAC) and an analog multiplexer (analog MUX) are additionally included by DC². The chip codes of the applied DAC and the applied analog MUX are LTC2644 and MAX4519 respectively. Their corresponding failure rates are 5.30 [19] and 3.42 [20] FITs (FIT: one failure in one billion hour) respectively at 55 °C with a 60% confidence level. Basically, DC² can operate under dual modes: digital driving mode (DDM) and analog driving mode (ADM).

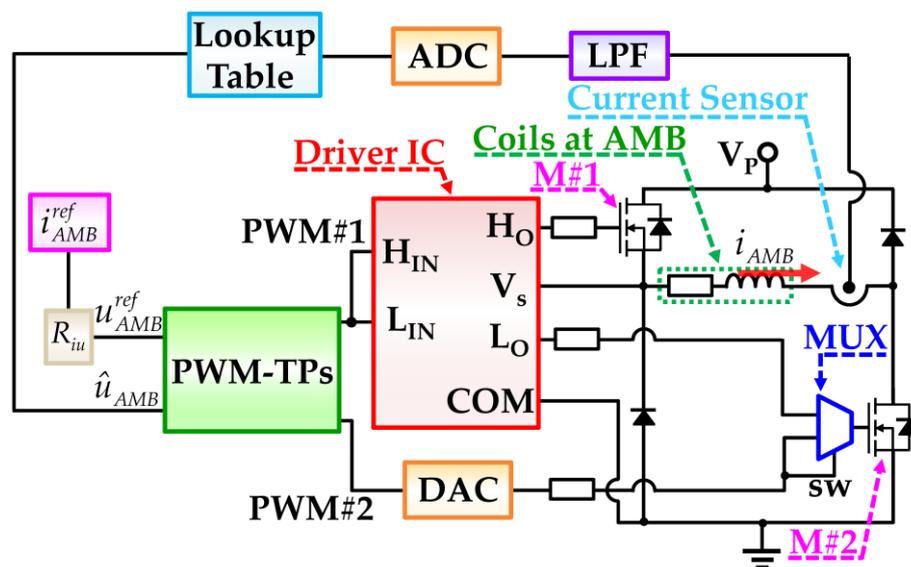


Figure 1. Circuit configuration of DC² (dual cooperative drive circuit).

2.1. Digital Drive Mode (DDM)

From the viewpoint of signal processing, the corresponding circuit under DDM is nothing but a switching power amplifier. The operation states of the switching power amplifier are illustrated in Figure 2. Under DDM, solely one PWM signal, *PWM#1*, is required to synchronously switch on/off the two MOSFETs (metal-oxide-semiconductor field-effect transistors), marked as *M#1* and *M#2* in Figure 1. When the two MOSFETs are turned on, shown in Figure 2a, the switching power amplifier operates at charge state. On the other hand, as the two MOSFETs are both turned off, shown in

Figure 2b, the power amplifier is at discharge state. Under DDM, the two MOSFETs are switched on/off synchronously so that the output current is able to track the desired output as fast as possible. Accordingly, AMBs can adjust the levitation force promptly against the external disturbance to the rotor. That is, DDM is suitable to be applied for the transient state of the rotor/AMB dynamics. As the rotor/AMB dynamics is at *steady state*, instead, ADM takes over to provide a relatively stable levitation force to the rotor, with a relatively mild ripple effect that makes the rotor wobble reciprocally.

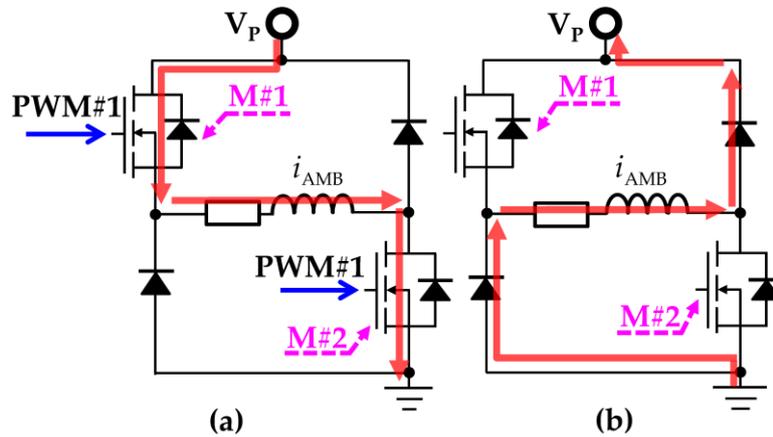


Figure 2. Operation states of DC² (dual cooperative drive circuit) under DDM (digital driving mode): (a) charge state; (b) discharge state.

2.2. Analog Drive Mode (ADM)

The corresponding operation states under ADM are depicted in Figure 3. *PWM#1* is applied to adjust the drain-source voltage of *M#2*, while *PWM#2* is utilized to regulate the AMB coil current. Under ADM, *M#2* always operates within saturation region so that its characteristics can be analogous to a voltage-controlled power transducer. As long as the MOSFET operates within its saturation region, the output current is strongly dependent of the gate-source voltage, V_{GS} . Accordingly, the digital control sequence to adjust the output current of DC², i.e., *PWM#2*, is converted into analog fashion via a digital-to-analog converter (DAC), shown in Figure 1, prior to feeding to *M#2*. It is noted that the higher the duty cycle of *PWM#1*, the more energy consumed by *M#2*. That is, a trade-off for real-time adjustment strategy is required to be included. As a result, the duty cycle of *M#1* is regulated by *PWM#1* to minimize, to some extent, the energy consumption by *M#2*.

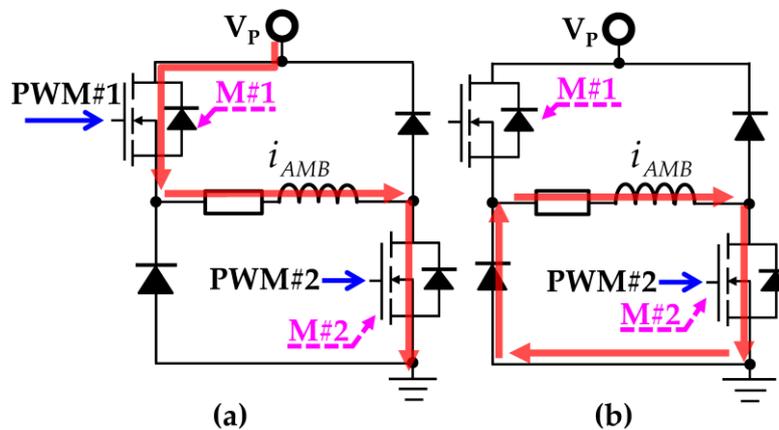


Figure 3. Operation states of DC² (dual cooperative drive circuit) under ADM (analog driving mode): (a) charge state; (b) discharge state.

3. Pulse Width Modulation-Tuning Processors (PWM-TPs)

Since DC² conducts under two distinct operation modes, i.e., DDM and ADM, two pulse width modulation-tuning processors (PWM-TPs) are required to appropriately tune the output current to the AMB unit. In addition, a switch is applied to determine the timing of change between these two modes, i.e., DDM and ADM. The schematic diagram for the PWM-TPs of DC² is shown in Figure 4. i_{AMB}^{ref} is the desired current applied to the AMB coils while u_{AMB}^{ref} is the corresponding voltage command. On the other hand, i_{AMB} is the actual current at the AMB coils, and \hat{u}_{AMB} is the converted voltage with respect to the measured current by the equipped current sensor (CS) upon i_{AMB} . If the error between u_{AMB}^{ref} and \hat{u}_{AMB} is significant, DC² operates under DDM so that the output current by DC², \hat{u}_{AMB} , can **quickly** catch up the desired quantity, u_{AMB}^{ref} . On the contrary, once the output current gets close to the desired quantity, DC² operates under ADM to generate a relatively stable current. The design procedure of PWM-TPs is shown in Figure 5.

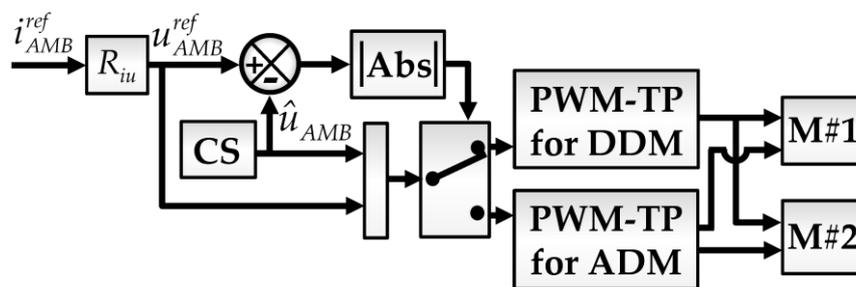


Figure 4. Schematic diagram for two PWM-TPs (pulse width modulation-tuning processors) embedded in DC² (dual cooperative drive circuit).

The steps to determine the PI gains for DDM and ADM are described by Remark#1 and Remark#2 respectively as follows:

Remark 1. *The steps to tune the PI gains for DDM*

- Step 1 Based on the step-wise reference shown in Section 4, the gain, K_p , with respect to proportional action at PI controller is tuned from zero to infinity.
- Step 2 As K_p is tuned from zero to $K_p^* = 300$, the steady-state errors are all decreased gradually. By contrast, as K_p is tuned from K_p^* to infinity, the corresponding steady-state errors are all increased gradually.
- Step 3 It is concluded that $K_p^* = 300$ is the gain finally chosen.
- Step 4 Secondly, the gain, K_I , with respect to the integral action at PI controller is tuned from zero to infinity.
- Step 5 It is observed that the steady-state errors are altered from negative values to positive values as K_I from zero to infinity while $K_p^* = 300$ is fixed. Therefore, $K_I^* = 1000$ is chosen since the corresponding steady-state errors are approximated to zero.

Remark 2. *The steps to tune the PI gains for ADM*

- Step 1 Based on the step-wise reference shown in Section 4, the gain, K_p , with respect to proportional action at PI controller is tuned from zero to infinity.
- Step 2 As K_p is tuned from zero to $\hat{K}_p = 100$, the steady-state errors are all decreased gradually. By contrast, as K_p is tuned from \hat{K}_p to infinity, the corresponding steady-state errors are all increased gradually.

- Step 3 Different from the DDM, as $\hat{K}_p \geq 95$, the tracking errors of output current would be more than the threshold such that ADM is cut off and then switched to DDM. In this case, we finally choose $K_p^* = 90$ to prevent any potential switch to DDM.
- Step 4 Secondly, the gain, K_I , with respect to the integral action at PI controller is tuned from zero to infinity.
- Step 5 It is observed that the steady-state errors are altered from negative values to positive values as K_I from zero to infinity while $K_p^* = 90$ is fixed. Therefore, $K_I^* = 300$ is chosen since the corresponding steady-state errors are approximated to zero.

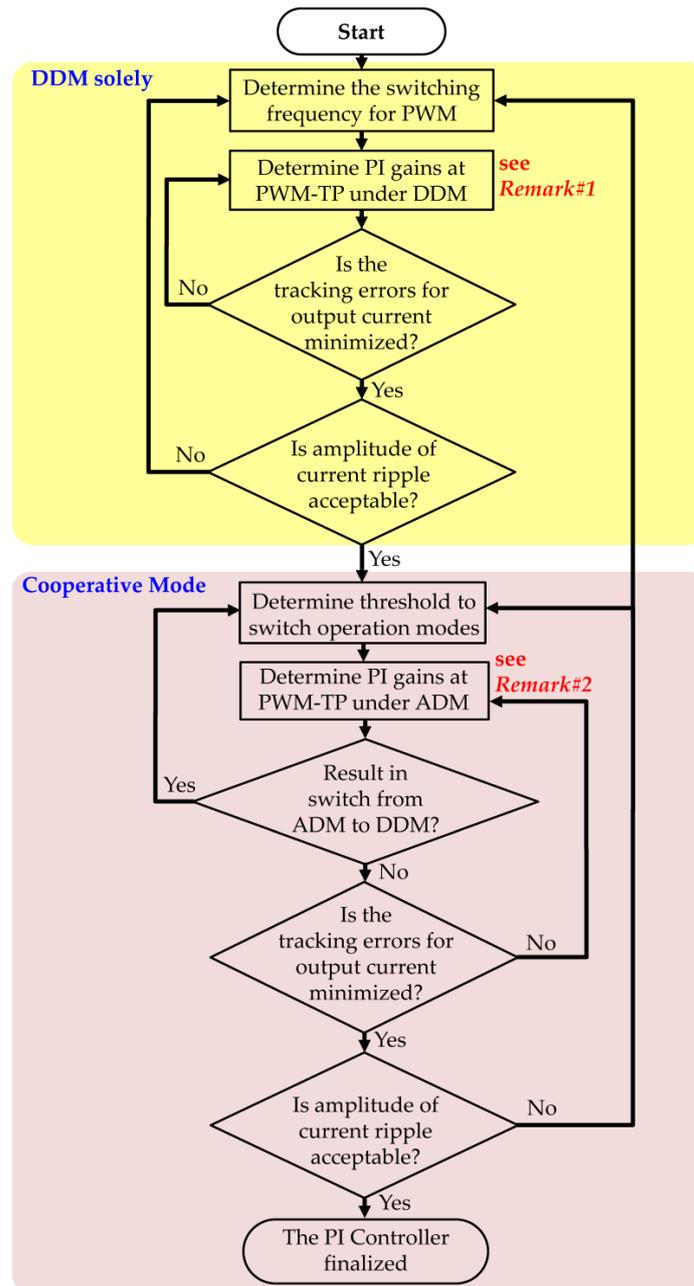


Figure 5. Design procedure of PWM-TPs (pulse width modulation-tuning processors).

3.1. PWM-TP for DDM

One of the major merits of DC² under DDM is: relatively *high* current slew rate of output current. In other words, DDM is suitable to be employed to the transient state of rotor/AMB dynamics. For there always exists high-frequency current ripples under DDM, the derivative action of a PID (Proportional–Integral–Derivative) controller would certainly downgrade the quality of output current. Therefore, instead a PI (Proportional–Integral) controller, shown in Figure 6, is employed to tune the duty ratio of *PWM#1*.

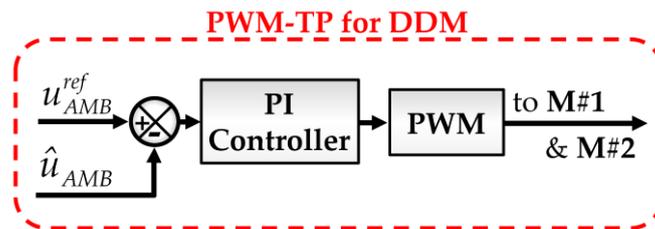


Figure 6. Schematic diagram for PWM-TP (pulse width modulation-tuning processors) under DDM (digital driving mode).

3.2. PWM-TP for ADM

Owing to one of the main shortcomings of DDM: inevitable fluctuation of the supplied voltage downgrades the quality of the output current to AMB coils, another operation mode, i.e., ADM, is joined to provide a relatively stable output current to regulate the *steady state* of rotor position. Under ADM, *M#2* operates, within saturation region, to work like a voltage-controlled power transistor. The schematic diagram for PWM-TP under ADM is depicted in Figure 7. Within the PWM-TP under ADM, a low pass filter (LPF) is employed to eliminate the current ripples lurked in the feedback signal. As a consequence, the degree of fluctuation of the feedback signal is much decreased from “*intermediate level*” down to “*mild or low level*”. Afterwards, the error between u_{AMB} and \hat{u}_{AMB} is fed to a PI controller to adjust the desired output current precisely. In addition, this error is also applied for fine-tuning the duty ratio of *PWM#1* such that the energy consumption can be reduced. Furthermore, there are two compensators, i.e., K_b^V and K_b^I shown in Figure 6, are included to adjust the duty ratios of *PWM#1* and *PWM#2* respectively.

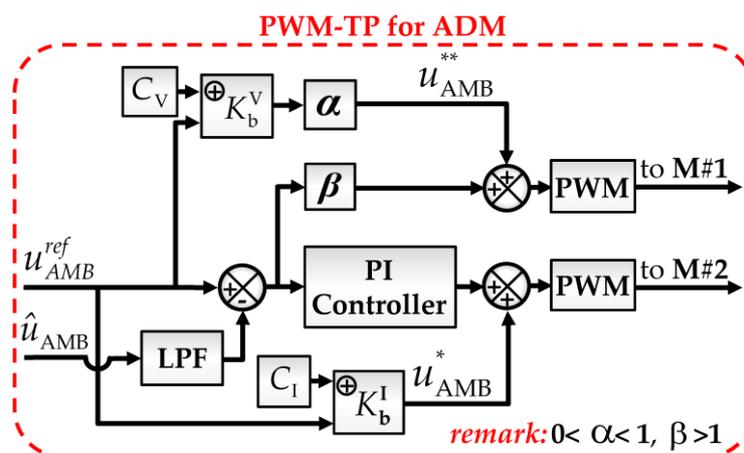


Figure 7. Schematic diagram for PWM-TP (pulse width modulation-tuning processors) under ADM (analog driving mode).

4. Simulations

In this section, intensive simulations, via MATLAB/Simulink, for PWM-TPs to track the desired output are undertaken to verify the feasibility and efficacy of DC². The Simulink model of DC² is depicted in Figure 8. The corresponding environmental setup for the simulations undertaken later is listed in Table 1. The reference signal to track, i.e., the desired output, is a step-wise function while the applied voltage to drive the AMB coil, V_P , is set as 40 VDC. i_{AMB}^{ref} is the desired current applied to the AMB coils while u_{AMB}^{ref} is the corresponding voltage command. The constant resistance with respect to u_{AMB}^{ref} over i_{AMB}^{ref} , i.e., R_{iu} in Figure 5, is set as 1 Ω . On the other hand, i_{AMB} is the actual current at the AMB coils, and \hat{u}_{AMB} is the converted voltage with respect to the measured current by the equipped current sensor (CS) upon i_{AMB} . The constant resistance for i_{AMB} converted to \hat{u}_{AMB} is set as 1 Ω . In addition, a switch is applied to determine the timing of change between these two modes, i.e., DDM and ADM. As long as the error between i_{AMB} and i_{AMB}^{ref} is over 0.03 A, then DC² is controlled by PWM-TP under DDM. On the contrary, as the error between i_{AMB} and i_{AMB}^{ref} is less than or equal to 0.03 A, then DC² is controlled by PWM-TP under ADM.

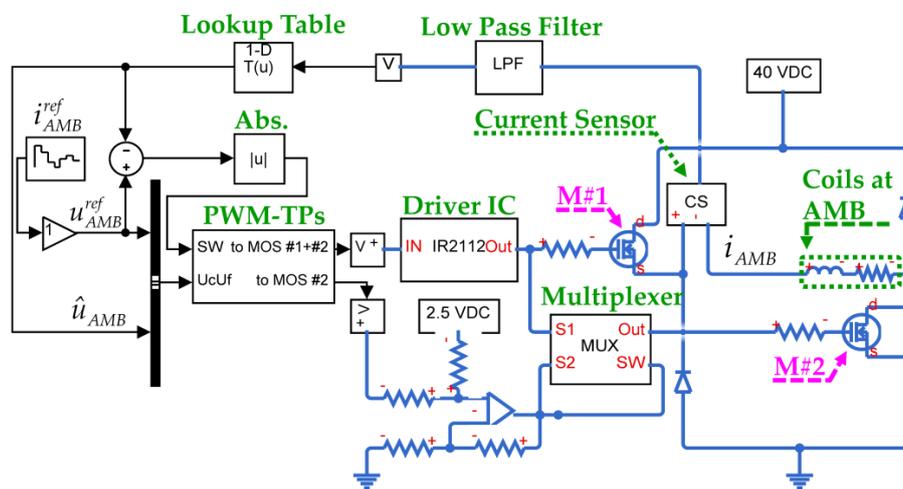


Figure 8. Simulink model of DC² (dual cooperative drive circuit).

Table 1. Environmental setup of simulations.

Parameters	Value
Solver Type	Variable-step
Numerical Method	ode23t
Supply Voltage to AMB	40 VDC
PWM Switching Frequency	22 kHz
Threshold to Switch Operation Mode	0.03 V
Threshold of Multiplexer	3.005 V
Gate-source threshold voltage of MOSFET	3 V
Drain-source on Resistance of MOSFET	0.028 Ω
Channel Modulation of MOSFET	0.01 V ⁻¹
Gate-source voltage for R_DS (on)	10 V
Drain current for R_DS (on)	75 A
Transresistance of Current Sensor	1 Ω
P Gain of PWM-TP under DDM	300
I Gain of PWM-TP under DDM	1000
P Gain of PWM-TP under ADM	90
I Gain of PWM-TP under ADM	300
Resistance of AMB coil	0.9 Ω
Inductance of AMB coil	13 mH

The desired output, i.e., i_{AMB}^{ref} shown in Figure 8, to be tracked is nothing but a sequence of step-wise signal whose quantity in ampere is limited by [0, 1.5 A]. The time duration of each step is set as 5 ms. The output currents and the tracking errors with respect to output current by two-state Power Amplifier (PA), i.e., DDM, three-state PA, DC² and ADM, are shown in Figure 9. Unfortunately, the peak-to-peak amplitudes of current ripples under DDM are up to 30 mA. Since the current ripples are too severe so that the regulation performance of AMB upon the position deviation of rotor is much downgraded, the other operation mode, namely ADM, has to be joined to reduce the aforesaid current ripples. It is noted that, under ADM, *M#2* is ensured to operate in saturation region. As *M#2* operates in its saturation region, the higher the gate-source voltage, V_{GS} , the steeper the slope of the curve of i_D versus V_{DS} . Therefore, under ADM, once V_{DS} of *M#2* is altered during a very short time interval which is taken by the on-off action of *M#1*, the lower i_D is induced, the minor current ripple would be carried by the resulted output current. From the simulation results under ADM, when i_{AMB} is 1.5 A, the amplitude of current ripples is about 7.2 mA. Moreover, the amplitude of current ripples is down to 2.1 mA as i_{AMB} is reduced to 0.01 A. On the other hand, it can be observed that steady-state errors are within [−10 mA, +5 mA] under DDM. In comparison, the steady-state errors are, all the time, within the range [0, +8 mA] under ADM. This range is about half of that under DDM. Besides, it is obvious that there are a few spikes induced at three-state PA while no any spikes at DC².

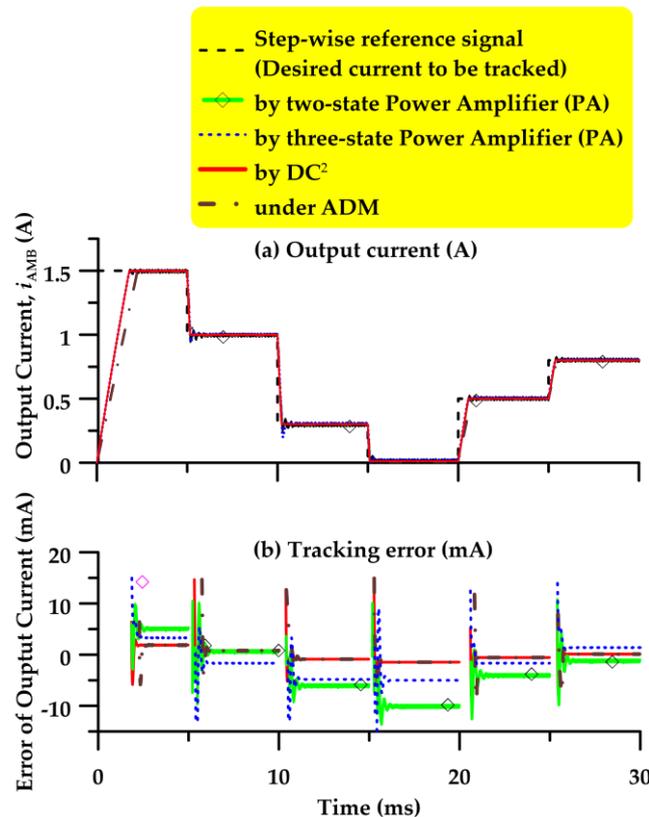


Figure 9. Output currents and tracking errors by three-state PA (power amplifier), DDM (digital driving mode), ADM (analog driving mode) and DC² (dual cooperative drive circuit).

Since DDM and ADM possess their own particular merits, DDM and ADM are combined together to constitute the proposed DC². As a matter of fact, taking advantage of joint DDM and ADM, the tracking of DC² can be further reduced to be within [−1.5 mA, +2 mA]. To be more explicitly compared, the tracking errors (TEs) by two-state power amplifier (PA), three-state PA and DC² are summarized in Table 2. As the applied current to AMB coils, i_{AMB} , is 0.01 A, the tracking error by DC² is below 15% while the tracking error by two-state PA is up to 133.6%. As i_{AMB} is much greater

than 0.01 A, the tracking by DC² is reduced to 0.3% or below. On the other hand, the comparison, in terms of the peak-to-peak amplitude of current ripple (PPACR), by a two-state PA, three-state PA and DC² is concluded by Table 3. During the kth time interval, PPACR is defined as the difference between $\max\{i_{AMB}(t), (k - 1)T_S < t \leq kT_S\}$ and $\min\{i_{AMB}(t), (k - 1)T_S < t \leq kT_S\}$ where $k \in N$, T_S is the time period and $i_{AMB}(t)$ the actual current applied to coils at AMB. The current ripples by two-state PA are over 27 mA. Compared with two-state PA, the improvement degree of reducing peak-to-peak amplitude of current ripples by DC² is up to 94% as i_{AMB} is by 0.01 A, i.e., low drive current, under steady-state circumstance. On the contrary, as i_{AMB} is by 1.5 A, i.e., high drive current, under transient-state circumstance, the improvement degree of reducing the peak-to-peak amplitude of current ripples by DC² is about 74%. Besides, the power dissipations at MOSFETs by two-state PA, three-state PA and DC² are summarized in Table 4. The power dissipation at MOSFETs is mainly determined by the applied current, on-resistance of MOSFET and the time duration at on-state, i.e., duty cycle. It is noted that the duty cycle under ADM are normally below 20% while it is usually above 75% under DDM. Therefore, though the on-resistance of M#2 under ADM is about twice of that under DDM, the power dissipation at M#2 under ADM is still lower than that under DDM. To sum up, owing to the mutually complementary cooperation between DDM and ADM, the proposed DC² at least has three outstanding advantages such as: (i) the slew rate of output current is high, (ii) the tracking errors can be greatly reduced, and (iii) the current ripples are significantly suppressed.

Table 2. Tracking errors on i_{AMB} .

A: Amplitude of i_{AMB} (A)	Two-State PA (DDM)		Three-State PA		DC ²	
	B: TE (mA)	C: Error C = B/A (%)	D: TE (mA)	E: Error E = D/A (%)	F: TE (mA)	G: Error G = F/A (%)
0.01	13.36	133.600	5.49	54.900	1.46	14.600
0.30	6.78	2.260	5.43	1.810	0.87	0.290
0.50	3.77	0.754	2.36	0.472	0.56	0.112
0.80	1.72	0.215	5.36	0.670	0.11	0.014
1.00	0.30	0.030	1.41	0.140	0.79	0.079
1.50	5.21	0.347	3.29	0.219	1.86	0.124

Note: TE is the abbreviation of "tracking error". TE is the error between the desired current and the actual output current in RMS sense.

Table 3. Peak-to-peak amplitudes of current ripples.

Amplitude of i_{AMB} (A)	Two-State PA (DDM)		Three-State PA		DC ²	
	A: PPACR (mA)	D: PPACR (mA)	E: Imp. E = (A - D)/A	F: PPACR (mA)	F: Imp. F = (A - F)/A	
0.01	31.4	2.4	92.36%	1.7	94.59%	
0.30	30.5	4.6	84.92%	3.4	88.85%	
0.50	30.0	5.5	81.67%	4.2	86.00%	
0.80	29.3	5.8	80.20%	5.2	82.25%	
1.00	28.9	6.3	78.20%	5.8	79.93%	
1.50	27.7	7.5	72.92%	7.2	74.01%	

Note: 1. "PPACR" is the abbreviation of "peak-to-peak amplitude of current ripple"; 2. "Imp." is the abbreviation of "improvement".

Table 4. Power dissipation at MOSFETs.

Amplitude of i_{AMB} (A)	Two-State PA (DDM)		Three-State PA		DC ²	
	A: PD (mW)	B: PD (mW)	C: Imp. C = (A - B)/A	D: PD (mW)	E: Imp. E = (A - D)/A	
0.01	0.02	<0.01	95.59%	<0.01	96.80%	
0.30	3.93	0.50	87.28%	0.84	78.63%	
0.50	10.99	1.68	84.71%	2.65	75.89%	
0.80	28.42	6.09	78.57%	8.15	71.32%	
1.00	44.69	11.20	74.94%	14.08	68.49%	
1.50	102.06	32.76	69.90%	39.69	61.11%	

Note: 1. "PD" is the abbreviation of "power dissipation". 2. "Imp." is the abbreviation of "improvement".

The simulation results to track the sinusoidal wave by two-state power amplifier (PA), three-state PA and DC² are shown in Figures 10 and 11. The reference signal in Figure 10 is a sinusoidal wave with 50 Hz and amplitude 0.7 A while the reference signal in Figure 11 is a sinusoidal wave with 500 Hz amplitude 0.1 A. It is noted that the current ripples are almost eliminated by DC² (see Figures 10i and 11ii). On the other hand, the tracking errors and the current ripples by DC² are very close to those by the three-state PA either for low-speed case or for high-speed case. To sum up, the performance to track a sinusoidal wave by DC² is superior to that by the three-state PA.

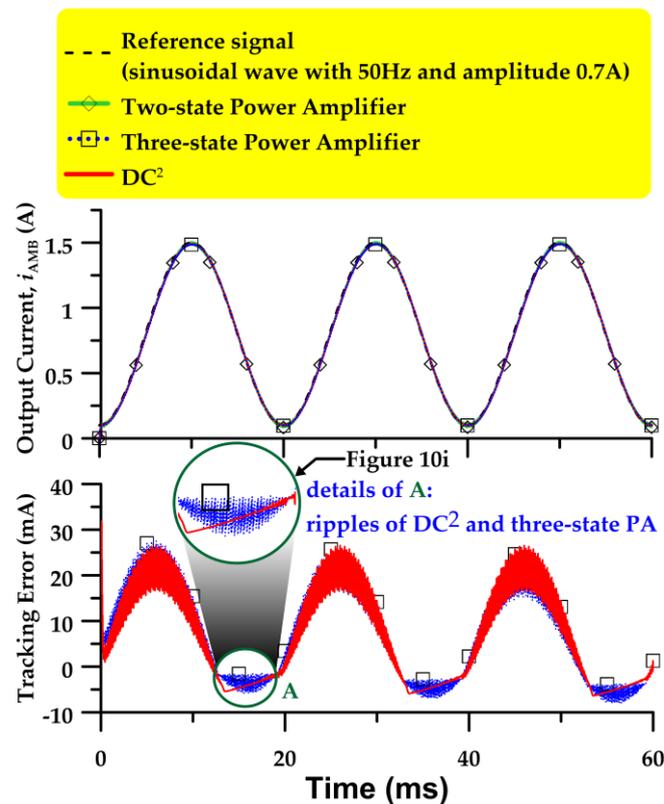


Figure 10. Output currents and tracking errors by two-state power amplifier, three-state power amplifier and DC² (dual cooperative drive circuit). (Reference signal: sinusoidal wave with 50 Hz and amplitude 0.7 A).

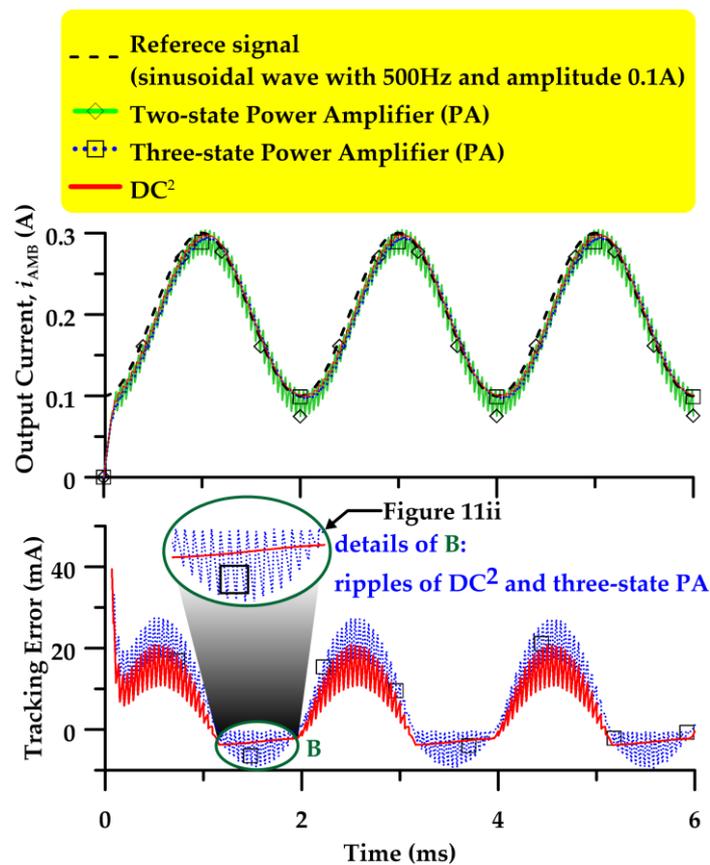


Figure 11. Output currents and tracking errors by two-state power amplifier, three-state power amplifier and DC^2 (dual cooperative drive circuit). (Reference signal: sinusoidal wave with 500 Hz and amplitude 0.1 A).

5. Experiments

In this section, a few experiments are undertaken to examine the feasibility and capability of the proposed DC^2 in terms of tracking capability and ripple suppression. The photographs and the corresponding layout of DC^2 are shown in Figure 12. Besides, the experimental test rig, shown in Figure 13, is constructed. The schematic diagram of equipped facilities to conduct the corresponding experiments is depicted in Figure 14. The load to drive, with respect to DC^2 , is any one of the axial AMBs of the 5-DOF rotor/active magnetic bearing (Rotor/AMB) unit. The electrical resistance and the nominal inductance of this AMB coil are 0.9Ω and 7 mH respectively. Two sets of direct current (DC) power supplies are employed. The first set, *ITech IT6332A*, is employed to provide +5 VDC/+15 VDC for DC^2 . The other set, *Keithley 2268-40-21*, is applied to drive the AMB coils by +40 VDC. The DSP chip, *Texas Instruments TMS320F28335*, in which the PWM-TPs are embedded, receives the signal from the CS (Current Sensor), \hat{u}_{AMB} , and then generates the two control commands, i.e., *PWM#1* and *PWM#2*, to DC^2 .

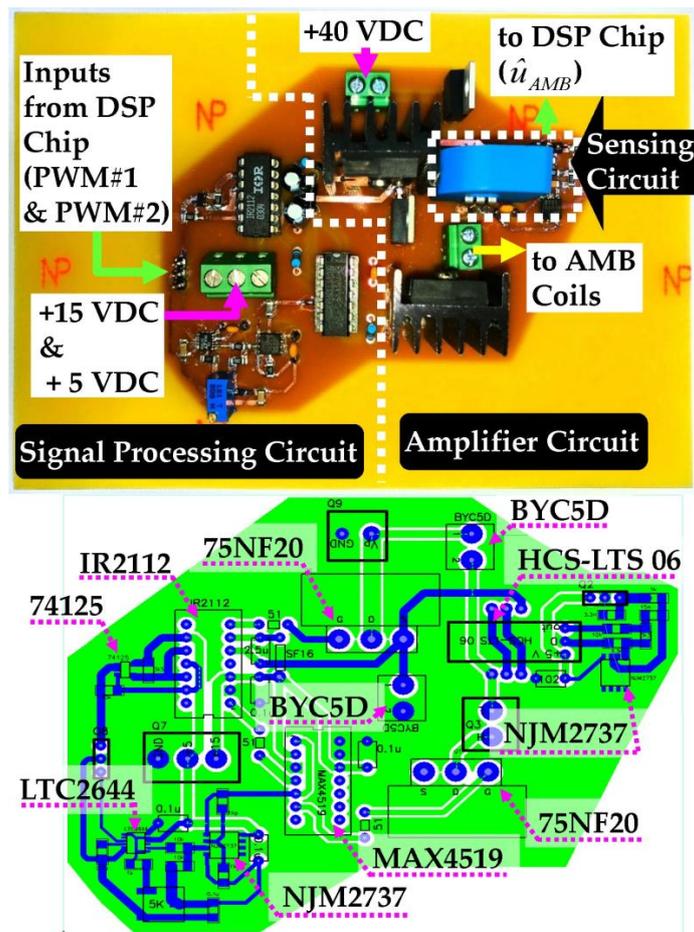


Figure 12. Photograph and layout of dual cooperative drive circuit (DC²).

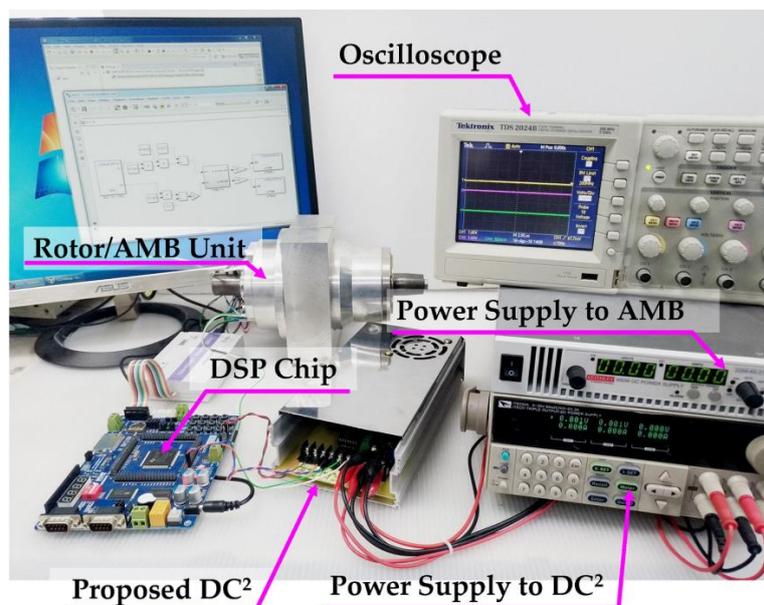


Figure 13. Experimental setup.

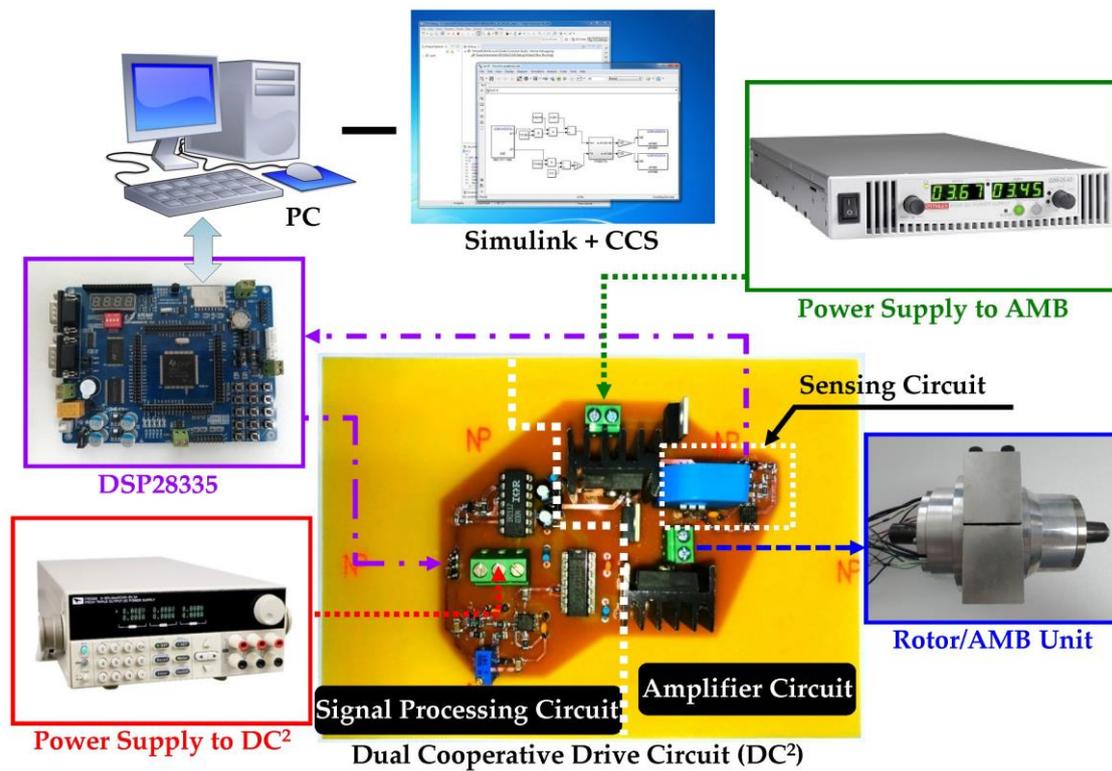


Figure 14. Schematic diagram of equipped facilities to conduct experiments.

For the experiments undertaken, the desired output, a sequence of step-wise reference, is preset at the DSP chip. The amplitude and the duty ratio of the step-wise reference are set as 1 A and 50% respectively. The tracking performances under DC², DDM and ADM are shown in Figure 15. From Figure 15, it is observed that at the beginning of tracking the step-wise signal, i.e., starting 2.5 ms to 2.8 ms, the DC² operates by DDM. Under DDM, the duty ratio of *PWM#1* (with respect to the Observation Point *OP#1*) is adjusted to its maximum, i.e., 95%, so that the output current is able to track the desired output as fast as possible. This is the major advantage of DDM at *transient state* of the rotor position regulation dynamics. However, *M#1* and *M#2* operate both in on-off fashion synchronously under DDM which unfortunately induces severe current ripples embedded in the output current. In comparison, unlike the digital control command to *M#2* under DDM (with respect to *OP#2'*), the control command to *M#2* under ADM (with respect to *OP#2*) is in analog manner. Accordingly, as soon as the output current to the axial AMB coil gets close to the desired quantity, DC² is switched to ADM. As DC² is switched from DDM to ADM, the mechanism of *M#2*, which is in on-off fashion under DDM, is changed to the voltage-controlled fashion to refine the output current more precisely. In addition, at *steady state* of the rotor position regulation dynamics, compared with the duty cycle of *PWM#1* under DDM (with respect to *OP#3'*), the duty cycle of *PWM#1* under ADM (with respect to *OP#3*) is reduced to a relatively low degree. As a result, the fluctuation of output current is greatly limited under ADM (see *OP#4*) so that the amplitudes of current ripples can be certainly much reduced.

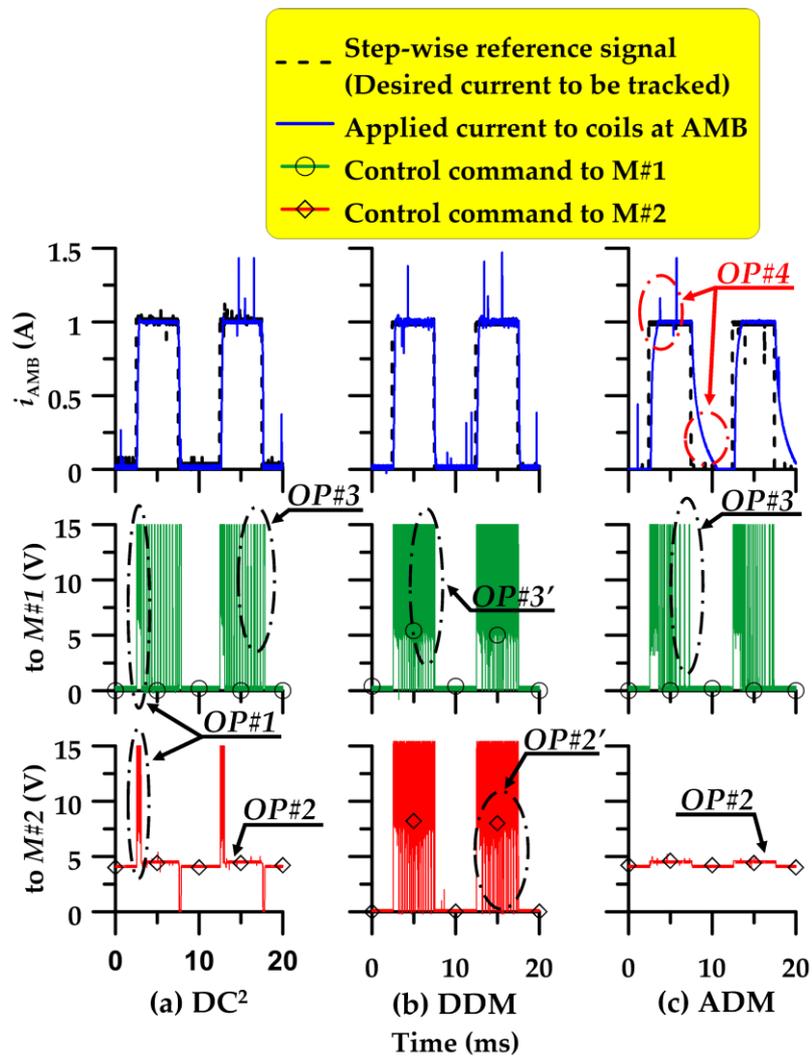


Figure 15. Experimental results: (a) under DC² (dual cooperative drive circuit); (b) under DDM (digital driving mode); (c) under ADM (analog driving mode).

To be explicitly compared, the corresponding tracking performances under DDM, ADM and DC² are depicted by Figure 16. It is observed that the rise time under ADM, i.e., 1.08 ms from A_0 to A' , is much longer than that under DDM or DC², i.e., 0.40 ms from A_0 to A . That is, the response of ADM is relatively slow so that ADM is inappropriate to be applied at *transient state*. On the other hand, the amplitude of the peak-to-peak current ripples under DDM is about 50 mA while it is merely 19 mA under either ADM or DC². That is, the improvement degree of reducing the peak-to-peak amplitude of current ripples by DC² is about 62%. This also implies that DDM is inappropriate to be applied at *steady state*. In comparison to DDM, DC² additionally possesses the merit of significantly limited current ripples. To sum up, DC² can provide a relatively stable current to drive AMB coils with a much shorter rise time and relatively minor ripples.

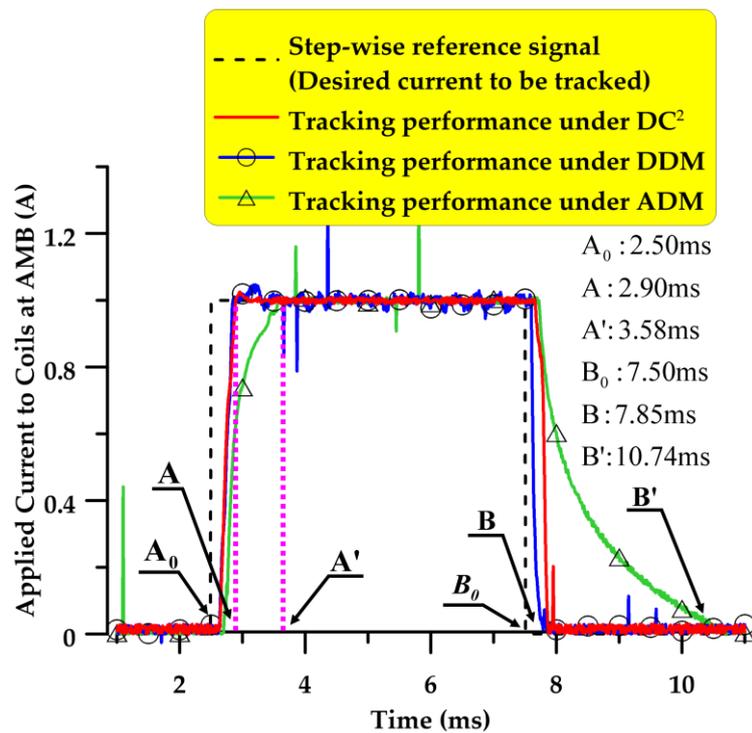


Figure 16. Tracking performances of i_{AMB} under DDM (digital driving mode), ADM (analog driving mode) and DC^2 (dual cooperative drive circuit).

6. Conclusions

A novel compact hybrid power amplifier applied for active magnetic bearings (AMBs), named as dual cooperative drive circuit (DC^2), is proposed and verified by intensive simulations and experiments. DC^2 can conduct under any of the two operation modes: digital driving mode (DDM) and analog driving mode (ADM). As the rotor/AMB dynamics is at *transient state*, DC^2 operates under DDM to provide quick energization to AMBs by higher current slew rate. On the other hand, as the rotor/AMB dynamics is at *steady state*, DC^2 operates under ADM to provide an output current with relatively mild current ripples so that the *steady state* of the rotor can be well-retained, without significant reciprocated fluctuation away from the neutral position. It is noted that all the signals fed to DC^2 are of pulse width modulation (PWM) fashion. As a matter of fact, DC^2 can be directly driven by a digital signal processing (DSP) chip. From the experimental results, compared to a traditional two-level power amplifier, the improvement degree of reducing the amplitudes of peak-to-peak current ripples is up to 62% by DC^2 . From the viewpoint of response, the rise time by DC^2 is much shorter than that under ADM. By taking advantages of complementary cooperation between DDM and ADM, the proposed DC^2 manifests its superiorities on both high current slew rate, low tracking error, and significantly suppressed current ripples.

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List of Abbreviations

ADM	Analog Driving Mode
AMB	Active Magnetic Bearing
CS	Current Sensor
DAC	Digital to Analog Converter
DC	Direct Current
DC ²	Dual Cooperative Drive Circuit
DDM	Digital Driving Mode
DSP	Digital Signal Processor
EMI	Electromagnetic Interference
Imp.	Improvement
IC	Integrated Circuit
LPF	Low Pass Filter
MUX	Multiplexer
MOSFET	Metal-Oxide-Semiconductor Field-Effect Transistor
OP	Observation Point
PA	Power Amplifier
PD	Power Dissipation
PI	Proportional–Integral
PID	Proportional–Integral–Derivative
PPACR	Peak-to-Peak Amplitude of Current Ripple
PWM	Pulse Width Modulation
PWM-TP	Pulse Width Modulation-Tuning Processor
SLH	Switch-Linear Hybrid
TE	Tracking Error

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