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Abstract: The infection countermeasure, in which the main idea is to prevent adversaries from exploiting faulty ciphertexts to break the key by spreading the induced fault, is a very effective countermeasure against fault attacks. However, most existing infection countermeasures struggle to defend against double-fault attacks effectively due to the single-fault assumption. By analyzing the principle of infection mechanism and adding different random Boolean masks in the two encryption paths, this paper proposes a measure called a random mask infection countermeasure to defend against double-fault attacks. In addition, the multiplication mask is used to randomize the fault diffusion to further resist single-byte fault attacks. The experimental results indicate that the random mask infection countermeasure proposed can perform fault diffusion effectively when the cryptographic circuit suffers double-fault attacks, and the fault diffusion shows randomness, and can effectively defend against these fault attacks.

Keywords: AES; infection countermeasure; mask; fault attacks

1. Introduction

As an international common encryption algorithm, block cipher has been widely used in security systems such as IoT, aerospace, medical, transportation, finance, and identity verification [1–5]. Since the proposal of the advanced encryption standard (AES), as an important part of block cipher, many attack methods have emerged against its hardware and software [1,5–13], and, among these attack methods, fault attacks have the advantages of high attack capability and low time complexity, which pose a great threat to the security of the AES [5,11–13].

Until now, lots of countermeasures have been proposed to resist fault attacks, which mainly fall into two categories: one type of approach is the use of fault detection [14–20], which detects the occurrence of faults by repeating the computation of some operation, module, or the entire algorithm utilizing a multiplexing technique [15,18,19], with the disadvantage that the comparison phase itself is vulnerable to new attacks. This is because the detection locations are associated with the data being processed [21]. Another type of approach is the use of fault infection [22–24]. Since the infection strategy does not need to utilize the detection locations, it effectively avoids the potential threat of being attacked due to the presence of detection locations. It spreads the fault by destroying the invariance of the fault propagation, and, thus, making the faulty ciphertext unavailable [23]. However, Lomne et al. has proved that the infection mechanism alone is not sufficient to resist fault injection attacks, and that the idea of randomization should be introduced on the basis of the infection mechanism [24]. On the basis of infection mechanism, the literature [25] advocates the method of randomly adding virtual rounds transformation and redundant calculation to resist fault attacks. However, the paper [26] pointed out that the virtual round transformation infection mechanism proposed in the literature [25] was not completely secure, and successfully attacked it. And the paper [21] further pointed out that the infection



Citation: Wu, W.; Wan, X.; Zhang, J.; Cheng, S. Research on a Random Mask Infection Countermeasure against Double Fault Attacks. *Appl. Sci.* 2023, *13*, 12530. https://doi.org/ 10.3390/app132212530

Academic Editor: Nuno Silva

Received: 4 October 2023 Revised: 3 November 2023 Accepted: 15 November 2023 Published: 20 November 2023



Copyright: © 2023 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). mechanism in [25] was deficient due to the same unknown mask being used in the infection process and improved on [25]. In 2019, by utilizing the encryption/decryption circuit, [22] proposed a countermeasure against fault attack called random infection mechanism.

Since most of these existing countermeasures against fault attacks assume a singlefault condition, i.e., the assumption that only one of the two concurrent circuits employed (e.g., two identical cryptographic circuits) will be injected with a fault [16,23], thus, it is difficult to defend against double-fault attacks. Although it is difficult to inject the same fault for both encryption or decryption paths (in other words, it is difficult to perform a double-fault attack), with the development of fault injection accuracy, it has become possible to inject double faults [27]. Fortunately, researchers have recognized the threat of double-fault attacks and have discussed them to some extent [22,24,28,29]. In [24], when analyzing existing methods against fault attacks, the possibility of doublefault attacks on cryptographic circuits is clearly stated, but no specific countermeasure is given. In 2016, Wang et al. first systematically analyzed the problem of double-fault attacks in the fault injection phase, constructed a fault injection model, and analyzed how to reduce the difficulty of double-fault injection in the fault injection phase and discussed how to effectively defend against double-fault attacks [28]. Immediately afterward, Zhang et al. proposed a fault propagation method using encrypted/decrypted circuits to resist double-fault attacks and analyzed the fault depth with good results [22]. In [29], the authors proposed the concept of a second-order infection mechanism to counter the threat of double-fault attacks by constructing a three-strip cryptographic circuit, which increases the circuit area by approximately half compared to the traditional infection mechanism due to the use of a three-strip cryptographic circuit and the risk of higher fault (e.g., triple-fault) attacks.

In view of the serious threat of fault attacks to cryptographic circuits, and the shortcomings of existing infection mechanisms against double-fault attacks, and the fact that the infection mechanism has a single diffusion mode and cannot resist single-byte faults, this paper proposes an infection countermeasure based on random masks. Furthermore, to defend against possible single-byte fault attack, a random multiplication mask also is proposed to be used in the fault diffusion to achieve the purpose of randomizing the fault diffusion. By randomly selecting a set of plaintext and key, and conducting multiple experiments, the experimental results show that when a fault is induced, even if the induced fault is the same each time, the output ciphertext of the AES circuit designed is different, and the ciphertext exhibits a certain degree of randomness; thus, it shows that our proposed countermeasure has good performance in defending against fault attacks.

Compared to existing related works, our research has the following contributions:

- A called random mask infection countermeasure is proposed. By adding a random Boolean mask in the encryption process, the AES circuit which applied the random mask infection countermeasure can resist double-fault attacks;
- (2) The countermeasure proposed can also resist single-byte exhaustive fault attacks. By using a random multiplication mask to achieve randomization of fault diffusion, the adversaries are, thus, unable to effectively retrieve the keys through exhaustive methods.

The rest of this paper is organized as follows. In Section 2, a brief introduction to the fault attack countermeasure as well as the principle and deficiency of existing infection countermeasures are presented. Section 3 provides the specific principles and design steps of the proposed countermeasure. Experimental analysis is given in Section 4. Finally, Section 5 offers the conclusion of this paper.

2. Preliminaries

2.1. Previous Fault Attack Countermeasures on the AES

Here, we briefly summarize the existing fault attack countermeasures against the AES.

(1) *Countermeasures against differential-fault attack(DFA)*. These use redundant calculations to detect whether a fault has occurred, such as [15,18,19]. Due to the comparison

phase, there is the disadvantage of vulnerability to new attacks, and other countermeasures by infecting faults were presented, for example [23–26]. Although these countermeasures can effectively defend against DFA, due to them assuming that there is only one single encryption path, they are unable to defend against double-fault attacks which are based on two encryption paths;

(2) Countermeasures against double-fault attack. In 2016, Wang et al. first analyzed how to reduce the difficulty of double-fault injection in the fault injection phase and discussed how to effectively defend against double-fault attacks [28]. Soon afterwards, Zhang et al. proposed the concept of a second-order infection mechanism, which utilized three encryption paths [29]. Due to the use of three encryption paths, it will undoubtedly significantly increase the area consumption of the circuit, and there is also a risk of being attacked by three faults. In 2019, Zhang et al. further designed encryption/decryption circuits to defend against double-fault attacks [22], for which the designed circuit adopts an entire encryption path and a partial decryption path.

2.2. Principle and Deficiency of Existing Infection Countermeasures

To perform fault attack on AES cryptographic circuits, the attacker first makes the cryptographic device perform some faulty operations by inducing faults (the precision and size of the induced faults are controllable) and, then, retrieves the key according to the faulty ciphertexts. Due to the diffusion mode of the AES being known, the adversary can accurately infer the fault spread according to the characteristics of the cryptographic algorithm, and, on this basis, derive the equation relationships between the faulty ciphertexts, correct ciphertexts, and the induced fault, and, finally, obtain the key by exhaustive search under these equation relationships.

The infection countermeasure defends against fault attacks by amplifying the impact of the injection fault so that the fault information inside the cryptographic circuit spreads over a wider area rather than simply following the flow of the AES algorithm itself, thus making it impossible for an attacker to crack the key through the faulty ciphertexts. Figure 1 depicts the principle of the existing infection countermeasure.



Figure 1. Principle of the existing infection countermeasure.

In Figure 1, the state matrices A and A' represent the intermediate states of the normal encrypted data and the encrypted data after inducing fault at the same moment in the AES circuit, respectively.

The principle of resisting a single-fault injection attack based on the countermeasure in Figure 1 is as follows: two identical plaintexts P are encrypted simultaneously via two paths, and, when a certain encryption operation is executed, the difference M is calculated between matrices A and A'. And, then, utilizing the diffusion function F (F satisfies F(0) = 0) to diffuse the difference M to obtain M', and, then, XOR A and A' with M', respectively, to achieve the purpose of diffusion fault, thus preventing the attacker from retrieving the key based on the output faulty ciphertext and correct ciphertext.

In [30], a general diffusion function *F* as shown in Expression (1) was presented, where M_{ii} is denoted as the elements of the *i*-th row and *j*-th column of a 4 × 4-byte matrix.

$$F: \mathbf{M}'_{ij} = \sum_{n=0}^{3} \mathbf{M}_{in} \oplus \sum_{m=0}^{3} \mathbf{M}_{mj}$$
(1)

A fault will spread to its row and column by the diffusion function F (as shown in Figure 1). However, there are two shortcomings in the above infection mechanisms.

The infection countermeasure above cannot effectively defend against doublefault attacks.

Double-fault attack refers to inducing a fault at the same location in each of the two paths of the circuit in Figure 1 to avoid the detection of mismatched intermediate values in the two paths.

For example, after simultaneous injection of the same fault on both encryption paths of the cryptographic device, the state matrices A = A', so that the difference between A and A' is $M = A \oplus A' = 0$. And, since the diffusion function satisfies F(0) = 0, the value M' after diffusion by F() is still 0. Then, A and A' are XOR with M', respectively. In the final output results, neither S nor S' are changed. That is, the faults induced could not be diffused by F(), and the defence against double-fault attacks by the infection mechanism in Figure 1 cannot be rendered by the infection countermeasure described in Figure 1.

The diffusion mode of F() is fixed and is not resistant to fault attacks which are based on the byte fault model.

When there is a single-byte fault attack, if F() is fixed, there will be only 256 (that is 2^8) different diffusion cases after diffusion by F(). And, then, the attacker can retrieve the key according to the idea of DFA by enumerating and analyzing these 256 possible diffusion results.

To address the problems above, what is called a random mask infection countermeasure is presented to defend against fault attacks. By using random Boolean masks and a random multiplication mask in the two encryption paths to achieve randomization of fault propagation, fault attacks such as double-fault attacks as well as single-byte fault attacks are, finally, prevented.

3. Design of a Random Mask Infection Countermeasure

The specific ideas and steps of designing the proposed random mask infection countermeasure are shown in Figure 2.



Figure 2. The design ideas and steps of the proposed random mask infection countermeasure.

As described in Figure 2, the designed countermeasure must meet two conditions, that is, it can defend against both double-fault attacks and single-byte attacks. For defending against double-fault attacks, the main idea is to prevent the attacker from inducing double faults, making it impossible to obtain effective fault information. This requires that, in the event of a fault occurring, the two encryption paths handle different data. Thus, different random masks are used on the two encryption paths. For defending against single-byte fault attacks, the main idea is to randomize the spread of faults, preventing the attacker from easily cracking the key through exhaustive methods. This is mainly achieved by designing randomized diffusion functions. Finally, by combining the two ideas above, the random mask infection countermeasure is proposed.

3.1. Prevent Double-Fault Induction

To conduct a double-fault attack, the adversary must first know the difference between the data in the two encryption paths when injecting faults, and, then, inject the corresponding faults based on the difference, so as to offset the difference between the two and bypass the infection mechanism.

As pointed out in [31], the mask cannot protect against fault attacks. However, the purpose of using the random mask here is to render attackers unable to induce double faults. Based on the idea of information hiding, this paper proposes the introduction of different random masks (N_1 and N_2) in each of the two paths of data encryption, so that the adversary cannot ascertain the difference between the data in the two paths at any moment. Thus, the attacker cannot inject the same fault into the two paths simultaneously and avoid the influence of the infection countermeasure; the specific structure is shown in Figure 2.

As shown in Figure 3, the principle of resisting double-fault attacks is: the plaintext P is encrypted by the encryption paths (1) and (2), respectively. The random number generator generates two different random numbers N_1 and N_2 (N_1 and $N_2 \neq 0$), which are XOR with P, respectively, to obtain $P_1 = P \oplus N_1$ and $P_2 = P \oplus N_2$. The register unit stores the data for unmasking in advance, and outputs the random numbers N_1' and N_1' corresponding to N_1 and N_2 for unmasking. The infection countermeasure unit implements a countermeasure of the diffusion of fault data and infects the diffused fault data back to the original encryption path (seeing Figure 1).



Figure 3. Structure of the random mask infection.

The data are encrypted in several rounds to obtain the intermediate states A_0 and A_0' . Because of the using of random mask N_1 and N_2 , it can guarantee that the values of A_0 and A_0' are two completely different values and the difference between them is randomly varying. The process of demasking is conducted by the XOR operation $A_0 \oplus N_1'$ and $A_0' \oplus N_2'$. Through the above processing, it is impossible to synchronously inject faults in A_0 and A_0' so that $(A_0 \oplus N_1') \oplus (A_0' \oplus N_2') = 0$. Because A_0 and A_0' are two completely different values and the difference between them varies randomly, the adversary can not obtain the exact difference between them.

3.2. Randomized Design of the Diffusion Function

In the infection countermeasure unit, a randomized diffusion function is designed to close the loopholes in the infection mechanism mentioned in Section 2.2 (that is, it cannot be resistant to fault attacks which are based on the byte fault model). The specific expression of the designed diffusion function F is shown in (2).

$$F : \begin{cases} M'_{il} = \sum_{n=0}^{3} M_{in} \oplus \sum_{m=0}^{3} M_{mj} \\ \theta = N_3 \cdot M'_{il} \end{cases}$$
(2)

where N₃ is the 4 × 4 random number matrix generated by the random number generator and is non-zero. *F* is the result after diffusion, also a 4 × 4 matrix. When M = 0, the value of M'_{ij} and θ = N₃. M'_{ij} equals zero and satisfies the property that the diffusion function F(0) = 0. Once M \neq 0, then the value of M'_{ij} and *F* will not be equal to zero, and θ has randomness due to N₃ being randomly varying.

Based on Figure 3 and Equation (2), what is called a random mask infection countermeasure is designed, which can both defend against double-fault attacks and avoid the disadvantage of the simplification of diffusion function mapping. In Algorithm 1, the algorithm description of this strategy is shown.

Algorithm 1: Random Mask Infection Countermeasure				
Input : plaintexts P, random masks N ₁ , M ₂ , and M ₃ (M ₁ , M ₂ , and M ₃ \neq 0)				
Output: ciphertext C.				
1) $P_1 \leftarrow P \oplus N_1, P_2 \leftarrow P \oplus N_2;$				
2) $A_0 \leftarrow Cipher(P_1), A_0' \leftarrow Cipher(P_2);$				
3) $A \leftarrow (A_0 \oplus N_1'), A' \leftarrow (A_0' \oplus N_2');$				
4) $M \leftarrow A \oplus A';$				
5) $\theta \leftarrow N_3 \cdot F(M);$				
5) $A \leftarrow A \oplus \theta, A' \leftarrow A' \oplus \theta;$				
7) $C \leftarrow Cipher(A)$.				

The flow of the entire algorithm is: the initial plaintexts of the two encryption paths are all P. We perform masking operations on P, i.e., $P_1 \leftarrow P \oplus N_1$, $P_2 \leftarrow P \oplus N_2$. Then, we encrypt the mask-transformed P_1 and P_2 , and, after several encryption operations, the intermediate states obtained are A_0 and A_0' . Subsequently, we calculate $A \leftarrow (A_0 \oplus N_1')$, $A' \leftarrow (A_0' \oplus N_2')$, and $M \leftarrow A \oplus A'$, where N_1' and N_2' are the random numbers used for demasking corresponding to the random numbers N_1 and N_2 , respectively. Then, we detect whether M is zero and if $M \neq 0$, it means that at least one of the two paths possess a fault in the encryption process, followed by the diffusion function $\theta \leftarrow N_3 \cdot F(\Delta)$ which spreads the fault to other bytes. Finally, after mixing the diffused faults into A and A', i.e., $A \leftarrow A \oplus \theta$ and $A' \leftarrow A' \oplus \theta$, we continue to perform subsequent encryption operations and output the random faulty ciphertext C. If M = 0, then the random mask infection countermeasure will not be effective, and will continue to complete subsequent encryption operations, and output the correct ciphertext C. Finally, the correct ciphertext C will be output.

4. Experimental Analysis

4.1. Verification of Against Double-Fault Attacks

The double-fault attacks assumed in this paper refer to a class of double-fault attacks that force single-bit data at the same location in two encryption paths to change to 0 or 1. To prove the effectiveness of our proposed random mask infection countermeasure to defend against double-fault attacks, the AES cryptographic circuit incorporating this countermeasure is simulated and attacked. In this paper, the AES is utilized with count mode. Assuming the location of the fault injection occurs after the R-1 (R represents the number of rounds of the AES, and for AES-128, R = 10) round of key-add operation and



before the last round (that is, the R-th round) of subbytes (SB) operation, the schematic diagram of the double-fault attack is described in Figure 4.

Figure 4. Diagram of double-fault attacks.

In Figure 4, the random mask infection countermeasure unit implements the mask, unmask, and random diffusion of fault data, and infects the diffused fault data back to the original encryption path (see Algorithm 1 for specific description). Here, we take AES-128 as an example to discuss, then Round *R*-1 is the 9th round of the AES-128 cryptographic circuit. A and A' represent the state matrix after the completion of the 9th round of encryption, N₁ and N₂ are random numbers used for the mask, and N₁ \neq N₂. Ciphertext C is the final output ciphertext of the circuit.

Select an arbitrary pair of plaintext P and key K as follows:

P: f3 01 a6 8a 9e 9f fa 50 84 45 81 d9 e2 90 d8 18

K: 09 36 3c fc c8 3d 9c a3 7f 42 7e da da b2 c2 82

The random masks N_1 and N_2 are as follows:

N₁: d0 71 6a 29 02 14 4e 9e d5 43 71 63 ec 8f af 07

N₂: d8 bf 52 38 f8 54 80 65 f4 f6 e1 ec e8 72 49 a5

When the 9th round of encryption operation ends, the intermediate states A and A' at the end of the 9th round of encryption are as follows:

A: 79 71 3f 8b 57 e3 4f 9d 93 c8 66 6b 06 4e 2e 76

A': 7b 1a 81 47 5f 45 51 d5 f9 e0 e8 23 a4 cc 50 01

The random numbers N_1' and N_2' (N_1' and N_2' correspond to N_1 and N_2 , respectively) corresponding to A and A' demasking are as follows:

N₁': ba bc 39 3f 9e 37 96 ac d2 60 d5 f4 b7 57 1b a5

N₂': b8 d7 87 f3 96 91 88 e4 b8 48 5b bc 15 d5 65 d2

When there is a fault induced in the first byte of A and A' at the same time (this means that there is a double-fault attack), then, the values of A and A' become as follow:

A: 70 71 3f 8b 57 e3 4f 9d 93 c8 66 6b 06 4e 2e 76

A': 70 1a 81 47 5f 45 51 d5 f9 e0 e8 23 a4 cc 50 01

The difference $M = (A_0 \oplus N_1') \oplus (A_0' \oplus N_2')$ at this time is:

From the above experiments, it can be seen that when the same fault is injected, $M \neq 0$, the adversary cannot bypass our countermeasure, which indicates that the random mask infection mechanism designed in this paper can defend against double-fault attacks.

4.2. Verification and Analysis of the Randomness of the Random Mask Infection Countermeasure

When there is a single-byte fault attack, taking only the first byte of the difference M between A and A' is not zero, for example. To verify the randomness of our random mask infection countermeasure, the same plaintext needs to be encrypted multiple times (here, the corresponding key is kept constant) to check whether the final output ciphertext C exhibits randomness when the input M of the diffusion function is the same. The specific results of the experiments are as follow in Table 1.

Number	Diffusion Results			
	P: f3 01 a6 8a 9e 9f fa 50 84 45 81 d9 e2 90 d8 18			
	K: 09 36 3c fc c8 3d 9c a3 7f 42 7e da da b2 c2 82 M: 02 00 00 00 00 00 00 00 00 00 00 00 00			
1	f4 5b 3b 13 63 a1 c2 c0 63 b5 d8 f6 63 a0 c2 e3			
2	22 ab 41 b0 18 e5 af 9d 30 4e 32 67 02 cd fe 43			
3	ae 3b 0e 64 c3 67 30 d5 81 66 74 9a c6 36 42 6b			
4	08 88 e4 23 d4 89 3c 67 d4 89 3c 67 d4 89 3c 67			
5	86 1e 3c 78 f0 91 53 a5 3d 7a 46 8c 69 d2 d5 db			
6	c7 ff 8e 6f de cd eb a7 3f 7e fc 89 63 c6 fd 8b			
7	7d e2 36 8c 95 9d 32 67 95 9d 32 66 95 9d 32 ce			
8	ed ab 27 4e 9c 49 92 55 aa 25 4a 94 59 b2 15 2a			
9	54 a8 21 42 84 79 f2 95 5b b6 1d 3a 74 e8 a1 33			
10	66 cc e9 a3 37 6e dc c9 e3 b7 1f 3e 7c f8 80 74			
11	e6 bd 0b 16 2c 58 b0 11 22 34 61 c2 f5 9b 47 8e			
12	6d da c5 fb 87 7f fe 8d 6b d6 dd cb e7 bf 0f 1e			
13	3c 78 f0 91 53 a6 3d 7a f4 99 43 86 7d fa 85 7b			
14	f6 9d 4b 96 5d ba 05 0a 14 28 50 a0 31 62 c4 f9			
15	83 77 ee ad 2b 56 ac 29 52 a4 39 72 e4 b9 03 06			
16	18 30 60 f1 93 57 ae 2d 5a b4 19 32 64 c8 e1 b3			
17	17 2e 5c b8 01 02 71 e2 b5 1b 34 68 d0 cf 5e 09			
18	12 24 90 51 a2 35 6a d4 d9 c3 f7 9f 4f 9e 4d 9a			
19	45 8a 65 ca e5 bb 07 0e 1c 38 70 e0 b1 13 26 4c			
20	98 41 82 75 ea a5 3b 76 ec a9 23 46 8c 69 d2 c7			

Table 1. Diffusion simulation results.

In Table 1, P and K are the input plaintext and the encryption key, respectively. M is the difference between the intermediate states A and A', and the serial numbers 1 to 20 are the output ciphertext C encrypted 20 times for plaintext P.

From Table 1, although P, K, and M are the same, the output ciphertext C for each encryption is different. In addition, the ciphertext shows uncertainty and randomness, which indicates that the diffusion effect of our countermeasure is random.

In Section 2.2 we have explained that the complexity of the diffusion function determined by the mapping relationship is 2^8 when only a single-byte fault occurs, and an attacker can exhaust the 2^8 cases to break the key. Since the countermeasure proposed is a mapping relationship constructed by a random diffusion function with randomness, its complexity is 2^{128} when a single-byte fault occurs, and it is unrealistic to crack the key by exhaustively enumerating 2^{128} cases.

4.3. Comparison of Existing Countermeasures

In Table 2, we briefly compare some countermeasures against fault attacks mentioned in this paper and indicate the possible threats that they may suffer from power attacks, as well as fault attacks, such as double-fault attacks, single-byte fault attacks, and higher fault attacks.

Papers [11,14,23,25] only assume single-fault attacks and, thus, neither can defend against double-fault attacks. Paper [11] improves the performance of AES circuits in defending against byte fault attacks by redesigning matrix for mix-column module, and successfully increases the exhaustive complexity of byte fault attacks to 2¹¹⁶. In [14], four

countermeasures were proposed, and they were all based on Hamming code and parity bits. These carry the risk of being exploited by power analysis (PA) attacks. In [25], the authors utilized randomly added virtual round transformation and redundant computation based on the infection countermeasure to resist fault attacks, but there is a risk of suffering from single-byte fault attacks because the same unknown mask is used in the infection process. The second-order infection mechanism proposed in [29], although it can defend against double-fault attacks, is subject to the risk of triple-fault attacks (three cryptographic circuits injecting the same fault at the same time) because of the use of three cryptographic circuits.

Countermeasures	Whether They Can Defend against Double-Fault Attacks (DFA)	Power Attack Threats	Fault Attack Threats
[11]	Y/N	Ν	double-fault attack
[14]	Y/N	Y	double-fault attack
[23]	Y/N	Ν	double-fault attack, single-byte fault attack
[25]	Y/N	Ν	double-fault attack, single-byte fault attack
[29]	Y/Y	Ν	triple-fault attack
This article	Y/Y	Ν	

Table 2. Comparison of existing countermeasures.

5. Conclusions

This paper addresses the shortcomings of the traditional infection countermeasure, i.e., it cannot resist double-fault attacks and the diffusion function of single mapping can be broken by exhaustive enumeration. Based on the idea of randomization, we propose a random mask infection countermeasure, which resists double-fault attacks by adding a random Boolean mask to the encryption process and using a random multiplication mask to achieve randomization of fault diffusion to further resist single-byte exhaustive fault attacks. Through theoretical analysis and experimental verification, it is shown that the AES cryptographic circuit designed using the countermeasure proposed can effectively resist fault attacks, including double-fault attacks.

When considering some other block ciphers, such as 3DES, Blowfish, IDEA, etc., due to their similar structure to the AES, the countermeasure designed in this paper can still be applied in them. The main issue to consider is how to efficiently remove the random mask after encryption. In addition to this, this paper mainly considers the defense performance of the designed countermeasure against fault attacks, without considering the area consumption. How to further improve defense performance while reducing area consumption is also the research direction of our future.

Author Contributions: Conceptualization, W.W. and J.Z.; methodology, X.W. and J.Z.; software, W.W. and X.W.; validation, W.W., J.Z. and S.C.; formal analysis, X.W., J.Z. and W.W.; resources, W.W. and J.Z.; data curation, W.W.; writing—original draft preparation, W.W. and J.Z.; writing—review and editing, X.W. and J.Z.; supervision, S.C. and J.Z.; project administration, W.W. and J.Z.; funding acquisition, J.Z. and W.W. All authors have read and agreed to the published version of the manuscript.

Funding: This research was funded by the Natural Science Research in Colleges of Jiangsu Province (grant numbers 21KJB520040 and 22KJD520006) and the Basic Science Research Project of Nantong (grant number JC2020144).

Institutional Review Board Statement: Not applicable.

Informed Consent Statement: Not applicable.

Data Availability Statement: The data presented in this study are available on request from the corresponding author. The data are not publicly available due to privacy.

Acknowledgments: The author would like to thank the anonymous reviewers for their valuable suggestions and comments that improved the quality of this paper.

Conflicts of Interest: The authors declare no conflict of interest.

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