# Error-Correction Coding Using Polynomial Residue Number System 

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#### Abstract

There has been a tendency to use the theory of finite Galois fields, or GF( 2 n ), in cryptographic ciphers (AES, Kuznyechik) and digital signal processing (DSP) systems. It is advisable to use modular codes of the polynomial residue number system (PRNS). Modular codes of PRNS are arithmetic codes in which addition, subtraction and multiplication operations are performed in parallel on the bases of the code, which are irreducible polynomials. In this case, the operands are small-bit residues. However, the independence of calculations on the bases of the code and the lack of data exchange between the residues can serve as the basis for constructing codes of PRNS capable of detecting and correcting errors that occur during calculations. The article will consider the principles of constructing redundant codes of the polynomial residue number system. The results of the study of codes of PRNS with minimal redundancy are presented. It is shown that these codes are only able to detect an error in the code combination of PRNS. It is proposed to use two control bases, the use of which allows us to correct an error in any residue of the code combination, in order to increase the error-correction abilities of the code of the polynomial residue number system. Therefore, the development of an algorithm for detecting and correcting errors in the code of the polynomial residue number system, which allows for performing this procedure based on modular operations that are effectively implemented in codes of PRNS, is an urgent task.


Keywords: residue number system (RNS); polynomial residue number system (PRNS); detecting and correcting errors; positional characteristic; polynomial interval

## 1. Introduction

Modular codes occupy a special place among arithmetical codes that allow for the maximal possible performance of computing devices. There are two types of modular codes:

- codes of residue number system (RNS);
- codes of polynomial residue number system (PRNS).

The basic principles for constructing modular codes of residue number system were first described in several books [1-4]. It was shown in these works that codes of RNS can increase the speed with which arithmetical operations (addition, subtraction and multiplication) can be performed. This is due to the fact that multi-digit integers are replaced by the corresponding tuple of residues in codes of RNS, which are obtained by dividing these operands by $n$ bases of the code, which are relatively prime numbers. At the same time, residues are processed in parallel and independently of each other on bases of the code. The works [5-7] show methods for performing modular addition operation as well as their circuit solutions. Basic principles of circuit implementations of high-speed modular multipliers are presented in [8-10].

Codes of RNS have found application in digital signal processing (DSP) systems due to their high speed. The works [11-14] present mathematical models of special DSP as well as their circuit solutions, use of which allows for orthogonal signal transformations in real time.

Mathematical models of adaptive and classical digital filters implemented in the code of RNS were developed in order to improve the quality of signal processing. The works [15,16] present algorithms for performing signal filtering, in which the use of a code of RNS makes it possible to increase the speed of the calculating filter's response. The work [17] shows methods for constructing a fault-tolerant digital filter functioning in codes of RNS. Implementation of adaptive filters using codes of residue number system is presented in [18-20]. An algorithm for choosing coefficients of a digital filter using integral arithmetic of codes of residue number system was developed in [21] and provides zero error in the response of the filter.

Codes of RNS have found application in neural systems. Neural networks using a finite ring neural network model were developed using the parallel functioning principle of neural networks and combining it with parallel principles of computation in a code of RNS [22-24]. Use of this model allowed for an increase in the speed and accuracy of neural networks.

One of the areas of effective application of codes of RNS is cryptography. As a rule, the residue number system is used in cryptographic systems with a public key. The expediency of using codes of RNS in RSA encryption algorithm is proved in the works [25-29]. Use of codes of RNS aims to create a method for fast and efficient encryption based on elliptical cryptography while ensuring high cryptographic strength of the cipher and the integrity of the transmitted information.

However, modular codes of RNS are able to increase both the speed of calculations and to detect and correct errors that occur during calculations due to failures in the operation of the computing device. The theory of constructing redundant codes of RNS is considered in detail in [2-4]. These works show fundamental differences between redundant codes of RNS and error-correction codes; $r$ additional control bases are introduced to detect and correct errors in the code of RNS, which is specified by $k$ informational bases. As a result, the code combination is expanded by $r$ residues. At the same time, control bases are equal in relation to informational bases. This is due to the fact that all residues contain information about the original integer. Therefore, control bases are involved in performing arithmetical operations as well as informational ones.

As a rule, control symbols are obtained in error-correction codes by summing modulo two of corresponding informational bits. In this case, there is no possibility of simultaneous execution of arithmetical operations on both the informational and control parts of the error-correction code.

Therefore, redundant arithmetical codes of RNS have found wide application in the construction of fault-tolerant DSP systems, as the usage of redundant arithmetical codes of RNS allows for the operability of the computing device to be maintained by detecting and correcting errors that occur during data processing. For example, the works [30-33] consider the issues with building a fault-tolerant special processor operating in codes of RNS. The issues with increasing the fault tolerance of digital filters functioning in codes of RNS are considered in [34-37].

Algorithms for detecting and correcting errors in the codes of the residue number system are based on positional characteristics (PC), which show the location of an integer represented as a redundant code combination of RNS relative to the range of allowed code combinations (the allowed range), which is determined by the product of $k$ informational bases. If the number is within this range, then the code combination does not contain an error; otherwise the code combination is erroneous.

One of the features of redundant modular codes of RNS is their multivariance of positional characteristics (PC). One of these characteristics is the number interval. Algorithms for calculating this PC, as well as their circuit implementations, are provided in [2,3,38-41].

Residues are sequentially excluded from the code combination and then compared with the allowed range in the method of projections. If all reduced code combinations (obtained during transformations) are less than the allowed range (i.e., they are within this range), then the code combination of RNS is an allowed one. Otherwise, all reduced code combinations will be greater than the allowed range except for one in which the erroneous residue is removed.

The method of the extending the bases of a code of RNS used for error detection and correction is presented in [42,43]. This method is based on the equality of informational and control residues, as they are all obtained from the original integer by calculating its residues' modulo bases of the code of RNS; $r$ control residues are calculated using $k$ informational residues. Then, the calculated residues are compared with control residues in the code combination. If they all match, then there is no error in the code combination of RNS.

In the works [44,45], the use of higher coefficients of mixed radix conversion (MRC) is proposed as a positional characteristic. This system is used to perform reverse conversion from a code of RNS to a positional code. If the higher MRC's coefficients are zero, this means that the code combination of RNS does not contain an error.

Summarizing what has been said to this point, we can conclude that the theoretical foundations for the construction of redundant codes of RNS capable of detecting and correcting calculation errors are well developed, and that is why codes of RNS have found application in the construction of fault-tolerant high-speed DSP special processors.

Codes of polynomial residue number system received their name based on the fact that irreducible polynomials are used as bases of the modular code [46-48]. It is necessary to represent an integer as a polynomial and then calculate residues of this polynomial's modulo bases of PRNS in order to obtain a code combination of PRNS. As the code is defined in the polynomial ring, there are modular operations for it (addition, subtraction and multiplication), which are performed in parallel and independently on the bases using small-bit residues [49,50]. Such principles of constructing codes of PRNS make it possible to increase the speed of calculations. Therefore, these codes are used when orthogonal transformations of signals are being performed in real time [51]. New algorithms for constructing modular multipliers, as well as their circuit implementations, are proposed in [52-54] for reducing the execution time of multiplicative operations in PRNS.

Unlike codes of RNS, codes of polynomial residue number system have found wide application in cryptography, as shown in [55-58]. The work [59] presents a technique that allows for switching from the AES encryption algorithm implemented in the finite field $\mathrm{GF}\left(2^{8}\right)$ to calculations in the ring of finite fields of a smaller order, $\mathrm{GF}\left(2^{4}\right)$. It is known that the AES block cipher is implemented in the Galois field GF $\left(2^{8}\right)$ using a generating polynomial $p(z)=z^{8}+z^{4}+z^{3}+z+1$. The present work proposes to perform all AES cryptographic transformations using two irreducible polynomials, $p_{1}(z)=z^{4}+z+1$ and $p_{2}(z)=z^{4}+z^{3}+1$. In this case, the plaintext byte will be represented as two residues, which are obtained when it is divided by the polynomials $p_{1}(z)=z^{4}+z+1$ and $p_{2}(z)=z^{4}+z^{3}+1$. As a result, the encryption process using the AES algorithm is performed using the code of polynomial residue number system, which has two informational bases. Circuit implementations of this technique are given in the works [60-63].

As failures may occur during the operation of computing devices using elements of finite Galois fields $\operatorname{GF}\left(2^{\mathrm{n}}\right)$, it becomes necessary to use codes that are able to detect and correct these errors.

Currently, error-correction BCH codes, which belong to polynomial cyclic codes, are widely used in signal processing and transmission systems. BCH codes are capable of correcting multiple errors. The construction of such codes is based on the theory of finite Galois fields GF( $\left.2^{\mathrm{n}}\right)$. Depending on the multiplicity of the error being corrected, developers determine the number of irreducible polynomials that are used to obtain the generating polynomial. For example, we can use the code $(15,11)$ and the generating polynomial $g(x)=x^{4}+x+1$ in order to correct a single error, which is understood as a distortion of one binary digit in a code combination. This code is defined over the Galois field GF $\left(2^{4}\right)$. It
is necessary to use the code $(15,7)$ in order to correct two errors in the code combination. The product of two minimal polynomials, $x^{4}+x+1$ and $x^{4}+x^{3}+1$, will provide the generating polynomial $g(x)=x^{8}+x^{7}+x^{5}+x^{4}+x^{3}+x+1$ of the considered code.

However, there is a difference between codes of PRNS and BCH codes; the latter does not allow the use of polynomial cyclic codes for detecting and correcting errors that occur during calculations. All residues in modular codes of PRNS, both informational and redundant, are obtained by dividing the original polynomial into bases of the code, which are irreducible polynomials. Because the process of obtaining the residues is the same, we can say that they are equal in relation to each other. Therefore, codes of PRNS are arithmetical codes and can be used when calculations are being performed. At the same time, arithmetical operations will take place in the same way on all bases of the code. It is the equality of residues in relation to each other that allows us to dynamically change the number of informational and control bases. For example, the following irreducible polynomials can be defined for $\mathrm{GF}\left(2^{4}\right): m_{0}(x)=x+1, m_{1}(x)=x^{4}+x+1, m_{3}(x)=x^{4}$ $+x^{3}+x^{2}+x+1, m_{5}(x)=x^{2}+x+1, m_{7}(x)=x^{4}+x^{3}+1$. If it is necessary to detect a single error, which is understood as a distortion of one residue of the code, then one base of the code can be taken as a control base, for example, $m_{7}(x)=x^{4}+x^{3}+1$. The four remaining bases of the code of PRNS will be considered informational. If a single error needs to be corrected during the calculation process, a polynomial, $m_{1}(x)=x^{4}+x+1$, can be added to the control bases. Then, the code will have three informational bases and two control bases. At the same time, in the BCH code it is not necessary to make any recalculations such as shown above with the generating polynomial.

BCH codes are not arithmetical. In BCH codes, control symbols are determined by summing modulo two corresponding information digits. In this case, informational and control bits are not equal in relation to each other. If calculations are performed in BCH codes, then the corresponding arithmetical operations can be performed only on informational bits. However, these operations cannot be performed on control bits, as the result will lead to incorrect control bits. As BCH codes are not arithmetical, they cannot be used for detection and correction of errors that occur during calculations [64,65].

However, the issues of building redundant codes of PRNS have not yet found wide application. This disadvantage hinders the expansion of the scope of application of codes of polynomial residue number system. Therefore, the objectives of the present article are:

- To conduct studies on the error-correction abilities of redundant codes of polynomial residue number system with one and two control bases.
- To develop an algorithm for detecting and correcting errors in redundant codes of PRNS based on the calculation of a positional characteristic (a polynomial interval) in which only modular operations are used, that is, without performing a reverse conversion from a code of PRNS to a positional code and then performing a division operation.


## 2. Modular Codes

Modular codes belong to the class of arithmetical codes. The name of these codes is associated with the rules of their construction: arithmetical operations are performed modulo the bases of the code. As mentioned above, modular codes are of two types, and we will consider both of them here.

### 2.1. Codes of Residue Number System

Relatively prime numbers, $m_{1}, m_{2}, \ldots, m_{n}$, are used as bases in a code of RNS. The integer $D$ is represented in a code of RNS as a tuple of residues:

$$
\begin{equation*}
X=\left(x_{1}, x_{2}, \ldots, x_{n}\right) \tag{1}
\end{equation*}
$$

where $x_{i} \equiv X \bmod m_{i}$ and $i=1,2, \ldots, n$.

The product of the bases determines the range of possible code combinations, which is called the operating range:

$$
\begin{equation*}
W_{n}=\prod_{i=1}^{n} m_{i} \tag{2}
\end{equation*}
$$

In order to unambiguously represent the integer $X$ as a code combination of RNS, the following condition must be met:

$$
\begin{equation*}
X<W_{n} \tag{3}
\end{equation*}
$$

The Chinese Remainder Theorem gives a bijection between an integer $X$ that satisfies Condition (3), along with a residues vector, $\left(x_{1}, x_{2}, \ldots, x_{n}\right)$. Thanks to this property, it is possible to perform calculations with sets of the short-length residues of long numbers rather than the long numbers themselves. At the same time, calculations for each of the modules can be performed in parallel [1-4]:

$$
\begin{equation*}
U \bullet X=\left(\left(u_{1} \bullet x_{1}\right) \bmod m_{1}, \ldots,\left(u_{n} \bullet x_{n}\right) \bmod m_{n}\right) \tag{4}
\end{equation*}
$$

where $\bullet$ are addition, subtraction, and multiplication operations, $U \equiv u_{i} \bmod m_{i}$, and $i=1,2, \ldots, n$.

Codes of RNS are able to increase the performance of arithmetic devices because calculations occur in parallel on the bases of the code and the operands have a small bit depth.

### 2.2. Codes of Polynomial Residue Number System

The bases are irreducible polynomials, $p_{i}(z), i=1, \ldots, n$, in a code of polynomial residue number system. It is necessary to convert an integer, $D$, presented in a binary number system into a polynomial, $D(z)$, prior to its conversion into the code combination of PRNS. After that, residues obtained by dividing the polynomial $D(z)$ by bases of the code of PRNS are found in [2-4]:

$$
\begin{equation*}
D(z)=\left(d_{1}(z), d_{2}(z), \ldots, d_{n}(z)\right) \tag{5}
\end{equation*}
$$

where $d_{i}(z) \equiv D(z) \bmod p_{i}(z)$ and $i=1, \ldots, n$.
The operating range of the code of polynomial residue number system is defined as

$$
\begin{equation*}
W_{n}(z)=\prod_{i=1}^{n} p_{i}(z) \tag{6}
\end{equation*}
$$

Then, using the isomorphism generated by the Chinese Remainder Theorem (CRT) in polynomials, we obtain

$$
\begin{equation*}
D(z) \bullet U(z)=\left(\left(d_{1}(z) \bullet u_{1}(z)\right) \bmod p_{1}(z), \ldots,\left(d_{n}(z) \bullet u_{n}(z)\right) \bmod p_{n}(z)\right) \tag{7}
\end{equation*}
$$

where $\bullet$ are addition, subtraction, and multiplication operations, $U(z) \equiv u_{i}(z) \bmod p_{i}(z)$, and $i=1,2, \ldots, n$.

Despite the similar principles of implementing codes of RNS and codes of PRNS, the latter have not found as wide application due to the insufficient level of development of the theoretical foundations for constructing redundant codes of polynomial residue number system that are capable of detecting and correcting errors that occur during calculations in the polynomial ring.

## 3. Theoretical Foundations of Building Redundant Codes of Polynomial Residue Number System

### 3.1. PRNS Codes with One Control Base

The basis of the construction of error-correction codes is the introduction of redundancy, which allows us to increase the total number of all possible combinations and obtain
two disjointed subsets. The first subset contains allowed code combinations, and the second contains prohibited code combinations. We will use this approach for construction of error-correction codes of polynomial residue number system.

A similar result can be achieved if the set of allowed code combinations of PRNS is reduced [2-4]. In this case, $j$ bases are chosen from $n$ bases of the code of PRNS, which will be considered redundant. Thus, there is a decrease in the number of allowed code combinations. Then, the new range of allowed code combinations of PRNS will be determined as

$$
\begin{equation*}
P_{j}(z)=\frac{W_{n}(z)}{\prod_{j} p_{j}(z)} \tag{8}
\end{equation*}
$$

where $j=1, \ldots, n$.
It can be concluded based on this equality that an increase in the number of redundant bases leads to a decrease in the subset of allowed code combinations for which $\operatorname{deg} D_{1}(z)<$ $\operatorname{deg} P_{j}(z)$. The remaining code combinations of the code of PRNS will belong to prohibited code combinations for which $\operatorname{deg} D_{2}(z) \geq \operatorname{deg} P_{j}(z)$.

This can be illustrated by an example.
Example 1. Let four bases of the code of PRNS be given as $p_{1}(z)=z^{5}+z^{4}+z^{3}+z^{2}+1$, $p_{2}(z)=z^{5}+z^{3}+z^{2}+z+1, p_{3}(z)=z^{5}+z^{2}+1, p_{4}(z)=z^{5}+z^{3}+1$. Then, the operating range of the code of polynomial residue number system is determined by Expression (6), and is equal to

$$
W_{4}(z)=\prod_{i=1}^{4} p_{i}(z)=z^{20}+z^{19}+z^{18}+z^{17}+z^{15}+z^{12}+z^{9}+z^{8}+z^{5}+z^{3}+z^{2}+z+1
$$

Take one base, $p_{4}(z)=z^{5}+z^{3}+1$, and divide the operating range $W_{4}(z)$ by it. As a result, the original operating range, $W_{4}(z)$, will be divided into two subsets, namely, allowed code combinations and prohibited code combinations, and the range of allowed code combinations will be equal to

$$
P_{4}(z)=\frac{W_{4}(z)}{p_{4}(z)}=z^{15}+z^{14}+z^{9}+z^{6}+z^{5}+z^{4}+z^{2}+z+1
$$

Thus, allowed code combinations of PRNS will be those that satisfy the condition $\operatorname{deg} D_{1}(z)<$ $\operatorname{deg} P_{4}(z)$, while the condition $\operatorname{deg} D_{2}(z) \geq \operatorname{deg} P_{4}(z)$ will be valid for prohibited code combinations of PRNS.

Take two bases, $p_{4}(z)=z^{5}+z^{3}+1$ and $p_{3}(z)=z^{5}+z^{2}+1$. This will reduce the subset of allowed code combinations:

$$
P_{3,4}(z)=\frac{W_{4}(z)}{p_{3}(z) p_{4}(z)}=z^{10}+z^{9}+z^{7}+z^{6}+z^{5}+z^{4}+z^{3}+z+1
$$

Then, allowed code combinations of PRNS will be those that satisfy the condition $\operatorname{deg} D_{1}(z)<$ $\operatorname{deg} P_{3,4}(z)$, while the condition $\operatorname{deg} D_{2}(z) \geq \operatorname{deg} P_{3,4}(z)$ will be valid for prohibited code combinations of PRNS.

The following conclusion can be drawn based on the above example: we can set a value $P_{j}(z)$ by choosing an appropriate set of $j$ bases of the modular code to determine all allowed code combinations of PRNS, and usage of $j$ redundant bases allows us to detect and correct errors that occur in code combinations of PRNS. At the same time, it is obvious that if the condition $\operatorname{deg} D_{1}(z)<\operatorname{deg} P_{j}(z)$ is met, then $\left[\frac{D_{1}(z)}{P_{j}(z)}\right]=0$, and if the condition $\operatorname{deg} D_{2}(z) \geq \operatorname{deg} P_{j}(z)$ is met, then $\left[\frac{D_{2}(z)}{P_{j}(z)}\right]>0$, where $\left[\frac{f(z)}{g(z)}\right]$ is the quotient obtained from the result of polynomial long division of polynomial $f(z)$ by polynomial $g(z)$.

Consider a special case in which only one base, $p_{i}(z)$, of the code of polynomial residue number system is used. Then,

$$
\begin{equation*}
P_{i}(z)=\frac{W_{n}(z)}{p_{i}(z)} \tag{9}
\end{equation*}
$$

Using the last equality, we obtain a set of code combinations in which residue modulo $p_{i}(z)$ is being changed. These code combinations can be presented as

$$
\begin{equation*}
D_{i R}(z)=\left(d_{1}(z), \ldots, d_{i-1}(z), R_{i}(z), d_{i+1}(z), \ldots, d_{n}(z)\right) \tag{10}
\end{equation*}
$$

where $R_{i}(z)=\left\{0,1, z, \ldots, z^{\operatorname{deg} p_{i}(z)-1}+z^{\operatorname{deg} p_{i}(z)-2}+\ldots+z+1\right\}$ are residues modulo $p_{i}(z)$.
Then, using the CRT isomorphism in polynomials, it is necessary to prove that two code combinations, $D_{i R}(z)=\left(d_{1}(z), \ldots, R_{i}(z), \ldots, d_{n}(z)\right)$ and $D_{i V}(z)=\left(d_{1}(z), \ldots\right.$, $\left.V_{i}(z), \ldots, d_{n}(z)\right)$, in which residues modulo the $i$-th base differ from each other $\left(V_{i}(z) \neq R_{i}(z)\right)$, will provide different results when they are divided by $P_{i}(z)$, or, in other words, $\left[\frac{D_{i R}(z)}{P_{i}(z)}\right]-\left[\frac{D_{i V}(z)}{P_{i}(z)}\right] \neq 0$. Fulfillment of this property is the condition that will allow us to detect and correct errors using PRNS.

Theorem 1. If we have a code of polynomial residue number system with bases $p_{1}(z), p_{2}(z), \ldots, p_{n}(z)$ where $p_{1}(z), p_{2}(z), \ldots, p_{n}(z)$ are irreducible polynomials, then for code combinations $D_{i R}(z)=\left(d_{1}(z), \ldots, d_{i-1}(z), R_{i}(z), d_{i+1}(z), \ldots, d_{n}(z)\right)$ and $D_{i V}(z)=\left(d_{1}(z), \ldots, d_{i-1}(z)\right.$, $\left.V_{i}(z), d_{i+1}(z), \ldots, d_{n}(z)\right)$ where $V_{i}(z) \neq R_{i}(z)$ and $\operatorname{deg}\left\{V_{i}(z), R_{i}(z)\right\}<\operatorname{deg} P_{i}(z)$, the following condition is met:

$$
\begin{equation*}
\left[\frac{D_{i R}(z)}{P_{i}(z)}\right]-\left[\frac{D_{i V}(z)}{P_{i}(z)}\right] \neq 0 \tag{11}
\end{equation*}
$$

where $P_{i}(z)=\frac{W_{n}(z)}{p_{i}(z)}, W_{n}(z)=\prod_{i=1}^{n} p_{i}(z)$ is the operating range of the code of PRNS, and $\mathrm{i}=1,2, \ldots, n$.

Proof of Theorem 1. According to the Chinese Remainder Theorem in polynomials, orthogonal bases are used to perform an inverse conversion from a code of PRNS to a positional code, defined as

$$
\begin{equation*}
B_{i}(z)=m_{i}(z) \frac{W_{n}(z)}{p_{i}(z)} \tag{12}
\end{equation*}
$$

where $W_{n}(z)=\prod_{i=1}^{n} p_{i}(z)$ and $m_{i}(z)$ is the weight of the $i$-th orthogonal basis such that $m_{i}(z) \frac{W_{n}(z)}{p_{i}(z)} \equiv 1 \bmod p_{i}(z)$

Then, the difference between two code combinations of PRNS $D_{i R}(z)$ and $D_{i V}(z)$ translated into a positional code is determined by

$$
\begin{equation*}
D_{i R}(z)-D_{i V}(z)=\left|R_{i}(z)-V_{i}(z)\right|_{p_{i}(z)} m_{i}(z) \frac{W_{n}(z)}{p_{i}(z)} \tag{13}
\end{equation*}
$$

where $|f(z)|_{g(z)}$ is a notation for the expression $f(z) \bmod g(z)$.
If Condition (11) is met, then the following equality is valid:

$$
\begin{equation*}
\left|R_{i}(z)-V_{i}(z)\right|_{p_{i}(z)} m_{i}(z) \frac{W_{n}(z)}{p_{i}(z)}=k(z) \frac{W_{n}(z)}{p_{i}(z)} \tag{14}
\end{equation*}
$$

where $k(z)$ is a coefficient and $k(z) \neq 0$.
By transforming (reducing) the left and right sides of the equality, we obtain

$$
\begin{equation*}
k(z)=\left|R_{i}(z)-V_{i}(z)\right|_{p_{i}(z)} m_{i}(z) \tag{15}
\end{equation*}
$$

If the weight of the orthogonal basis is $m_{i}(z)=1$, then Expression (15) will take the form

$$
\begin{equation*}
k(z)=\left|R_{i}(z)-V_{i}(z)\right|_{p_{i}(z)} \tag{16}
\end{equation*}
$$

As $V_{i}(z) \neq R_{i}(z)$ and $k(z) \neq 0$, we obtain

$$
\begin{equation*}
V_{i}(z)=\left|R_{i}(z)-k(z)\right|_{p_{i}(z)} \tag{17}
\end{equation*}
$$

Using the CRT for polynomials and converting the code combinations into a positional code, we obtain

$$
\begin{equation*}
D_{i R}(z)=\left|d_{1}(z) B_{1}(z)+\ldots+R_{i}(z) B_{i}(z)+\ldots+d_{n}(z) B_{n}(z)\right|_{W_{n}(z)} \tag{18}
\end{equation*}
$$

For the second combination,

$$
\begin{align*}
& D_{i V}(z)=\left|d_{1}(z) B_{1}(z)+\ldots+V_{i}(z) B_{i}(z)+\ldots+d_{n}(z) B_{n}(z)\right|_{W_{n}(z)}= \\
& =\left|d_{1}(z) B_{1}(z)+\ldots+\left|R_{i}(z)-k(z)\right|_{p_{i}(z)} B_{i}(z)+\ldots+d_{n}(z) B_{n}(z)\right|_{W_{n}(z)}=  \tag{19}\\
& =\left|d_{1}(z) B_{1}(z)+\ldots+R_{i}(z) B_{i}(z)+\ldots+d_{n}(z) B_{n}(z)\right|_{W_{n}(z)}-\left|k(z) B_{i}\right|_{W_{n}(z)}= \\
& =D_{i R}(z)-\left|k(z) B_{i}\right|_{W_{n}(z)}
\end{align*}
$$

Therefore, the following inequality holds:

$$
\begin{equation*}
\left[\frac{D_{i R}(z)}{P_{i}(z)}\right]-\left[\frac{D_{i V}(z)}{P_{i}(z)}\right] \neq 0 \tag{20}
\end{equation*}
$$

If the weight of the orthogonal basis is $m_{i}(z) \neq 1$, then

$$
\begin{equation*}
|R(z)-V(z)|_{p_{i}(z)}^{+}=\left|\frac{k(z)}{m_{i}(z)}\right|_{p_{i}(z)} \tag{21}
\end{equation*}
$$

As $m_{i}(z) \neq 0$ and $k(z) \neq 0$, the right side of Equality (21) is a nonzero element $\beta_{\mathrm{i}}(\mathrm{z})$, which is a residue modulo $p_{i}(z)$. Then, we obtain

$$
\begin{align*}
& D_{i V}(z)=\left|d_{1}(z) B_{1}(z)+\ldots+V_{i}(z) B_{i}(z)+\ldots+d_{n}(z) B_{n}(z)\right|_{W_{n}(z)}= \\
& =\left|d_{1}(z) B_{1}(z)+\ldots+\left|R_{i}(z)-\beta_{i}(z)\right|_{p_{i}(z)} B_{i}(z)+\ldots+d_{n}(z) B_{n}(z)\right|_{W_{n}(z)}=  \tag{22}\\
& =\left|d_{1}(z) B_{1}(z)+\ldots+R_{i}(z) B_{i}(z)+\ldots+d_{n}(z) B_{n}(z)\right|_{W_{n}(z)}-\left|\beta_{i}(z) B_{i}\right|_{W_{n}(z)}= \\
& =D_{i R}(z)-\left|\beta_{i}(z) B_{i}\right|_{W_{n}(z)}
\end{align*}
$$

Therefore, the following inequality holds:

$$
\begin{equation*}
\left[\frac{D_{i R}(z)}{P_{i}(z)}\right]-\left[\frac{D_{i V}(z)}{P_{i}(z)}\right] \neq 0 \tag{23}
\end{equation*}
$$

The theorem is proved.
Theorem 2. If an inequality

$$
\begin{equation*}
\left[\frac{D_{i R}(z)}{P_{i}(z)}\right] \neq\left[\frac{D_{i V}(z)}{P_{i}(z)}\right] \tag{24}
\end{equation*}
$$

exists for two code combinations $D_{i R}(z)$ and $D_{i V}(z)$ presented in the code of polynomial residue number system, then the following inequality holds:

$$
\begin{equation*}
R_{i}(z) \neq V_{i}(z) \bmod p_{i}(z) \tag{25}
\end{equation*}
$$

Proof of Theorem 2. The difference between two code combinations $D_{i R}(z)=\left(d_{1}(z), \ldots\right.$, $\left.R_{i}(z), \ldots, d_{n}(z)\right)$ and $D_{i V}(z)=\left(d_{1}(z), \ldots, V_{i}(z), \ldots, d_{n}(z)\right)$ is determined by the following expression:

$$
\begin{equation*}
D_{i R}(z)-D_{i V}(z)=\left|R_{i}(z)-V_{i}(z)\right|_{p_{i}(z)} m_{i}(z) \frac{W_{n}(z)}{p_{i}(z)} \tag{26}
\end{equation*}
$$

Given Inequality (24), we can conclude that the first multiplier of Expression (26) is not zero. Then,

$$
R_{i}(z) \neq V_{i}(z) \bmod p_{i}(z)
$$

The theorem is proved.
Consider the application of the theorem in the example of a code of PRNS with various bases.

Example 2. The code of PRNS is set with the following bases: $p_{1}(z)=z+1, p_{2}(z)=z^{2}+z+1$, $p_{3}(z)=z^{4}+z^{3}+z^{2}+z+1, p_{4}(z)=z^{4}+z^{3}+1, p_{5}(z)=z^{4}+z+1$. The operating range of the code is $W_{5}(z)=\prod_{i=1}^{5} p_{i}(z)=z^{15}+1$. The orthogonal bases for this modular code will have the following form:

$$
\begin{gathered}
B_{1}(z)=z^{14}+z^{13}+z^{12}+z^{11}+z^{10}+z^{9}+z^{8}+z^{7}+z^{6}+z^{5}+z^{4}+z^{3}+z^{2}+z+1 \\
B_{2}(z)=z^{14}+z^{13}+z^{11}+z^{10}+z^{8}+z^{7}+z^{5}+z^{4}+z^{2}+z \\
B_{3}(z)=z^{14}+z^{13}+z^{12}+z^{11}+z^{9}+z^{8}+z^{7}+z^{6}+z^{4}+z^{3}+z^{2}+z \\
B_{4}(z)=z^{14}+z^{13}+z^{12}+z^{11}+z^{9}+z^{7}+z^{6}+z^{3} \\
B_{5}(z)=z^{12}+z^{9}+z^{8}+z^{6}+z^{4}+z^{3}+z^{2}+z
\end{gathered}
$$

Consider the zero code combination $D(z)=(0,0,0,0,0)=0$. Let $i=2$; then, we obtain the following code combinations:

$$
\begin{gathered}
D_{2,1}(z)=(0,1,0,0,0)=z^{14}+z^{13}+z^{11}+z^{10}+z^{8}+z^{7}+z^{5}+z^{4}+z^{2}+z \\
D_{2, z}(z)=(0, z, 0,0,0)=z^{14}+z^{12}+z^{11}+z^{9}+z^{8}+z^{6}+z^{5}+z^{3}+z^{2}+1 \\
D_{2, z+1}(z)=(0, z+1,0,0,0)=z^{13}+z^{12}+z^{10}+z^{9}+z^{7}+z^{6}+z^{4}+z^{3}+z+1 \\
\text { Consider } P_{2}(z)=\frac{W_{5}(z)}{p_{2}(z)}=z^{13}+z^{12}+z^{10}+z^{9}+z^{7}+z^{6}+z^{4}+z^{3}+z+1 . \\
\text { Then, we have }\left[\frac{D_{2,0}(z)}{P_{2}(z)}\right]=0, \quad\left[\frac{D_{2,1}(z)}{P_{2}(z)}\right]=z, \quad\left[\frac{D_{2, z}(z)}{P_{2}(z)}\right]=z+1, \quad\left[\frac{D_{2, z+1}(z)}{P_{2}(z)}\right]=1 .
\end{gathered}
$$

It can be concluded based on the analysis of the application of Theorems 1 and 2 for other bases of the code that if the degree of polynomials $D(z)$ presented as code combinations of PRNS does not exceed deg $P_{i}(z)$, then $\left[\frac{D(z)}{P_{i}(z)}\right]=0$. For other polynomials, those with $\operatorname{deg} D(z) \geq \operatorname{deg} P_{i}(z)$, we have $\left[\frac{D(z)}{P_{i}(z)}\right] \neq 0$.

The theorems and examples presented above demonstrate the potential possibility of constructing redundant codes of polynomial residue number system that are capable of detecting and correcting errors that distort residues in the code combination. In order to do this, it is necessary to choose bases $p_{j}(z)$ in such a way that a polynomial of the form

$$
\begin{equation*}
P_{j}(z)=\left[\frac{W_{n}(z)}{\prod_{j} p_{j}(z)}\right] \tag{27}
\end{equation*}
$$

is obtained such that its degree is greater than the degree of the polynomial $D(z)$, on the basis of which allowed the code combinations of PRNS can be constructed. Then, the remaining polynomials presented in the code of polynomial residue number system that satisfy the condition $\left(\operatorname{deg} C(z) \geq \operatorname{deg} P_{j}(z)\right) \cap\left(\operatorname{deg} C(z)<\operatorname{deg} W_{n}(z)\right)$ form a subset of prohibited code combinations of PRNS.

It is necessary to determine minimal number of control bases introduced into the modular code in order to solve the problem of constructing a code of PRNS capable of detecting and correcting errors that occur in the residues of the code combination of PRNS. This will allow for the development of algorithms for correcting erroneous residues in the code of polynomial residue number system, which can arise due to failures in the calculation process, while introducing a minimal amount of necessary redundancy.

Minimal redundancy of the code of polynomial residue number system is achieved by introducing one control base. For an ordered bases system used in PRNS, the redundant base $p_{n+1}(z)$ must satisfy the following condition:

$$
\begin{equation*}
\operatorname{deg} p_{1}(z) \leq \operatorname{deg} p_{2}(z) \leq \ldots \leq \operatorname{deg} p_{n}(z) \leq \operatorname{deg} p_{n+1}(z) \tag{28}
\end{equation*}
$$

In this case, the range of the code is changed to the value

$$
\begin{equation*}
W_{n+1}(z)=\prod_{i=1}^{n+1} p_{i}(z)=W_{n}(z) p_{n+1}(z) \tag{29}
\end{equation*}
$$

In this case, $W_{n}(z)$ is considered the operating range of the code and $W_{n+1}(z)$ is the full range. The following rule is adopted in order to detect and correct errors in the code of polynomial residue number system. Consider polynomials represented as $A(z)=\left(\alpha_{1}(z), \alpha_{2}(z), \ldots, \alpha_{n+1}(z)\right)$ and $C(z)=\left(c_{1}(z), c_{2}(z), \ldots, c_{n+1}(z)\right)$ for which the following conditions are met:

$$
\begin{equation*}
\operatorname{deg} A(z)<\operatorname{deg} W_{n}(z), \quad \operatorname{deg} C(z)<\operatorname{deg} W_{n}(z) \tag{30}
\end{equation*}
$$

If the degree of the result of performing modular arithmetic operations (modular addition and multiplication) in PRNS, namely,

$$
\begin{equation*}
D(z)=A(z) \bullet C(z)=\left(d_{1}(z), d_{2}(z), \ldots, d_{n+1}(z)\right) \tag{31}
\end{equation*}
$$

does not exceed the degree of the operating range

$$
\begin{equation*}
\operatorname{deg} D(z)<\operatorname{deg} W_{n}(z) \tag{32}
\end{equation*}
$$

then the original code combinations and the result do not contain errors. Otherwise, the code combination of PRNS contains errors and is prohibited.

Theorem 3. If the condition

$$
\begin{equation*}
\operatorname{deg} D^{*}(z) \geq \operatorname{deg} W_{n}(z) \tag{33}
\end{equation*}
$$

is satisfied for a polynomial $D^{*}(z)$ that corresponds to a code combination of the ordered code of polynomial residue number system with bases $p_{1}(z), \ldots, p_{n}(z), p_{n+1}(z)$, then the code combination for such a polynomial in PRNS contains at least one error. In other words, $D^{*}(z)$ represents a prohibited code combination.

Proof of Theorem 3. Let a single error in the code combination of PRNS occur on the $i$-th base, where $i=1,2, \ldots, n+1$. This leads to a distortion of the residue, as follows:

$$
\begin{equation*}
d_{i}^{*}(z)=\left|d_{i}(z)+\Delta d_{i}(z)\right|_{p_{i}(z)} \tag{34}
\end{equation*}
$$

where $\Delta d_{i}(z)$ is an error depth and $\operatorname{deg}\left(\Delta d_{i}(z)\right)<\operatorname{deg} p_{i}(z)$.

Then, the erroneous combination of PRNS has the form

$$
\begin{equation*}
D^{*}(z)=\left(d_{1}(z), \ldots, d_{i}^{*}(z), \ldots, d_{n+1}(z)\right) \tag{35}
\end{equation*}
$$

By way of contradiction, assume that the polynomial $D^{*}(z)$ presented in the code of polynomial residue number system does not contain an error. Then, we use the Chinese Remainder Theorem for polynomials and perform the inverse conversion:

$$
\begin{equation*}
D^{*}(z)=\left|d_{1}(z) B_{1}(z)+\ldots+d_{i}^{*}(z) B_{i}(z)+\ldots+d_{k+1}(z) B_{k+1}(z)\right|_{W_{n+1}(z)} \tag{36}
\end{equation*}
$$

At the same time, according to Theorem 1 , there is a polynomial $D(z)=\left(d_{1}(z), \ldots\right.$, $\left.d_{i}(z), \ldots, d_{n+1}(z)\right)$ among the allowed code combinations of PRNS that differs from $D^{*}(z)$ by residue $d_{i}(z)$ on the $i$-th base. Furthermore, the condition $\operatorname{deg} D(z)<\operatorname{deg} W_{n}(z)$ holds for such a code combination.

Then, based on the CRT, the given polynomial is presented as

$$
\begin{equation*}
D(z)=\left|d_{1}(z) B_{1}(z)+\ldots+d_{i}(z) B_{i}(z)+\ldots+d_{k+1}(z) B_{k+1}(z)\right|_{W_{n+1}(z)} \tag{37}
\end{equation*}
$$

Therefore, the following equality is valid:

$$
\begin{equation*}
\left[D^{*}(z) / W_{n}(z)\right]=\left[D(z) / W_{n}(z)\right] \tag{38}
\end{equation*}
$$

Substitute Expressions (36) and (37) into Equality (38) and, taking into account the properties of orthogonal bases of redundant PRNS, that is,

$$
\begin{equation*}
B_{i}(z)=m_{i}(z) \frac{W_{n+1}(z)}{p_{i}(z)}=m_{i}(z) \frac{W_{n}(z) p_{n+1}(z)}{p_{i}(z)} \tag{39}
\end{equation*}
$$

we obtain

$$
\begin{equation*}
\frac{d_{i}^{*}(z) m_{i}(z) p_{n+1}(z)}{p_{i}(z)}=\frac{d_{i}(z) m_{i}(z) p_{n+1}(z)}{p_{i}(z)} \tag{40}
\end{equation*}
$$

Equality (40) is satisfied under the condition $d_{i}(z)=\left|d_{i}(z)+\Delta d_{i}(z)\right|_{p_{i}(z)}$, in other words, when an error depth is $\Delta d_{i}(z)=0$.

However, according to the provided information, an error occurred in the code combination of PRNS and the residue is $d_{i}^{*}(z) \neq d_{i}(z)$. Therefore, the code combination $D_{i}^{*}(z)$ contains an error on the $i$-th base of the code of polynomial residue number system and Condition (33) is valid for it.

The proof is finished.
It is obvious that Theorem 3 underlies the principles of constructing redundant codes of polynomial residue number system that are capable of detecting and correcting errors in calculations. Using this theorem, it we have shown that the distortion of any residue of the code of polynomial residue number system transforms an allowed code combination into a prohibited one. It is the violation of Condition (30) that makes it possible to detect an error in the code combination of PRNS. Consider the issue of determining the location of the error in the PRNS code combination. We can use the following theorem in order to do this.

Theorem 4. If a combination $D^{*}(z)=\left(d_{1}(z), \ldots, d_{i}^{*}(z), \ldots, d_{n+1}(z)\right)$ has a single error, $d_{i}^{*}(z)=\left|d_{i}(z)+\Delta d_{i}(z)\right|_{p_{i}(z)}$, on the $i$-th base in an ordered code of polynomial residue number system, then the polynomial interval corresponding to this error is determined by the expression

$$
\begin{equation*}
S(z)=\left[\frac{\Delta d_{i}(z) m_{i}(z) p_{k+1}(z)}{p_{i}(z)}\right] \tag{41}
\end{equation*}
$$

Proof of Theorem 4. A single error in the redundant code of polynomial residue number system means a distortion of one residue in the code combination. Accordingly, if an error occurs on the $i$-th base of the code, an erroneous polynomial, $D^{*}(z)=\left(d_{1}(z), \ldots, d_{i}^{*}(z), \ldots\right.$, $\left.d_{n+1}(z)\right)$, is obtained by adding the allowed code combination $D(z)=\left(d_{1}(z), \ldots, d_{i}(z), \ldots\right.$, $\left.d_{n+1}(z)\right)$ and $d_{i}^{*}(z)=\left|d_{i}(z)+\Delta d_{i}(z)\right|_{p_{i}(z)}$, where $\Delta d_{i}(z)$ is an error depth.

Use the CRT to convert an erroneous combination of PRNS into a positional code. Then, we have

$$
\begin{align*}
& D^{*}(z)=\left|d_{1}(z) B_{1}(z)+\ldots+d_{i}^{*}(z) B_{i}(z)+\ldots+d_{n+1}(z) B_{n+1}(z)\right|_{W_{n+1}(z)}= \\
& =\left|d_{1}(z) B_{1}(z)+\ldots+\left|d_{i}(z)+\Delta d_{i}(z)\right|_{p_{i}(z)}^{+}(z) B_{i}(z)+\ldots+d_{n+1}(z) B_{n+1}(z)\right|_{W_{n+1}(z)}= \\
& =D(z)+\left|\Delta d_{i}(z) B_{i}(z)\right|_{W_{n+1}(z)^{\prime}} \tag{42}
\end{align*}
$$

where $B_{i}(z)$ is the $i$-th orthogonal basis of PRNS.
The polynomial interval of the code combination of PRNS is determined by the expression

$$
\begin{equation*}
S(z)=\left[\frac{D(z)}{W_{n}(z)}\right] \tag{43}
\end{equation*}
$$

From the Expression (43), we can see that if $\operatorname{deg} D(z)<\operatorname{deg} W_{n}(z)$, then $S(z)=0$. Find the polynomial interval for the erroneous combination:

$$
\begin{equation*}
S(z)=\left[\frac{D^{*}(z)}{W_{n}(z)}\right]=\left[\frac{D(z)+\left|\Delta d_{i}(z) B_{i}(z)\right|_{W_{n+1}(z)}^{+}}{W_{n}(z)}\right]=\left[\frac{\left|\Delta d_{i}(z) B_{i}(z)\right|_{W_{n+1}(z)}^{+}}{W_{n}(z)}\right] \tag{44}
\end{equation*}
$$

It is known that orthogonal bases of redundant PRNS are defined as

$$
\begin{equation*}
B_{i}(z)=m_{i}(z) \frac{W_{n+1}(z)}{p_{i}(z)}=m_{i}(z) \frac{W_{n}(z) p_{n+1}(z)}{p_{i}(z)} \tag{45}
\end{equation*}
$$

Substitute Expression (45) into Equality (44). We obtain

$$
\begin{equation*}
S(z)=\left[\frac{\left|\Delta d_{i}(z) B_{i}(z)\right|_{W_{n+1}(z)}^{+}}{W_{n}(z)}\right]=\left[\frac{\Delta d_{i}(z) m_{i}(z) p_{k+1}(z)}{p_{i}(z)}\right] \tag{46}
\end{equation*}
$$

The theorem is proved.
Example 3. Consider the application of Theorem 4 for the code of polynomial residue number system with informational bases $p_{1}(z)=z+1, p_{2}(z)=z^{2}+z+1, p_{3}(z)=z^{4}+z^{3}+z^{2}+z+1$, $p_{4}(z)=z^{4}+z^{3}+1$. The operating range of the code is $W_{4}(z)=\prod_{i=1}^{4} p_{i}(z)=z^{11}+z^{8}+z^{7}+$ $z^{5}+z^{3}+z^{2}+z+1$. Here, we select $p_{5}(z)=z^{4}+z+1$ as a control base.

Consider $D(z)=z^{5}=\left(1, z+1,1, z^{3}+z+1, z^{2}+z\right)$. The code combination for this polynomial is allowed, as $\operatorname{deg} D(z)<\operatorname{deg} W_{4}(z)$.

Let the error occur in the residue $d_{1}(z)$ and let the error depth be $\Delta d_{1}(z)=1$. Then, the erroneous combination is $D^{*}(z)=\left(0, z+1,1, z^{3}+z+1, z^{2}+z\right)$. Using Expression (41), the weight of the orthogonal basis is $m_{1}(z)=1$; thus, we obtain

$$
S(z)=\left[\frac{\Delta d_{1}(z) m_{1}(z) p_{5}(z)}{p_{1}(z)}\right]=\left[\frac{z^{4}+z+1}{z+1}\right]=z^{3}+z^{2}+z
$$

Calculating the polynomial interval using the CRT for polynomials and using the orthogonal bases from Example 2, we have

$$
D^{*}(z)=z^{14}+z^{13}+z^{12}+z^{11}+z^{10}+z^{9}+z^{8}+z^{7}+z^{6}+z^{4}+z^{3}+z^{2}+z+1
$$

In this case, $S(z)=\left[\frac{D^{*}(z)}{W_{n}(z)}\right]=z^{3}+z^{2}+z$.
It is possible to identify both a location of the erroneous residue of the code combination and an error depth using Theorems 1-3. It is necessary to find the corresponding polynomial interval in order to do this. Due to the minimal introduced redundancy, a collision may occur in the code of PRNS with one control base. In this case, errors on informational bases and errors on control bases may produce the same value of the polynomial interval. Let the error occur in the residue $d_{5}(z)$ and let the error depth be $\Delta d_{5}(z)=z^{2}+z+1$. Then, we have $D^{*}(z)=\left(1, z+1,1, z^{3}+z+1,1\right)$. Using Expression (41), the weight of the orthogonal basis is $m_{5}(z)=z$; thus, we obtain

$$
S(z)=\left[\frac{\Delta d_{1}(z) m_{1}(z) p_{5}(z)}{p_{5}(z)}\right]=\left[\left(z^{2}+z+1\right) z\right]=z^{3}+z^{2}+z
$$

We have obtained a polynomial interval that coincides with the polynomial interval in the example. This is due to the fact that the number of possible polynomial intervals with one control base is equal to

$$
\begin{equation*}
N_{1}=2^{\operatorname{deg} p_{n+1}(z)} \tag{47}
\end{equation*}
$$

At the same time, the number of polynomial intervals that correspond to errors on the bases of the code of PRNS is determined by

$$
\begin{equation*}
N_{2}=\sum_{i=1}^{n+1} 2^{\operatorname{deg} p_{i}(z)} \tag{48}
\end{equation*}
$$

It is obvious that $N_{1}<N_{2}$. Therefore, codes of polynomial residue number system with one control base are only able to detect calculation errors. It is possible to eliminate this collision by increasing the introduced redundancy in PRNS.

### 3.2. Codes of Polynomial Residue Number System with Two Control Bases

Consider an ordered code of PRNS with informational bases $p_{1}(z), p_{2}(z), \ldots, p_{n}(z)$ for which the following expression takes place:

$$
\begin{equation*}
\operatorname{deg} p_{1}(z) \leq \operatorname{deg} p_{2}(z) \leq \ldots \leq \operatorname{deg} p_{n}(z) \tag{49}
\end{equation*}
$$

Now, we introduce two control bases $p_{n+1}(z), p_{n+2}(z)$ into this code that satisfy the following condition:

$$
\begin{equation*}
\operatorname{deg} p_{n+1}(z)+\operatorname{deg} p_{n+2}(z) \geq \operatorname{deg} p_{n}(z)+\operatorname{deg} p_{n-1}(z) \tag{50}
\end{equation*}
$$

This will expand the full range of the code to

$$
\begin{equation*}
W_{n+2}(z)=\prod_{i=1}^{n+2} p_{i}(z)=W_{n}(z) p_{n+1}(z) p_{n+2}(z)=W_{n}(z) W_{k}(z) \tag{51}
\end{equation*}
$$

where $W_{k}(z)=p_{n+1}(z) p_{n+2}(z)$.
An increase in redundancy in PRNS leads to an increase in the number of polynomial intervals. In this case,

$$
\begin{equation*}
N_{1}=2^{\operatorname{deg}\left(p_{n+1}(z) p_{n+2}(z)\right)} \tag{52}
\end{equation*}
$$

Here, the number of polynomial intervals that correspond to errors on bases of the code of PRNS is defined as

$$
\begin{equation*}
N_{2}=\sum_{i=1}^{n+2} 2^{\operatorname{deg} p_{i}(z)} \tag{53}
\end{equation*}
$$

The analysis of expressions shows that $N_{1}>N_{2}$. Therefore, codes of polynomial residue number system with two control bases have the potential to correct single errors, which are understood as distortion of one residue in the code combination.

As codes of PRNS are modular codes, it is possible to use existing approaches to the development of algorithms designed to detect and correct errors; in other words, the procedure is the same as when codes of residue number system are used. It is proposed in $[2-4]$ to use positional characteristics to construct error-correction codes of residue number system. It is known from [3] that the code combination of a number is allowed in codes of RNS if the following condition is met:

$$
\begin{equation*}
X=\left(x_{1}, \ldots, x_{i}, \ldots, x_{n+2}\right)<W_{n} \tag{54}
\end{equation*}
$$

where $x_{i} \equiv X \bmod p_{i}$ and $W_{n}=\prod_{i=1}^{n} p_{i}$ is the operating range of the code of RNS.
At the same time, the full range of a redundant code of RNS with two control bases is

$$
\begin{equation*}
W_{n+2}=\prod_{i=1}^{n+2} p_{i}=W_{n} p_{n+1} p_{n+2}=W_{n} W_{k} \tag{55}
\end{equation*}
$$

where $W_{k}=p_{n+1} p_{n+2}$.
The positional characteristic shows the location (position) of the number presented as the code combination of RNS relative to the operating range [2-4]. It is obvious that there are $W_{k}=p_{n+1} p_{n+2}$ operating ranges $W_{n}$ within the full range $W_{n+2}$. If the numbers inside the full range are divided by the value $W_{n}$ and the quotient of the result is taken, we obtain a positional characteristic, that is, the interval [5], equal to

$$
\begin{equation*}
S=\left[\frac{X}{W_{n}}\right] \tag{56}
\end{equation*}
$$

If the code combination of RNS satisfies Expression (54), then the interval is equal to zero. If the number is outside the operating range, which corresponds to prohibited code combinations of RNS, then the value of the interval is in the range from 1 to $W_{k}-1$. This means that the interval $S$ depends on the modulus of the product of the control bases, $W_{k}$.

Through analysis of Expressions (43) and (56) we are able to conclude that there are similar approaches to the development of algorithms for detecting and correcting errors in codes of RNS and codes of polynomial residue number system. Hence, we can develop an algorithm for calculating the polynomial interval without using the division operation, as this operation is absent in the ring of irreducible polynomials.

Consider a code combination, $D(z)=\left(d_{1}(z), \ldots, d_{i}(z), \ldots, d_{n+2}(z)\right)$, presented in the code of PRNS with two control bases. Performing the inverse conversion using the CRT for polynomials,

$$
\begin{equation*}
D(z)=\sum_{i=1}^{n+2} d_{i}(z) B_{i}(z)-k(z) W_{n+2}(z) \tag{57}
\end{equation*}
$$

where $k(z)=\left[\frac{d_{1}(z) B_{1}(z)+\ldots+d(z) B_{i}(z)+\ldots+d_{n+2}(z) B_{n+2}(z)}{W_{n+2}(z)}\right]$ is a rank of the polynomial.

Substituting this expression in (56), we obtain:

$$
\begin{equation*}
S(z)=\left[\frac{D(z)}{W_{n}(z)}\right]=\left[\frac{\sum_{i=1}^{n+2} d_{i}(z) B_{i}(z)-k(z) W_{n+2}(z)}{W_{n}(z)}\right] \tag{58}
\end{equation*}
$$

The analysis in works [3-5] showed that both orthogonal bases of codes of RNS containing redundant bases and those without them have the similarity property. The conducted studies have confirmed this property for codes of polynomial residue number system. Thus, we have

$$
\begin{equation*}
\dddot{B}_{i}(z) \equiv B_{i}(z) \bmod W_{n}(z) \tag{59}
\end{equation*}
$$

where $\dddot{B}_{i}(z)$ is an orthogonal basis of PRNS using only information bases $p_{1}(z), p_{2}(z), \ldots$, $p_{n}(z)$, and $B_{i}(z)$ is an orthogonal base of redundant PRNS.

Hence, orthogonal bases can be presented as

$$
\begin{equation*}
B_{i}(z)=W_{n}(z) L_{i}(z)+\dddot{B}_{i}(z) \tag{60}
\end{equation*}
$$

where $L_{i}(z)=\left[\frac{B_{i}(z)}{W_{n}(z)}\right]$.
At the same time, for control bases we have $\dddot{B}_{n+1}(z)=0, \dddot{B}_{n+2}(z)=0$.
Then, using (47), we obtain

$$
\begin{align*}
& S(z)=\left[\frac{\sum_{i=1}^{n+2} d_{i}(z)\left(W_{n}(z) L_{i}(z)+\dddot{B}_{i}(z)\right)-k(z) W_{n+2}(z)}{W_{n}(z)}\right]=  \tag{61}\\
& =\left[\sum_{i=1}^{n+2} d_{i}(z) L_{i}(z)+\frac{\sum_{i=1}^{n} d_{i}(z) \dddot{B}_{i}(z)}{W_{n}(z)}-k(z) W_{k}(z)\right] .
\end{align*}
$$

When RNS was considered, it was shown that the values of the interval $S$ form a ring modulo $W_{k}$. Then, based on the similarity of RNS and PRNS we can conclude that values of the polynomial interval $S(z)$ do not exceed values modulo $W_{k}(z)$ either. In this case, we obtain

$$
\begin{align*}
& S(z)=\left\lvert\, \frac{\sum_{i=1}^{n+2} d_{i}(z) L_{i}(z)+\left[\frac{\sum_{i=1}^{n} d_{i}(z) \dddot{B}_{i}(z)}{W_{n}(z)}\right]-\left.k(z) W_{k}(z)\right|_{W_{k}(z)}=}{=\left|\sum_{i=1}^{n+2} d_{i}(z) L_{i}(z)+\dddot{k}(z)\right|_{W_{k}(z)},}\right. \text {, } \tag{62}
\end{align*}
$$

where $\dddot{k}(z)=\left[\frac{\sum_{i=1}^{n} d_{i}(z) \dddot{B}_{i}(z)}{W_{n}(z)}\right]$ is a rank of the polynomial in a non-redundant polynomial residue number system with bases $p_{1}(z), p_{2}(z), \ldots, p_{n}(z)$.

Thus, an algorithm has been developed for calculating the positional characteristic (a polynomial interval). Only modular operations are used in this algorithm, which allows us to detect and correct errors without performing a reverse conversion from the code of PRNS to a positional code or performing a division operation. Given that the calculations in PRNS are performed in parallel and independently on bases of the code, the error that occurred during the calculations on the $i$-th base will lead to a distortion of the final result only in the $i$-th residue. Therefore, the process of error detection and correction can be performed in parallel with conversion from the code of polynomial residue number system to the positional code, which reduces time costs.

Example 4. Using PRNS with three informational bases, $p_{1}(z)=z+1, p_{2}(z)=z^{2}+z+1$, and $p_{3}(z)=z^{4}+z^{3}+z^{2}+z+1$, and two control bases, $p_{4}(z)=z^{4}+z^{3}+1$ and $p_{5}(z)=z^{4}+z+1$, the operating range of the code is $W_{3}(z)=\prod_{i=1}^{3} p_{i}(z)=z^{7}+z^{6}+z^{5}+z^{2}+z+1$. The product of the two control bases is $W_{k}=p_{n+1} p_{n+2}=z^{8}+z^{7}+z^{5}+z^{4}+z^{3}+z+1$. The full range of PRNS is $W_{5}(z)=\prod_{i=1} p_{i}(z)=z^{15}+1$.

As an allowed code combination of PRNS, we choose a polynomial, $D(z)=z^{5}+z^{4}+z+1=$ ( $\left.0, \quad z, z^{3}+z^{2}+1,1, z^{2}+z\right)$, which satisfies Condition (32). The orthogonal bases of PRNS can be calculated as follows:

$$
\begin{gathered}
B_{1}(z)=z^{14}+z^{13}+z^{12}+z^{11}+z^{10}+z^{9}+z^{8}+z^{7}+z^{6}+z^{5}+z^{4}+z^{3}+z^{2}+z+1 \\
B_{2}(z)=z^{14}+z^{13}+z^{11}+z^{10}+z^{8}+z^{7}+z^{5}+z^{4}+z^{2}+z \\
B_{3}(z)=z^{14}+z^{13}+z^{12}+z^{11}+z^{9}+z^{8}+z^{7}+z^{6}+z^{4}+z^{3}+z^{2}+z \\
B_{4}(z)=z^{14}+z^{13}+z^{12}+z^{11}+z^{9}+z^{7}+z^{6}+z^{3} \\
B_{5}(z)=z^{12}+z^{9}+z^{8}+z^{6}+z^{4}+z^{3}+z^{2}+z
\end{gathered}
$$

The bases can then be divided into the operating range until the residue is obtained, thusly:

$$
\begin{gathered}
L_{1}(z)=z^{7}+z^{4}+z^{2}+z ; \dddot{B}_{1}(z)=z^{6}+z^{4}+z^{3}+z^{2}+1 \\
L_{2}(z)=z^{7}+z^{5}+z^{2}+z+1 ; \dddot{B}_{2}(z)=z^{6}+z^{5}+z+1 \\
L_{3}(z)=z^{7}+z^{4}+z^{3}+z+1 ; \dddot{B}_{3}(z)=z^{5}+z^{4}+z^{3}+z^{2}+z+1 \\
L_{4}(z)=z^{7}+z^{4}+z^{3} \\
L_{5}(z)=z^{5}+z^{4}+z
\end{gathered}
$$

Calculate the polynomial interval of the code combination of PRNS.
Now, obtain the value of the rank of the polynomial in non-redundant PRNS:

$$
\dddot{k}(z)=\left[\frac{z\left(z^{6}+z^{5}+z+1\right)+\left(z^{3}+z^{2}+1\right)\left(z^{5}+z^{4}+z^{3}+z^{2}+z+1\right)}{z^{7}+z^{6}+z^{5}+z^{2}+z+1}\right]=z
$$

This value is substituted in

$$
\begin{aligned}
& S(z)=\left|\sum_{i=1}^{5} d_{i}(z) L_{i}(z)+\dddot{k}(z)\right|_{W_{k}(z)}=\mid z\left(z^{7}+z^{5}+z^{2}+z+1\right)+\left(z^{3}+z^{2}+1\right) \times \\
& \times\left(z^{7}+z^{4}+z^{3}+z+1\right)+\left(z^{7}+z^{4}+z^{3}\right)+\left(z^{2}+z\right)\left(z^{5}+z^{4}+z\right)+\left.z\right|_{W_{k}}= \\
& =\left(z^{10}+z^{9}+z^{8}+z^{6}+z^{4}+z^{2}+z+1\right) \bmod z^{8}+z^{7}+z^{5}+z^{4}+z^{3}+z+1=0
\end{aligned}
$$

As the polynomial interval is equal to zero, it means that the code combination of PRNS does not contain an error.

Let the error occur on the base $p_{1}(z)$ and the error depth be $\Delta d_{1}(z)=1$. Then, the code combination has the form

$$
\begin{aligned}
& D^{*}(z)=\left(1^{*}, z, z^{3}+z^{2}+1,1, z^{2}+z\right)= \\
& =z^{14}+z^{13}+z^{12}+z^{11}+z^{10}+z^{9}+z^{8}+z^{7}+z^{6}+z^{3}+z^{2}
\end{aligned}
$$

Using Expression (56), we can find the polynomial interval:

$$
S(z)=\left[\frac{z^{14}+z^{13}+z^{12}+z^{11}+z^{10}+z^{9}+z^{8}+z^{7}+z^{6}+z^{3}+z^{2}}{z^{8}+z^{7}+z^{5}+z^{4}+z^{3}+z+1}\right]=z^{7}+z^{4}+z^{2}+z
$$

We now use the developed algorithm. First, we obtain a rank of the polynomial in nonredundant PRNS, $\dddot{k}(z)=z$, that we substitute in

$$
\begin{aligned}
& S(z)=\left|\sum_{i=1}^{5} d_{i}(z) L_{i}(z)+\dddot{k}(z)\right|_{W_{k}(z)}=\mid\left(z^{7}+z^{4}+z^{2}+z\right)+z\left(z^{7}+z^{5}+z^{2}+z+1\right)+ \\
& +\left(z^{3}+z^{2}+1\right)\left(z^{7}+z^{4}+z^{3}+z+1\right)+\left(z^{7}+z^{4}+z^{3}\right)+\left(z^{2}+z\right)\left(z^{5}+z^{4}+z\right)+\left.z\right|_{W_{k}}= \\
& =\left(z^{10}+z^{9}+z^{8}+z^{7}+z^{6}+1\right) \bmod z^{8}+z^{7}+z^{5}+z^{4}+z^{3}+z+1=z^{7}+z^{4}+z^{2}+z
\end{aligned}
$$

The obtained results coincide, which indicates the possibility of using redundant codes of polynomial residue number system to build fault-tolerant high-speed computing systems for digital signal processing and cryptographic protection of information operating in Galois fields.

## 4. Conclusions

The article discusses the principles of constructing redundant codes of polynomial residue number system. Several theorems concerning the properties of codes of PRNS were considered and proven. It was shown that the distortion of any residue transforms an allowed code combination into a prohibited one. In this case, the criterion for the presence of an error in the code combination of PRNS is a violation of Condition (32). Studies of the code of polynomial residue number system, which has minimal redundancy, have been carried out. We have shown that this code only allows for detection an error in the code combination of PRNS. It was proposed to introduce two control bases to expand error-correction abilities. We have shown that the use of two control bases makes it possible to correct any erroneous residue in the code combination of PRNS. The article presents the developed algorithm for calculating the positional characteristic (a polynomial interval) in which only modular operations are used. This algorithm makes it possible to detect and correct errors without performing a reverse conversion from a code of PRNS to a positional code or performing a division operation, which is confirmed by the examples given in the article.

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## References

1. Szabo, N.S.; Tanaka, R.I. Residue Arithmetic and Its Applications to Computer Technology; Mc-Graw Hill: New York, NY, USA, 1967.
2. Mohan, P.V. Residue Number Systems. Algorithms and Architectures; Springer: New York, NY, USA, 2002.
3. Omondi, A.; Premkumar, B. Residue Number Systems: Theory and Implementation; Imperial College: London, UK, 2007.
4. Mohan, A. Residue Number Systems. Theory and Applications; Springer International Publishing: Cham, Switzerland, 2016.
5. Vergos, H.T.; Efstathiou, C. A unifying approach for weighted and diminished-1 modulo ( $2^{\mathrm{n}}+1$ ) addition. IEEE Trans. Circuits Syst. II Express Briefs 2008, 55, 1041-1045. [CrossRef]
6. Patel, R.A.; Benaissa, M.; Boussakta, S. Fast Parallel-Prefix Architectures for Modulo 2n - 1 Addition with a Single Representation of Zero. IEEE Trans. Comput. 2007, 56, 1484-1492. [CrossRef]
7. Juang, T.B.; Chiu, C.-C.; Tsai, M.-Y. Improved Area-Efficient Weighted Modulo $2^{\mathrm{n}}+1$ Adder Design With Simple Correction Schemes. IEEE Trans. Circuits Syst. II Express Briefs 2010, 57, 198-202. [CrossRef]
8. Bajard, J.-C.; Eynard, J.; Gandino, F. Fault Detection in RNS Montgomery Modular Multiplication. In Proceedings of the 2013 IEEE 21st Symposium on Computer Arithmetic, Austin, TX, USA, 7-10 April 2013. [CrossRef]
9. Gandino, F.; Lamberti, F.; Paravati, G.; Bajard, J.-C.; Montuschi, P. An Algorithmic and Architectural Study on Montgomery Exponentiation in RNS. IEEE Trans. Comput. 2012, 61, 1071-1083. [CrossRef]
10. Plantard, T. Efficient Word Size Modular Arithmetic. IEEE Trans. Emerg. Top. Comput. 2021, 9, 1506-1518. [CrossRef]
11. Shahana, T.K.; Jose, B.R.; James, R.K.; Jacob, K.P.; Sasi, S. RRNS-Convolutional encoded concatenated code for OFDM based wireless communication. In Proceedings of the 2008 16th IEEE International Conference on Networks, New Delhi, India, 12-14 December 2008. [CrossRef]
12. Albicocco, P.; Cardarilli, G.C.; Nannarelli, A.; Re, M. Twenty years of research on RNS for DSP: Lessons learned and future perspectives. In Proceedings of the 2014 International Symposium on Integrated Circuits (ISIC), Singapore, 10-12 December 2014. [CrossRef]
13. Khalifa, M.A.; Emam, A.E.; Youssef, M.I. A Comparative Study for RNS Coding Scheme Performance in OFDM and SCFDMA Systems. In Proceedings of the 2020 12th International Conference on Electrical Engineering (ICEENG), Cairo, Egypt, 7-9 July 2020. [CrossRef]
14. Ananthalakshmi, A.V.; Rajagopalan, P. VLSI implementation of residue number system based efficient digital signal processor architecture for wireless sensor nodes. Int. J. Inf. Technol. 2019, 11, 829-840. [CrossRef]
15. Cardarilli, G.; Nunzio, L.D.; Fazzolari, R.; Nannarelli, A.; Petricca, M.; Re, M. Design Space Exploration based Methodology for Residue Number System Digital Filters Implementation. IEEE Trans. Emerg. Top. Comput. 2020, 10, 186-198. [CrossRef]
16. Praveen, M.; Reddy, M.V.S.R.; Raju, Y.D.S. An approach for fixed coefficient RNS-based FIR filter. Open Access Int. J. Sci. Eng. 2021, 6, 1-6.
17. Luan, Z.; Chen, X.; Ge, N.; Wang, Z. Simplified fault-tolerant FIR filter architecture based on redundant residue number system. Electron. Lett. 2014, 50, 1768-1770. [CrossRef]
18. Bernocchi, G.L.; Cardarilli, G.C.; Re, A.D.; Nannarelli, A.; Re, M. Low-power adaptive filter based on RNS components. In Proceedings of the 2007 IEEE International Symposium on Circuits and Systems, New Orleans, LA, USA, 27-30 May 2007. [CrossRef]
19. Rajni, D.S.K. Efficient Techniques for FIR Filter Designing. In Proceedings of First International Conference on Computational Electronics for Wireless Communications; Springer: Berlin/Heidelberg, Germany, 2022; Volume 329, pp. 97-114.
20. Rawat, P.; Nemade, S. High Speed and Area Efficient Pipelined Distributed Arithmetic Technique based Adaptive Filter. Int. J. Electron. Commun. Comput. Eng. 2020, 11, 8-15.
21. Kaplun, D.; Voznesensky, A.; Veligosha, A.V.; Kalmykov, I.A.; Sarma, K.K. Technique to Adjust Adaptive Digital Filter Coefficients in Residue Number System Based Filters. IEEE Access 2021, 9, 82402-82416. [CrossRef]
22. Olsen, E.B. RNS Hardware Matrix Multiplier for High Precision Neural Network Acceleration: "RNS TPU". In Proceedings of the 2018 IEEE International Symposium on Circuits and Systems (ISCAS), Florence, Italy, 27-30 May 2018. [CrossRef]
23. Nakahara, H.; Sasao, T. A High-speed Low-power Deep Neural Network on an FPGA based on the Nested RNS: Applied to an Object Detector. In Proceedings of the 2018 IEEE International Symposium on Circuits and Systems (ISCAS), Florence, Italy, 27-30 May 2018. [CrossRef]
24. Salamat, S.; Imani, M.; Gupta, S.; Rosing, T. RNSnet: In-Memory Neural Network Acceleration Using Residue Number System. In Proceedings of the 2018 IEEE International Conference on Rebooting Computing (ICRC), McLean, VA, USA, 7-9 November 2018. [CrossRef]
25. Ciet, M.; Neve, M.; Peeters, E.; Quisquater, J.-J. Parallel FPGA implementation of RSA with residue number systems-Can side-channel threats be avoided? In Proceedings of the 2003 46th Midwest Symposium on Circuits and Systems, Cairo, Egypt, 27-30 December 2003. [CrossRef]
26. Papachristodoulou, L.; Fournaris, A.P.; Papagiannopoulos, K.; Batina, L. Practical Evaluation of Protected Residue Number System Scalar Multiplication. IACR Trans. Cryptogr. Hardw. Embed. Syst. 2018, 2019, 259-282. [CrossRef]
27. Noordam, L. VHDL Implementation of 4096-bit RNS Montgomery Modular Exponentiation for RSA Encryption. TU Delft Electr. Eng. Math. Comput. Sci. 2019.
28. Wu, T. WITHDRAWN: Fast RNS elliptic curve point multiplication on FPGAs. Microprocess. Microsyst. 2020. [CrossRef]
29. Leelavathi, G.; Shaila, K.; Venugopal, K.R. Elliptic Curve Cryptography implementation on FPGA using Montgomery multiplication for equal key and data size over GF $\left(2^{\mathrm{m}}\right)$ for Wireless Sensor Networks. In Proceedings of the 2016 IEEE Region 10 Conference (TENCON), Singapore, 22-25 November 2016. [CrossRef]
30. Agbedemnab, P.A.; Agebure, M.; Akobre, S. A Fault Tolerant Scheme for Detecting Overflow in Residue Number Microprocessors. Int. J. Eng. Comput. Sci. 2018, 7, 23578-23587.
31. Tay, T.F.; Chang, C.-H. Fault-Tolerant Computing in Redundant Residue Number System. In Embedded Systems Design with Special Arithmetic and Number Systems; Springer: Berlin/Heidelberg, Germany, 2017; pp. 65-88.
32. Popov, D.I.; Gapochkin, A.V. Development of Algorithm for Control and Correction of Errors of Digital Signals, Represented in System of Residual Classes. In Proceedings of the 2018 International Russian Automation Conference (RusAutoCon), Sochi, Russia, 9-16 September 2018. [CrossRef]
33. Afriyie, Y. A Novel Exploitation of Errors in Redundant Residue Number System Architecture. Am. J. Appl. Sci. 2021, 18, 96-106. [CrossRef]
34. Selivanova, M.V.; Tyncherov, K.T.; Ikhsanova, F.A.; Kalmykov, I.A.; Olenev, A.A. A method of paired zeroing of numbers in a residue system. J. Phys. Conf. Ser. 2019, 1333, 022015. [CrossRef]
35. Pontarelli, S.; Cardarilli, G.C.; Re, M.; Salsano, A. A Novel Error Detection and Correction Technique for RNS Based FIR Filters. In Proceedings of the 2008 IEEE International Symposium on Defect and Fault Tolerance of VLSI Systems, Cambridge, MA, USA, 1-3 October 2008. [CrossRef]
36. Gao, Z.; Reviriego, P.; Pan, W.; Xu, Z.; Zhao, M.; Wang, J.; Maestro, J.A. Fault Tolerant Parallel Filters Based on Error Correction Codes. IEEE Trans. Very Large Scale Integr. (VLSI) Syst. 2015, 23, 384-387. [CrossRef]
37. Kalmykov, I.A.; Pashintsev, V.P.; Zhuk, A.P.; Kalmykov, M.I.; Olenev, A.A. Redundant Modular Codes for Development of Fault-Tolerant Systems of Satellite Identification. Int. J. Emerg. Trends Eng. Res. 2020, 8, 3160-3168. [CrossRef]
38. Sachenko, A.; Zhengbing, H.; Yatskiv, V. Increasing the Data Transmission Robustness in Wsn Using the Modified Error Correction Codes on Residue Number System. Elektron. Elektrotech. 2015, 21, 76-81. [CrossRef]
39. Tay, T.F.; Chang, C.-H. A new algorithm for single residue digit error correction in Redundant Residue Number System. In Proceedings of the 2014 IEEE International Symposium on Circuits and Systems (ISCAS), Melbourne, VIC, Australia, 1-5 June 2014. [CrossRef]
40. Tang, Y.; Boutillon, E.; Jégo, C.; Jézéquel, M. A new single-error correction scheme based on self-diagnosis residue number arithmetic. In Proceedings of the 2010 Conference on Design and Architectures for Signal and Image Processing (DASIP), Edinburgh, UK, 26-28 October 2010. [CrossRef]
41. Alhassan, A.; Saeed, I.; Agbedemnab, P.A. The Huffman's Method of Secured Data Encoding and Error Correction using Residue Number System (RNS). Commun. Appl. Electron. (CAE) 2015, 2, 14-18. [CrossRef]
42. Zhu, D.; Natarajan, B. Residue number system arithmetic inspired hopping pilot pattern design for cellular downlink OFDMA. In Proceedings of the 2010 Wireless Telecommunications Symposium (WTS), Tampa, FL, USA, 21-23 April 2010. [CrossRef]
43. Timarchi, S.; Fazlali, M. Generalised fault-tolerant stored-unibit-transfer residue number system multiplier for moduli set $\left\{2^{\mathrm{n}}-1\right.$, $\left.2^{\mathrm{n}}, 2^{\mathrm{n}}+1\right\}$. IET Comput. Digit. Tech. 2012, 6, 269-276. [CrossRef]
44. Afriyie, Y.; Daabo, M.I. Multiple Bits Error Detection and Correction in RRNS Architecture using the MRC and HD Techniques. Int. J. Comput. Appl. 2018, 180, 18-23. [CrossRef]
45. James, J.; Pe, A. Error correction based on redundant Residue Number System. In Proceedings of the 2015 IEEE International Conference on Electronics, Computing and Communication Technologies (CONECCT), Bangalore, India, 10-11 July 2015. [CrossRef]
46. Skavantzos, A.; Taylor, F.J. On the polynomial residue number system (digital signal processing). IEEE Trans. Signal Process. 1991, 39, 376-382. [CrossRef]
47. Skavantzos, A.; Stouraitis, T. Polynomial residue complex signal processing. IEEE Trans. Circuits Syst. II Analog. Digit. Signal Process. 1993, 40, 342-344. [CrossRef]
48. Yang, M.-C.; Wu, J.-L. A new interpretation of "polynomial residue number system". IEEE Trans. Signal Process. 1994, 42, 2190-2191. [CrossRef]
49. Paliouras, V.; Skavantzos, A.; Stouraitis, T. Multi-voltage low power convolvers using the polynomial residue number system. In Proceedings of the GLSVLSI '02: Proceedings of the 12th ACM Great Lakes Symposium on VLSI, New York, NY, USA, 18 April 2002; pp. 7-11. [CrossRef]
50. Abdallah, M.; Skavantzov, A. The multipolynomial channel polynomial residue arithmetic system. IEEE Trans. Circuits Syst. II Analog. Digit. Signal Process. 1999, 46, 165-171. [CrossRef]
51. Katkov, K.A.; Makarova, A.V. Parallel modular technologies in digital signal processing. Life Sci. J. 2014, 11, 435-438.
52. Chu, J.; Benaissa, M. GF $\left(2^{m}\right)$ multiplier using Polynomial Residue Number System. In Proceedings of the APCCAS 2008-2008 IEEE Asia Pacific Conference on Circuits and Systems, Macao, China, 30 November-3 December 2008. [CrossRef]
53. Kalimoldayev, M.; Tynymbaev, S.; Magzom, M.; Ibraimov, M.; Khokhlov, S.; Abisheva, A.; Sydorenko, V. Polynomials Multiplier under Irreducible Polynomial Module for High-Performance Cryptographic Hardware Tools. Available online: http:/ / ceur-ws. org /Vol-2393/ paper_363.pdf (accessed on 15 January 2021).
54. Kalimoldayev, M.; Tynymbayev, S.; Gnatyuk, S.; Khokhlov, S.; Magzom, M.; Kozhagulov, Y. Matrix multiplier of polynomials modulo analysis starting with the lower order digits of the multiplier. News Natl. Acad. Sci. Repub. Kaz. Ser. Geol. Technol. Sci. 2019, 4, 181-187. [CrossRef]
55. Kalimoldayev, M.; Tynymbayev, S.; Gnatyuk, S.; Ibraimov, M.; Magzom, M. The device for multiplying polynomials modulo an irreducible polynomial. News Natl. Acad. Sci. Repub. Kaz. Ser. Geol. Technol. Sci. 2019, 2, 199-205. [CrossRef]
56. Biyashev, R.G.; Nyssanbayeva, S.E.; Begimbayeva, Y.Y.; Magzom, M.M. Modification of the cryptographic algorithms, developed on the basis of nonpositional polynomial notations. In Proceedings of the International Conference on Circuits, Systems, Signal Processing, Communications and Computers (CSSCC 2015), Vienna, Austria, 15-17 March 2015; pp. 170-176.
57. Kapalova, N.; Dyusenbayev, D. Security analysis of an encryption scheme based on nonpositional polynomial notations. Open Eng. 2016, 6, 250-258. [CrossRef]
58. Biyashev, R.; Nyssanbayeva, S.; Kapalova, N. Secret Keys for Nonpositional Cryptosystems. Development, Investigation and Implementation; Lambert Academic Publishing: Chisinau, Moldova, 2014; pp. 136-147.
59. Good, T.; Benaissa, M. Very small FPGA application-specific instruction processor for AES. IEEE Trans. Circuits Syst. I Regul. Pap. 2006, 53, 1477-1486. [CrossRef]
60. Mangard, S.; Aigner, M.; Dominikus, S. A highly regular and scalable AES hardware architecture. IEEE Trans. Comput. 2003, 52, 483-491. [CrossRef]
61. Hodjat, A.; Verbauwhede, I. Minimum area cost for a 30 to 70 Gbits/s AES processor. IEEE Comput. Soc. Annu. Symp. VLSI 2004, 83-88. [CrossRef]
62. Dichenko, S.A.; Finko, O.A. Controlling and Restoring the Integrity of Multi-Dimensional Data Arrays through Cryptocode Constructs. Program. Comput. Softw. 2021, 47, 415-425. [CrossRef]
63. Samoylenko, D.V.; Eremeev, M.A.; Finko, O.A. A method of providing the integrity of information in the group of robotic engineering complexes based on crypt-code constructions. Autom. Control. Comput. Sci. 2017, 51, 965-971. [CrossRef]
64. Martínez-Peñas, U. Sum-Rank BCH Codes and Cyclic-Skew-Cyclic Codes. IEEE Trans. Inf. Theory 2021, 67, 5149-5167. [CrossRef]
65. Li, C.; Wu, P.; Liu, F. On Two Classes of Primitive BCH Codes and Some Related Codes. IEEE Trans. Inf. Theory 2019, 65, 3830-3840. [CrossRef]
