



# Article Enhancement-Mode Heterojunction Vertical β-Ga<sub>2</sub>O<sub>3</sub> MOSFET with a P-Type Oxide Current-Blocking Layer

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**Abstract:** The vertical heterojunction  $Ga_2O_3$  MOSFET (Metal-Oxide-Semiconductor Field-Effect Transistor) with the p-type oxide as the current-blocking layer (CBL) is investigated for the first time using SILVACO simulation software. The results show that the threshold voltage of the device is easily positive, which means that the device works in the enhancement mode. By adjusting the doping concentration (from  $2 \times 10^{17}$  cm<sup>-3</sup> to  $2 \times 10^{18}$  cm<sup>-3</sup>) and thickness (from 0.4 um to 2 um) of p-SnO CBL, the threshold voltage is around from 2.4 V to 2.8 V and the breakdown voltage of the device can be increased from 361 V to 518 V. Compared with the original homojunction  $Ga_2O_3$  vertical MOSFET with CBL, the p-SnO CBL can greatly improve the performance of the device. Other p-type oxides are also investigated as the CBL and show promising performances. This work has a certain guiding significance for the design of a vertical enhanced current-blocking layer MOSFET device and for the development of a  $Ga_2O_3$  heterojunction power device.

**Keywords:** gallium oxide; vertical heterojunction MOSFET; enhancement mode; current blocking layer; p-type oxide

# 1. Introduction

Due to the ultra-wide bandgap of about 4.5–4.9 eV [1–3],  $\beta$ -Ga<sub>2</sub>O<sub>3</sub> has a high critical field strength of 5–9 MV/cm [4]. These characteristics make Ga<sub>2</sub>O<sub>3</sub> a strong competitor for the next generation of power electronic devices. Although Ga<sub>2</sub>O<sub>3</sub> materials have such attractive advantages, due to its low thermal conductivity and various p-type doping problems (such as large effective hole mass [5–8] and large activation energy of traditional acceptors [3,9,10]), the performance of  $\beta$ -Ga<sub>2</sub>O<sub>3</sub> devices is still extremely inferior to those of GaN and SiC. To solve these problems, novel device structures, especially heterostructures that have the greatest potential to make use of Ga<sub>2</sub>O<sub>3</sub> materials, are urgently required.

In high-voltage applications, a vertical device can withstand a higher voltage and has a much smaller device area compared with a transverse device, so a vertical device has more advantages. At present, some vertical Ga<sub>2</sub>O<sub>3</sub> MOSFETs have been reported, including MOSFETs with Mg-ion implantation and N-ion implantation current-blocking layers (CBLs) [11,12], and single-fin or multi-fin gate vertical FinFETs [13,14]. Compared with a Ga<sub>2</sub>O<sub>3</sub> vertical FinFET, the fabrication process of vertical Ga<sub>2</sub>O<sub>3</sub> MOSFETs with CBL is relatively simple and currently has more potential to be realized. Although the reported vertical Ga<sub>2</sub>O<sub>3</sub> MOSFET with CBL can work in the enhanced mode, its breakdown voltage is still temporarily at a low level, which is due to the negative effect of the



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**Copyright:** © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). lack of p-type doping and the corresponding PN junction in Ga<sub>2</sub>O<sub>3</sub>, resulting in a low device performance.

In recent years, p-type metal oxides (SnO, NiO, etc.) have become a hot topic because of their high hole concentration. Due to the  $Ga_2O_3$  p-type doping problem, the p-type metal oxide provides an alternative way to form the heterojunction for  $Ga_2O_3$  device application. At present,  $Ga_2O_3$  heterojunction devices have been reported [15–21] but mainly used in the heterojunction diode. Additionally, none of vertical heterojunction  $Ga_2O_3$  MOSFETs has been studied in detail.

In this paper, the vertical heterojunction MOSFET with p-type SnO and other metal oxides as CBLs are studied for the first time using SILVACO software. Additionally, the simulation calculation in SILVACO is mainly based on Poisson's equation, the drift-diffusion transport equation, the Fermi distribution equation, and the impact ionization equation. Due to the hybrid orbital of p-SnO forming the effective hole transport path, it has relatively high hole mobility [22], which can be prepared by sputtering, PLD (Pulsed Laser Deposition), and electron beam technology [21–24]. The simulation result shows that the breakdown voltage of a vertical heterojunction MOSFET with a p-SnO CBL can be increased from 361 V to 518 V by adjusting the p-type doping concentration (from  $2 \times 1017$  cm<sup>-3</sup> to  $2 \times 1018$  cm<sup>-3</sup>), and the threshold voltage can be adjusted in a small range (2.4–2.8 V). Other p-type oxides are also investigated as the CBL and show promising performances. This work has a certain guiding significance for the fabrication of vertical heterojunction MOSFET with CBL and for the development of Ga<sub>2</sub>O<sub>3</sub>-based heterojunction power device.

## 2. Methods

In order to improve the accuracy of the model and the simulation results, we first carried out the model calibration using the reported results [25]. To save on calculation time, half of the device was established for calculation based on the symmetry of the structure. The device structure and some parameters are shown in Figure 1a. Table 1 also shows the geometrical parameters of the simulated device. As shown in Figure 1a,  $L_{ap}$ was 10 um. To make the CBL form a certain high-resistance region, the region was set as p-type Ga<sub>2</sub>O<sub>3</sub> region in the simulation, which was a virtual pn homojunction and was adopted in other reports [26]. The simulation result is shown in Figure 1b,c. When the gate voltage was 0 V, the breakdown voltage was 263 V (when the current exceeded 1 A/cm<sup>2</sup>, the corresponding voltage was the breakdown voltage); when the gate voltage was 6 V and the drain voltage was 20 V, the current density was  $0.028 \text{ kA/cm}^2$ . In all of the above results, the output and transfer characteristics curves and breakdown voltage of the simulation model are consistent with those reported in another study [25], which verifies the accuracy and feasibility of the model and simulation result. Based on the verified model, p-SnO and other p-type metal oxides as CBLs were included. How the thickness of p-SnO CBL and its doping concentration affect the breakdown voltage and  $V_{th}$  of the device were mainly studied.

Table 1. Geometrical parameters of the simulated device.

Symbol	Definition	Value
L <sub>GS</sub>	distance between the gate and source	5 um
$L_{src}$	source contact length	2 um
Lgo	overlapping length of the CBL and gate	7.5 um
L <sub>nh</sub>	length of the highly doped channel region	10 um
L <sub>nl</sub>	length of the lowly doped channel region	25 um
Lap	current aperture width	10 um
$T_{Al_2O_3}$	$Al_2O_3$ thickness	0.05 um
T <sub>ch</sub>	channel thickness	0.1 um
T <sub>CBL</sub>	current barrier layer thickness	0.8 um
T <sub>drift</sub>	drift layer thickness	8.2 um



**Figure 1.** (**a**) Semi-symmetric device structure of Ga<sub>2</sub>O<sub>3</sub> MOSFET with CBL used in the simulation, (**b**) transfer and output curves in the model, and (**c**) breakdown voltage curve in the model.

## 3. Results

## 3.1. Basic Characteristics

p-SnO was used as the CBL while all other factors remain unchanged. The output curve of the device is shown in Figure 2a, when the gate voltage was 0–6 v (the step size was 1 V) and the saturated leakage current density of the device was 0.029 kA/cm<sup>2</sup> at 6 V. Additionally, the V<sub>th</sub> of the device was positive, which was 2.8 V. This shows that, after replacing the CBL material with p-SnO, the threshold voltage of the device is less affected and the device can still be in the enhanced mode. Here, the p-type doping concentration of CBL is equivalent to the original one.



**Figure 2.** (**a**) Transfer and output curves of Ga<sub>2</sub>O<sub>3</sub> MOSFET with the p-SnO CBL and (**b**) breakdown voltage curve of Ga<sub>2</sub>O<sub>3</sub> MOSFET with the p-SnO CBL.

As seen in Figure 2b, the breakdown voltage of the device reached 379 V. Compared with the original device, the breakdown voltage of the heterojunction device increased by

44% when other parameters (such as the doping concentration and thickness of each part) remained unchanged, except for the change in material.

To better analyze the change in threshold voltage after replacing the CBL material, the transverse and longitudinal conduction band diagrams of the device were extracted from the simulation results. The transverse extraction range is shown by the red solid line in Figure 1a (in the channel near the bottom of  $Al_2O_3$ ), and the longitudinal extraction range is shown by the red dotted line in Figure 1a. The extraction results are shown in Figure 3a,b. It can be seen from Figure 3a that, when SnO is used as the CBL material, the average conduction band height in n-Ga<sub>2</sub>O<sub>3</sub> channel layer is lower than that of Ga<sub>2</sub>O<sub>3</sub> as the CBL. Through ouor study and analysis of the electron affinity and band gap width of SnO and  $Ga_2O_3$ , it was found that, because the electron affinity and band gap width of SnO are less than  $Ga_2O_3$ , a II-type heterojunction formed at the interface between the p-SnO CBL and the n-Ga<sub>2</sub>O<sub>3</sub> channel layer. When the heterojunction formed, a "spike" and "notch" formed at the interface of SnO and  $Ga_2O_3$  materials with different band gap widths, resulting in a sudden change in the energy band at the interface. It can be seen in the heterojunction at the circle in Figure 3a. After the heterojunction formed, the conduction band energy in the channel of SnO heterojunction MOSFET became lower than that in Ga<sub>2</sub>O<sub>3</sub> MOSFET due to the sudden change in energy band at the interface, and finally, the threshold voltage of SnO heterojunction MOSFET became slightly lower than that of  $Ga_2O_3$  MOSFET. This can also be seen from Figure 3b, which shows the conduction band diagram of the channel at 0 V gate voltage and the threshold voltage. The channel barrier of SnO heterojuncion MOSFET in the cut-off state was lower than that of the original device, so when the gate voltage increased, the gate voltage required to reach the channel conduction condition was relatively reduced.



**Figure 3.** (a) The conduction band diagram at 0 V gate voltage in the vertical direction of the oxide layer, channel, and CBL, and a heterojunction formed at the interface between channel and SnO CBL. (b) The conduction band diagram at 0 V gate voltage and threshold voltage with Ga<sub>2</sub>O<sub>3</sub> CBL or p-SnO CBL. (c) Comparison of the depletion layers of Ga<sub>2</sub>O<sub>3</sub> CBL and p-SnO CBL at breakdown voltage.

## 3.2. Analysis of Energy Band Structure and Characteristics

The increase in breakdown voltage mainly comes from the influence of different materials on the thickness of depletion layer. Since the voltage at the time of breakdown is much greater than the built-in potential of the heterojunction, the depletion layer width of the heterojunction device is given by the following Formula (1):

$$W_{heterojunction} = \sqrt{\frac{2\varepsilon_1\varepsilon_2(N_{A1} + N_{D2})^2 V}{qN_{A1}N_{D2}(\varepsilon_1 N_{A1} + \varepsilon_2 N_{D2})}}$$
(1)

Among them,  $\varepsilon_1$  and  $\varepsilon_2$  are the dielectric constants of SnO and Ga<sub>2</sub>O<sub>3</sub>, respectively;  $N_{A1}$  and  $N_{D2}$  are the p-type doping concentration of SnO and n-type doping concentration of Ga<sub>2</sub>O<sub>3</sub>, respectively; *V* is the applied voltage; and *q* is the electron charge.

Similarly, when the applied voltage is much greater than the built-in potential, the Formula (2) for the depletion region width of the PN junction of the same material is given:

$$W_{pn} = \sqrt{\frac{2\varepsilon_2 V(N_{A1} + N_{D2})}{q N_{A1} N_{D2}}}$$
(2)

Among them,  $\varepsilon_2$  is the dielectric constant of Ga<sub>2</sub>O<sub>3</sub>;  $N_{A1}$  and  $N_{D2}$  are the p-type and n-type doping concentrations of Ga<sub>2</sub>O<sub>3</sub>; V is the applied voltage; and q is the electron charge.

To better compare the effects of homogeneous junction and heterojunction on the width of depletion layer, the following comparison Formula (3) is given and simplified:

$$\frac{W_{heterojunction}}{W_{pn}} = \sqrt{\frac{N_{A1} + N_{D2}}{\frac{\varepsilon_2}{\varepsilon_1}N_{A1} + N_{D2}}}$$
(3)

As can be seen from the above formula,  $\varepsilon_2 < \varepsilon_1$ , that is, the dielectric constant of CBL material is greater than that of Ga<sub>2</sub>O<sub>3</sub> material. Under the same applied voltage, the depletion region formed by heterojunction is wider than that of homogeneous junction. A wider depletion layer means being better able to withstand higher voltages and having a greater breakdown voltage of the device. This can be seen from the depletion layer width of the simulation results. As shown in Figure 3c, the dielectric constant of SnO is greater than that of Ga<sub>2</sub>O<sub>3</sub>, and the thickness of the depletion layer generated by its heterojunction is thicker than that of the original device. Under the same breakdown field strength, the greater the depletion thickness, the greater the voltage applied to the depletion layer and the higher the breakdown voltage of the device.

### 3.3. Influence of Doping Concentration and Thickness of p-SnO CBL

In this section, the effects of p-type doping concentration and thickness of p-SnO CBL on the basic characteristics of device are systematically studied.

As shown in Figure 4a,b, when the thickness of p-SnO CBL was set to 0.8 um and the p-type doping concentration increased from  $2 \times 10^{17}$  cm<sup>-3</sup> to  $2 \times 10^{18}$  cm<sup>-3</sup>, the breakdown voltage of Ga<sub>2</sub>O<sub>3</sub> MOSFET with a p-SnO CBL can be in the range from 361 V (corresponding to  $2 \times 10^{18}$  cm<sup>-3</sup> doping concentration) to 518 V (corresponding to  $2 \times 10^{17}$  cm<sup>-3</sup> doping concentration), and the threshold voltage was in the range of 2.4–2.8 V. As shown in Figure 4c, the breakdown voltage of the vertical MOSFET with p-SnO CBL has a peak value. When the thickness of CBL was about 0.8 um, the breakdown voltage of the device reached a peak value of 518 V. When the thickness of CBL increased or decreased, the breakdown voltage decreased correspondingly, and the threshold voltage range of the device was small, which was 2.4–2.8 V. By adjusting the p-type doping concentration and thickness of p-SnO CBL, the breakdown voltage of the device can be greatly increased and the threshold voltage of the device to the preparation of power devices.



**Figure 4.** (a) The breakdown voltage, (b) the threshold voltage curves under different doping concentrations obtained when the thickness of CBL was 0.8 um, and (c) the doping concentration of CBL at  $2 \times 10^{17}$  cm<sup>-3</sup>, with the breakdown voltage and threshold voltage curves of the device obtained at different thickness.

To better understand the physical mechanism of p-type doping concentration and thickness of p-SnO CBL on the device, the energy band diagrams of CBL with different p-type doping concentration and thickness are extracted in Figure 5a. As shown in Figure 5a, the higher the p-type doping concentration of CBL, the higher the potential barrier in the channel region. To achieve the low barrier conduction state, a higher gate voltage is required so that the threshold voltage of the device is increased. In Figure 5a, the thickness of p-SnO CBL hardly affects the height of channel barrier, so the threshold voltage of the device hardly changes. As for the breakdown voltage, it can be seen from Figure 5b that the lower the p-type doping concentration of CBL, the thicker the depletion layer thickness of the drift layer of the device, causing an increase in the breakdown voltage. To see the relationship between CBL thickness and depletion layer more intuitively, the parameter was directly extracted from the simulation results and a curve was drawn, shown in Figure 5c. With the increase in CBL thickness, the effective depletion layer thickness increased and the breakdown voltage increased. Until the CBL thickness reached 0.8 um, the effective depletion layer thickness decreased and the breakdown voltage decreased. Additionally, that is consistent with the previous trend of breakdown voltage.

As seen from the above Formula (1), when the doping concentration and material remained unchanged, the thickness of the depletion layer mainly depended on the applied voltage. The CBL was thin and the device broke down, as seen from the left side of Figure 5d. At this time, there was a certain concentration of electrons in the CBL, indicating that part of the current flowed directly from the CBL. Additionally, the CBL lost the function of blocking the current, so the breakdown voltage of the device decreased and the thickness of the depletion layer decreased similarly. When the thickness of CBL increased, as shown on the right side of Figure 5d, the current in the CBL layer gradually decreased to 0. At this time, the breakdown voltage increased and the thickness of the depletion layer showed an upward trend. As can be seen from Figure 5e, with the increase in the thickness of the

current barrier layer, under the same applied voltage (VDS = 150 V), the edge peak electric field intensity of CBL also increased and the location of device breakdown was also at the edge of the lower right corner of CBL because the location was just at the sharp corner of CBL. It is well known that the electric field intensity at the sharp corner is the largest, the increase in edge peak electric field leads to the corresponding decrease in breakdown voltage, and then the thickness of depletion layer also decreases. Therefore, the reason for the thickest depletion layer formed by 0.8 um CBL is mainly that, when the thickness is less than 0.8 um, the effect of CBL plays a decisive role in the breakdown voltage and, when the thickness is more than 0.8 um, the peak electric field intensity at the edge of CBL plays a decisive role in the breakdown voltage and ecces and the breakdown voltage.

### 3.4. Other p-Type Metal Oxide CBL Ga<sub>2</sub>O<sub>3</sub> Heterojunction MOSFETs

For a comparison with other p-type metal oxides, NiO, Cu<sub>2</sub>O, WO<sub>3</sub>, and MoO<sub>3</sub> were selected as the CBL of a  $Ga_2O_3$  heterojunction MOSFET, and the relevant material simulation parameters were from References [27–30]. Figure 6 shows the channel conduction band of heterojunction device with different p-type metal oxide CBLs and its threshold voltage. It can be seen from the figure that, under different materials, the barrier height is different in the channel region, resulting in different threshold voltages. The trend relationship between the barrier height and the threshold voltage is consistent, as described above.

As can be seen from Figure 7, by adjusting the doping concentration and thickness of the CBL, the other p-type metal oxides can also be used as the CBL to control the overall breakdown voltage of the device in a large range. It is worth noting that, except for Cu<sub>2</sub>O and MoO<sub>3</sub> as the CBL, compared with Ga<sub>2</sub>O<sub>3</sub> CBL, other p-type metal oxides can improve the breakdown voltage to a certain extent at the same doping and thickness level, which is consistent with the dielectric constant-related results described above.



Figure 5. Cont.



Figure 5. Cont.



**Figure 5.** (a) Energy band diagram of different p-type doping concentrations and thicknesses. (b) Simulation results of the depletion layer thickness affected by p-type doping concentration (the label of electron concentration in the figure is a logarithm of ten). (c) Relationship curve between CBL thickness and depletion layer. (d) Simulation results of the electron concentration diagram after device breakdown when the thickness of CBL is 0.4 um (left) and 0.8 um (right). (e) When  $V_{DS} = 150 \text{ V}$ , the peak electric field intensity at the edge of CBL under different thickness of CBL.



**Figure 6.** (**a**) Various p-type oxide semiconductors, and (**b**) channel conduction band diagram and threshold voltages of heterojunction MOSFET with different p-type metal oxide CBLs.



**Figure 7.** Breakdown voltage curve of the p-type metal oxide CBL heterojunction MOSFET with different doping concentrations and thicknesses of the CBL.

# 4. Conclusions

By adjusting the doping concentration and thickness of the p-SnO CBL, the threshold voltage of the device can be adjusted in a small range of 2.4–2.8 V, and the breakdown voltage of the device is greater than 361 V, up to 518 V. Compared with the original vertical MOSFET with Ga<sub>2</sub>O<sub>3</sub> CBL, using heterojunction p-type metal oxide as the CBL can greatly improve the performance of the device. Other p-type oxides such as NiO, Cu<sub>2</sub>O, WO<sub>3</sub>, and MoO<sub>3</sub> are also investigated as the CBL and show promising performances. This work has a certain guiding significance for the design of a vertical enhanced MOSFET with CBL and for the development of a Ga<sub>2</sub>O<sub>3</sub>-based heterojunction power device.

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