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The Influence of AlGaN/GaN Heteroepitaxial Structure Fractal Geometry on Size Effects in Microwave Characteristics of AlGaN/GaN HEMTs

Nikolay A. Torkhov ^{1,2,3,*}, Leonid I. Babak ¹ and Andrey A. Kokolov ¹ 

¹ Laboratory of Intelligent Computer Systems, Tomsk State University of Control Systems and Radioelectronics (TUSUR), 634050 Tomsk, Russia; leonid.babak@mail.ru (L.I.B.); kokolovaa@gmail.com (A.A.K.)

² Research Institute of Semiconductor Devices, JSC, 634034 Tomsk, Russia

³ Tomsk State University (TSU), 634050 Tomsk, Russia

* Correspondence: trkf@mail.ru

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Abstract: The investigation of size effects appearing in the dependence of AlGaN/GaN HEMT high-frequency characteristics on channel width d and number of sections n is conducted using the notions of measure, metric and normed functional (linear) spaces. In accordance with the results obtained, in local approximation the phenomenon of similarity can exist, not only in metric spaces of heteroepitaxial structures, but also in the defined on them functional spaces of the measures of these structures' additive electrophysical characteristics. This provides means to associate size effects of the HEMTs with their structure material fractal geometry. The approach proposed in the work gives an opportunity, not only to predict the size of the structural elements (e.g., channel width and number of sections) of the transistor with the desired characteristics, but also to reconstruct its compact model parameters, which significantly speeds up the development and optimization of the HEMTs with the desired device characteristics. At transferring to the global approximation, when the topological and fractal dimensions of the structure coincide, its electrophysical characteristics, and subsequently, the values of the compact model equivalent circuit parameters, as well as HEMT high frequency characteristics, follow the classic (linear) laws peculiar to the spaces of integer topological dimensions D_T .

Keywords: fractals; metric space; functional space; gallium nitride; heteroepitaxial structures; HEMT; microwave S-parameters; linear model; size effects

1. Introduction

Investigations of the influence of the electric charge carrier chaotic motion on non-linear effects in the transfer characteristics of semiconductor devices (e.g., field-effect transistors, FETs) have been conducted since the 1960s. It has been revealed that in general cases, non-periodic and non-regular chaotic signal fluctuations follow the theory of deterministic chaos, and in local approximation can be described in notions of geometry of fractal dimensions [1–4]. In the result, it has become clear that at submicron- and nanoscale, the conventional wisdom about current flow being a drift-diffusional process can no longer be used for a precise description of FET electrical characteristics. One example for it is the model of charge carrier kinetics in the layer of the fractal structure, using the mathematical tool of the fractal integro-differentiation [5].

In the capacity of the contribution to the problem under consideration, this work studies the basic concepts of the Hausdorff–Besikovich fractal dimension geometry, investigates the peculiarity of AlGaN/GaN heteroepitaxial structure (HES) geometry, determines the main geometric factors

affecting high frequency (HF) HEMT characteristics, conducts experimental tests involving an analysis of HES electrophysical parameters $p_{k,i}$, measurement of manufactured transistors' HF characteristics $H_{21}(f)$, $G_{max}(f)$, f_T , and f_{max} , extraction of the parameters $P_{k,i}$ of their linear models and a verification and physical interpretation of the results obtained. The listed actions are taken in the context of the application in semiconductor materials science and exemplified by heterostructural AlGaIn/GaN field-effect transistors with high electron mobility (hereinafter referred to as HEMTs).

2. The Basic Concepts of Fractal Geometry in the Context of Its Application for the Description of AlGaIn/GaN HEMT Structures

Until recent times, the limitedness of the application of chaos theory and fractal geometry in semiconductor materials science was associated with the beliefs about the linearity of the processes taking place on the surface and in the volume of heteroepitaxial structures, and nonlinearities observed in general were explained by defects, dopants and crystalline anisotropy. In actual fact, nonlinear properties can be intrinsic to crystalline semiconductors. For example, distribution of dopants, defects, relief irregularities [6,7] and surface potential lateral irregularities, as well as processes of deposition, crystal nucleus formation and growth, and also mosaic structure of epitaxial films, have nonlinear chaotic character [4,8–12]. Due to their small sizes, such objects, as well as the quantum ones, can be considered as nanoscale objects in semiconductor electronics.

In local approximation (when the measurement scale linear size l is lower than some value L), physical parameters $p_{k,i}$ of the nano- and microstates of the objects listed above often have the properties of chaotic systems [8–12] including: (1) a strong dependence of the system state on initial condition (small changes of environmental condition lead to significant changes of the state); (2) relativity and indefiniteness of the measurement processes, i.e., dependence upon the choice of reference coordinates and the measurement scale; (3) existence of topological mixing properties—when components do not overlap each other [7]. As reported in [4], all of these properties can be used to improve the characteristics of the developed semiconductor devices.

The properties mentioned above often lead to the occurrence of so-called size effects, i.e., the dependence of the measures of semiconductor electronics objects under testing on the linear sizes of their structural elements [6,7]. In view of this, today the theory of deterministic chaos based on nonlinear (e.g., fractal [6,7]) geometry is used more and more widely to calculate (simulate) the classic and quantum transfer characteristics of semiconductor devices at submicron- and nanoscale levels.

Despite a growing number of investigations into this sphere [13–16], the nature of size effects in semiconductor device engineering, and specifically, its inquiry for the purpose of improving FET device characteristics, remain understudied. This is one of the main reasons why today there is no possibility to reliably predict the HF small signal and power characteristics of field-effect transistors in relation to the linear size of their structural elements (e.g., width of sections d and their number n —number of fingers). In other words, the development and optimization of transistor parameters have, in general, an experimental search nature today.

2.1. Using Linear Functional Spaces for a Description of Semiconductor Object Electrophysical Characteristics

The investigation of the influence of the AlGaIn/GaN heteroepitaxial structure geometry upon the device characteristics of HEMTs based on that structure was conducted using the notions of measure, metric and normed linear functional spaces (hereinafter referred to as functional spaces), which gave an opportunity to consider scalar and vector values, continuous functions and number sequences from the same point of view.

One of the important characteristics determining the application scope for the Euclidian geometry of integer topological dimensions D_T is the limit of local approximation L —the value of linear scale. When the measurement scale l of any linear size of a semiconductor object goes beyond the local approximation limit L (i.e., where $l > L$), the Hausdorff–Besikovich dimension D_H of this object becomes equal to its topological dimension: $D_H = D_T$. This is an applicable scope of Euclidian geometry.

When the linear size l of the object is less than or equal to L ($l \leq L$), the Hausdorff–Besikovich dimension D_H of the object becomes less than its topological dimension D_T : $D_H < D_T$. In this scope the D_H value can be fractional.

In most cases, only functions being essentially integrals of some non-negative additive functions determined at the families of Minkovsky space sets, which satisfy triangle, symmetry and zero distance axioms [17], are considered for definition of measures M . The Minkovsky space is a four-dimensional pseudo-Euclidian space of the signature {1,3} [18].

At this, for every function it is necessary to consider only families of Minkovsky space sets measurable by the given measure M for which this measure exists. In the capacity of such spaces let us use metric spaces R formed by the pair $R = \{X, \rho\}$, consisting of some set of semiconductor device elements of construction and heteroepitaxial structure $X = \{x_i\}$, in which the distance ρ between any pair of elements is defined with the triangle, symmetry and zero distance axioms [17,19]. In this case, a single-valued, non-negative real function $\rho = \rho(r)$ must be defined for any r (r is a radius-vector) from the X set.

The set of surface points (relief) can be counted as the metric space of the heteroepitaxial structure; the FET structural element set X (the set of ohmic and barrier contacts, field-effect gates, etc.) in which the distance between any pair of elements is defined with the triangle, symmetry and zero distance axioms, can be counted as the FET metric space $R = \{X, \rho\}$. For example, in FET metric spaces we can consider subsets R^* in which ρ is defined by the number of sections n and/or channel width d : $\rho = n \times d = W$. It is obvious that these subsets have all the properties of the basic set. So hereinafter we are going to use R instead of R^* . This approach gives an opportunity to investigate the geometry of semiconductor electronics objects, but it does not help to reveal the relation of this geometry and the electrophysical properties of the objects.

In the context of semiconductor materials science and device engineering, depending on the task in any individual case, it is necessary to use functional spaces which properties are defined by the class of the used functions (the type of metric) describing one of the object electrophysical parameters $p_{k,i}$. Unlike metric spaces R , the measures of functional spaces (functionals Φ) can also have negative values, e.g., the measures of electrical charge, resistance, current density, etc. Specifically, integrals of electrical charge, current or mass density functions can be considered as the measures revealing the capacitive, conductive and mass-dimensional properties of line wire, two-dimensional surfaces or three-dimensional objects [20–23].

Let us consider the integral of some function $F_k(x,y,z)$ —functional Φ_k —as the measure in the functional space $M_k = \Phi_k$:

$$M_k = \Phi_k = \int_r F_k(r) dr. \quad (1)$$

The relation between functional and metric spaces can be built by an association of each area from the metric space set R with some functional $\Phi_{k,i}$ from the linear functional space $M_k = \{\Phi_{k,i}\}$ —the set of functions describing measures $\Phi_{k,I} = M_{k,i}$ of the k^{th} additive (integral) physical characteristic of the semiconductor object, where $k = 1, 2, \dots, K$ is a number of such measure, characterizing for example, $M_{1,I} = R_{gs,i}$, $M_2 = R_{ds,i}$, $M_3 = C_{gs,i}$, $M_4 = C_{gd,i}$, $M_5 = C_{ds,i}$, $M_6 = g_{max,i}$, and many others, and $I = 1, 2, \dots, \dots, N$ is a number of the element with the k^{th} measure. At this, all arguments of the functions $F_{k,i}$ from the linear functional spaces M_k must belong to the metric space set R .

$$M_k = \{M_{k,i}\}. \quad (2)$$

Therefore, each area from the metric space set R can be matched with an integral measure $M_{k,i}$ (functional) from the functional space, describing through the arguments belonging to the metric space R some additive (integral) electrophysical characteristic.

The association of R and M_k forms the so-called “metric space with a measure” $S_{M,k} = \{R, M_k\}$, that is essentially an assembly of the elements of this metric space R , on which some measure set

$M_{k,i}$ from the linear functional space M_k [12,17] depends (through the arguments). In this case the dimension of both metric spaces R and functional spaces M_k can be represented by any one of the real numbers from 0 to 3.

In the context of transistors, the set X of transistor structure points belongs to the metric spaces $R = \{X, \rho\}$, and the set of measures of their electrophysical and device characteristics $\{M_{k,i}\}$ belongs to functional spaces. Thus, we obtain the metric space with a measure $S_{M,k} = \{R, M_k\}$ that is essentially an assembly of elements of the transistor metric space R , to which parts some set M_k of measures $M_{k,i}$ of the transistor electrophysical and device characteristics is assigned [12].

2.2. Measures of Semiconductor Objects' Physical Parameters

In semiconductor materials science, in the local limit ($l < L$) the measure M of such (e.g., fractal) objects can generally be represented by any additive value: mass; defect, charged particles or irregularities density; surface, interface or heterojunction area; diffusion front shape, etc.

The problem of the definition of semiconductor object measure M involves the definition of how many times N the object under measurement, which is enclosed with a limited space R^D , can be filled with some measuring (gauge) object described by the function $l(\delta) = \gamma(D_T)\delta^{D_M}$, where γ is a normalizing index, δ is a dimensionless scale, D_M is a Minkovsky dimension (the dimension of the limited set in metric space). Then, according to [7], $N = 1/\delta^{D_M}$. As in the given paper we are going to work only with a subset of metric spaces and functional subspaces defined on them, let us use the Hausdorff–Besikovich dimension D_H (hereinafter referred to as the Hausdorff dimension) instead of the Minkovsky dimension D_M , because these notions are close enough $D_H \approx D_M$ [7]. Therefore, the dimension D_H of a limited set in metric space can be represented in the following way:

$$D_H = D_M = \lim_{d \rightarrow 0} \frac{\ln(\eta)}{-\ln(\zeta)}, \quad (3)$$

where η is a minimum number N of diameter d sets, with which it is possible to lay down (fill) a set under measurement, if the value d is decreased by a factor of ζ [7].

Then, the semiconductor object measure M can be written in the following way:

$$M = N(\delta)d(\delta) = \gamma(D)\delta^{D_T - D_H}. \quad (4)$$

The Hausdorff–Besikovich dimension D_H relates to the fractal dimension D_f as $D_f = D_T + D_H$, where $D_T = 1, 2, 3$ is an integer topological dimension.

In the case of self-similar (e.g., fractal) sets, the Hausdorff dimension can be considered as a similarity dimension $D_S = D_H$.

If the known value of the measure M_0 of some semiconductor object under investigation is taken as a normalizing index $\gamma(D)$ in Equation (4), the expression can be written in the following way:

$$M = M_0 \delta^{2D_T - D_f}. \quad (5)$$

After taking the logarithm of both left and right parts we obtain an expression for the dimension D_f represented through the object measure M :

$$D_f = \frac{\ln\left(\frac{M_0}{M}\right)}{\ln(\delta)} + D_H. \quad (6)$$

It is worth reminding ourselves that an ideal fractal object is formed by the embedded non-overlapping sets of self-similar forms, and can be characterized by the following parameters: local approximation limit L , fractal dimension D_f , scaling indexes ζ and η , the law of affine transformation $x_i = Ax_{i-1}$ [7]. Recall that at $l \geq L$, $D_f = D_T$.

At the same time, naturally the most Brownian and chaotic objects, which semiconductors can be counted as, are not self-similar in the literal sense. It is possible to consider only some statistical similarity and statistical self-affinity—the similarity in some interval of measurement scales characterizing with an average per set fractal dimension value. However, the object under investigation can be characterized, not by one, but by several fractal dimension values depending upon the measurement scale l , $D_f = D_f(l)$ —the so-called multifractal object. In such cases it is often convenient to use an average value of fractal dimension $\langle D_f \rangle$ which is obtained by means of averaging out fractal dimensions determined for different surface sections [24].

With some simple transformations it is possible to reveal that in the local approximation ($l < L$) the relation of the measures of fractal objects corresponding to different linear dimensions d has more gently-sloping power-law dependence than in global approximation ($l \geq L$):

$$\frac{M_i}{M_{i+1}} = \left(\frac{d_i}{d_{i+1}} \right)^{2D_T - D_f}. \quad (7)$$

From Equation (7) it is possible to define the value D_f :

$$D_f = \frac{\ln\left(\frac{M_i}{M_{i+1}}\right)}{\ln\left(\frac{d_{i+1}}{d_i}\right)} + D_H. \quad (8)$$

The object measure M_{i+1} of the following similarity level:

$$M_{i+1} = M_i \left(\frac{d_{i+1}}{d_i} \right)^{2D_T - D_f}. \quad (9)$$

It follows from Equation (9) that at transferring to global approximation (at $l \geq L$), where $D_T = D_f$, the relation of measures M follows the standard law:

$$\frac{M_{T,i+1}}{M_{T,i}} = \left(\frac{d_{i+1}}{d_i} \right)^{D_T}. \quad (10)$$

For example, in a local approximation a three-times change in the linear size of the side of the projection on the (x,y) plane of the square section $d \times d$ of the fractal surface with $D_f = 2.36$, leads to the change of its measure M_d (e.g., surface electric charge), not in $3^{D_T} = 3^2 = 9$ times, but much less, to be exact, just in $3^{4-D_f} = 3^{4-2.36} = 3^{1.64} \approx 6.06$ times (7). Simultaneously, a relative value of some variable $M_d(d)$, say electrical charge density $\sigma_d = M_d(d)$, is fully defined by the linear size d of the section under measurement:

$$\sigma(d) = M_0 \frac{d^{D_T - D_f}}{d_0^{2D_T - D_f}}, \quad (11)$$

where d_0 is a linear size of the projection onto the (x,y) plane.

Upon transferring to global approximation, when $D_f = D_T$, the expression (1) is transformed into the conventional, invariant with d , expression:

$$\sigma(d) \Big|_{D_f = D_T} = \sigma_0 = M_0 / d_0^{D_T} = \text{Const}. \quad (12)$$

Therefore, the mathematical relations (9) and (11) describe a law which allows, in local approximation, the control the physical properties of semiconductor objects at the nanoscale level by means of changing their linear sizes d , in a far greater ($\gg 100$ nm) interval of measurement scales compared to the one for quantum objects (≤ 100 nm).

As functional space subsets exhibit the metric space basic properties of triangle, symmetry and zero distance, the expressions (6) and (8) can be used to define their Hausdorff dimensions. In this case, M_0 must be represented by the highest or the lowest value of the functional at the functional space under investigation, corresponding to the smallest $l = l_0$ or to the largest $l = l_{max}$ value of our measurement scale. Assuming $\eta = \Phi_{k,i}/\Phi_{k,0}$ and $\zeta = l_i/l_0$, from Equation (3) it is possible to define the values D_S of functional spaces, e.g., layer resistances [12].

To estimate the operating peculiarities of the AlGaIn/GaN transistors under investigation, we will use a small-signal model (SS-model), which elements belong to linear functional spaces $S_{M,k} = \{R, M_k\}$, defined depending on n and d in some area of metric space $R = \{X, \rho(n, d)\}$ of these transistors' structural elements. The SS-model in a form of equivalent circuit (EC) is essentially an assembly of $k = 1 \dots K$ interacting equivalent elements $P_{k,i}$ [25,26] belonging to some $S_{M,k}$. The used field-effect transistor EC rather accurately describes the behavior of HEMT small-signal S -parameters in a frequency range up to 60 GHz [27].

3. The Connection between the Electrophysical Parameters of the AlGaIn/GaN HEMT Heteroepitaxial Structure and Its Geometry

In this section, we investigate in local approximation the connection between the geometry and electrophysical characteristics of the used AlGaIn/GaN heteroepitaxial structure (HES) grown on a semi-insulated sapphire wafer with the diameter of ≈ 50 mm by means of the metalorganic chemical vapor deposition (MOCVD) technique. The thickness of the semi-insulating buffer gallium nitride (GaN) layer is 2000 nm, the one of the aluminum nitride (AlN) layer is 1.5 nm, the $\text{Al}_{30}\text{Ga}_{70}\text{N}$ spacer is 4 nm; the thicknesses of the high-alloy ($N_D > 10^{18} \text{ cm}^{-3}$) $\text{Al}_x\text{Ga}_{1-x}\text{N}:\text{Si}$ n^+ layer and encapsulating unalloyed GaN layer are 14 nm and 2 nm, correspondingly. Electron density of two-dimension (2D) electron gas $N_S = 1.36 \times 10^{13} \text{ cm}^{-2}$, mobility μ (at $T = 300$ K) = $1200 \text{ cm}^2/(\text{V} \times \text{s})$, and the dislocation density is $\sim 10^8 \text{ cm}^{-2}$ [22,28].

For the purpose of HES conductivity estimation, the matrix of test elements with different width d and channel length l_{ds} has been formed on one part of the structure. The elements have the form of active rectangular areas with thickness d insulated from each other by means of reactive ion etching.

On the areas there are parallel-plate ohmic contacts (OC) with the distance l_{ds} between any two of them (Figure 1a), formed by means of explosive lithography, TiAlNiAu deposition and subsequent annealing in nitrogen atmosphere at the temperature of 780°C during 30 s.

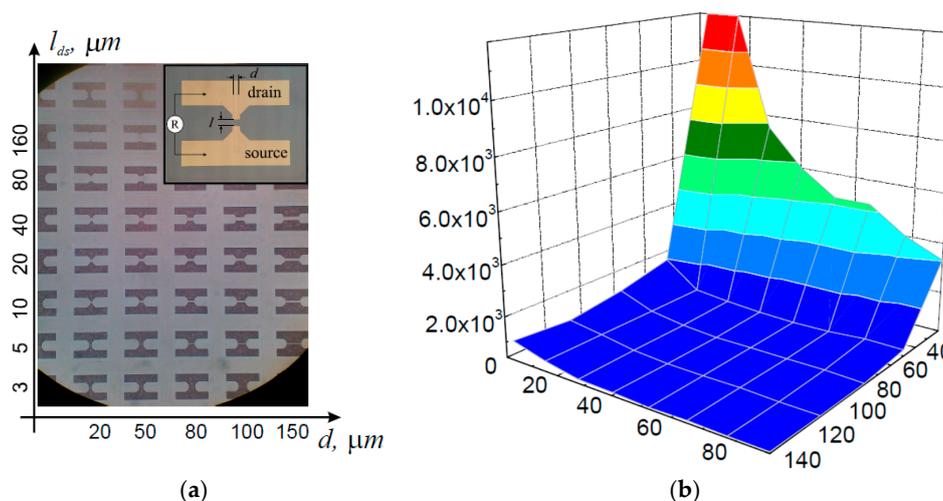


Figure 1. The optical image of the matrix of TiAlNiAu ohmic contacts of the test elements with different values of channel length l_{ds} and width d , formed on the AlGaIn/GaN high-electron-mobility transistor (HEMT) heteroepitaxial structure—(a). The dependence of the channel resistance per unit length ρ on the length l_{ds} and width d —(b).

In order to define the HES Hausdorff dimension, we used tracing—a technique based upon the similarity dimension D_S definition by calculating the number of closed contours [7] encircling surface irregularities in the (x,y) plane, or lateral irregularities of electrostatic potential. After the tracing we calculated the number N_0 of closed contours obtained at the given measurement scale l_0 . At the next stage, scale l_i was decreased to obtain an integer number of closed contours $N_i > N_0$, and so on, until l_i was equal to the minimum distance between pixels. With the use of expression (3), taking into account that $\eta = N_{i-1}/N_i$ and $\zeta = l_{i-1}/l_i$, we defined the metric space similarity dimension D_S for the surface under investigation. A graphical representation of the closed contour number N_i vs. l_i is called “freaking stairs”, due to different height and width of its steps (Figure 2a,b).

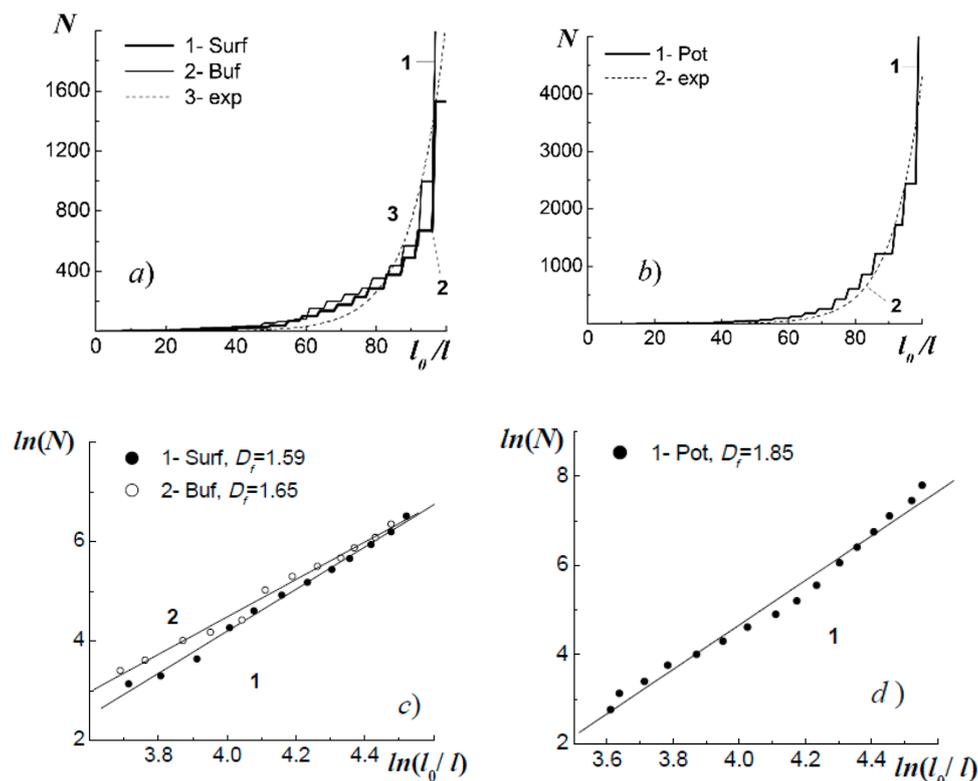


Figure 2. The “freaking stairs” for the $50 \times 50 \mu\text{m}$ areas of the relief surfaces of the AlGaIn/GaN HEMT heteroepitaxial structure (polyline 1), gallium nitride (GaN) buffer layer (polyline 2), and their exponential approximation (curve 3)—(a); the “freaking stairs” for the contact potential difference (CPD) $\Delta\varphi(x,y)$ of the AlGaIn/GaN heteroepitaxial structure (polyline 1) and its exponential approximation (curve 2)—(b). The linear dependences of $\ln(N)$ on $\ln(l_0/l)$ for the surface areas of AlGaIn/GaN HES structure (line 1) and GaN buffer layer (line 2)—(c); the same dependence for the contact potential difference (CPD) $\Delta\varphi(x,y)$ of AlGaIn/GaN heteroepitaxial structure—(d).

Measurement of electrophysical parameters $p_{k,i}$ and estimation of HEMT HES surface relief shape $h(x,y)$ (Figure 3a) were conducted with the use of the Solver-HV atomic force microscope (AFM)—made by NT-MDT (Zelenograd, Moscow)—in semi-contact mode [29]. The contact-potential difference (CPD) $\Delta\varphi(x,y)$ between cantilever needle point and HEMT HES (Figure 3b) was measured by means of the AFM Kelvin probe technique in semi-contact mode with the use of the following expression:

$$\Delta\varphi(x,y) = \varphi_p - \varphi_s(x,y), \quad (13)$$

where φ_p was a surface potential of the cantilever needle point plating material (p for probe), and φ_s was a potential of the surface (s for surface) under investigation. Therefore, being aware of the work function of the cantilever needle point ($q\varphi_p$), from the expression (13) it was possible to find out the

distribution of potential $\varphi_S(x,y)$ (or work function $q\varphi_S$) throughout the surface area under investigation. AFM measurements were conducted in air in semi-contact mode with the use of the a double-pass technique with the resolution of 65,536 pixels ($NN = 256$ points in vertical and horizontal sweeps). The maximum size of the AFM scan area was equal to $100 \times 100 \mu\text{m}^2$. In order to provide semi-contact mode, NSG10 cantilevers were used, which needles had tungsten carbide W_2C plating with electron work function of ≈ 4.9 eV. The instrument influence of the other structural elements of the cantilever at the contact point was not more than 25%.

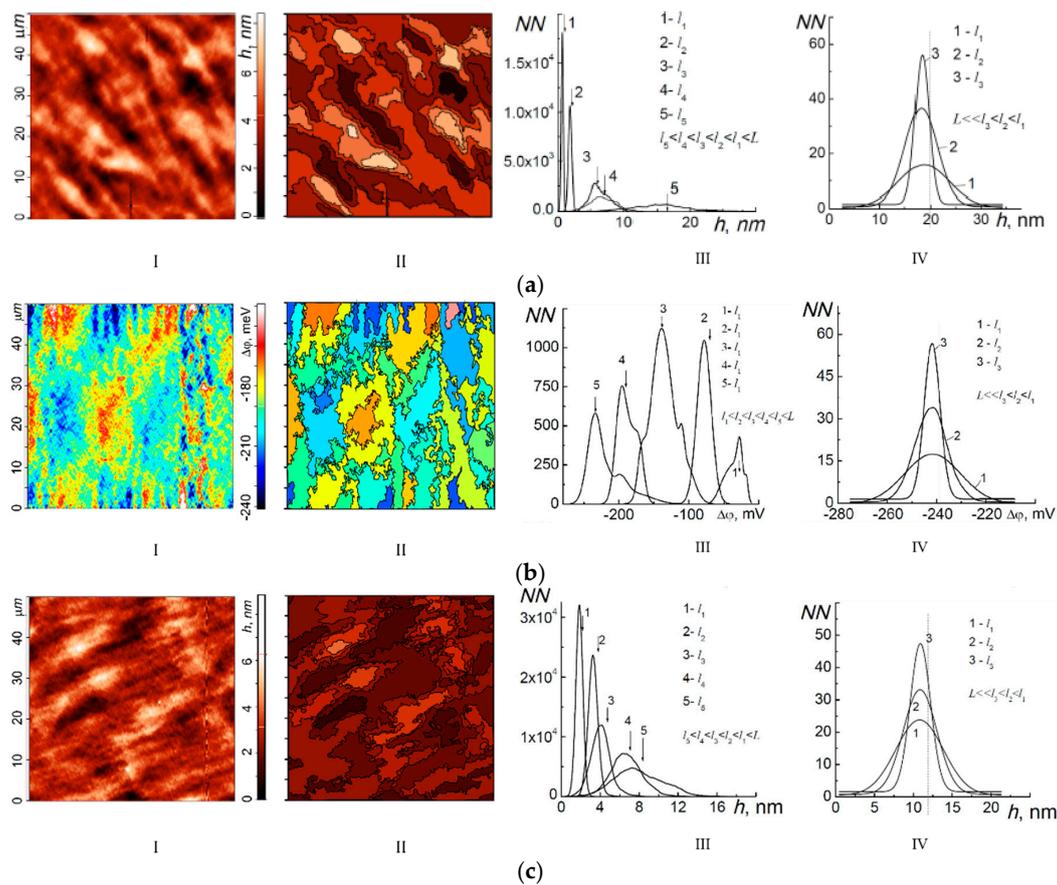


Figure 3. The atomic force microscopy (AFM) images of $50 \times 50 \mu\text{m}$ surface areas (column I): (a) the relief $h(x,y)$ of the AlGaIn/GaN HEMT structure; (b) irregularities of surface potential $\Delta\varphi(x,y)$; (c) the relief $h(x,y)$ of the GaN buffer layer—with the corresponding contour images (column II) and distribution histograms in local (column III) and global (column IV) approximations.

Figure 3a(I) shows the results of the Brownian relief $h(x,y)$ initial check for the HES surface area of $50 \times 50 \mu\text{m}$. The investigation was conducted with the use of the AFM techniques described above. Statistical studies demonstrate that the average values of the relief irregularities $\langle h \rangle$ depend on the measurement technique. As distribution bar charts $h(NN)$ show, at $NN = \text{Const}$, the decrease of the measurement scale $l_i = a_i/NN < L$ through the reduction of the studied area side length a leads to a monotonous decrease of the surface irregularities and roughness average values $\langle h \rangle$ and $\langle R_a \rangle$, correspondingly, in a row: for the area of $100 \times 100 \mu\text{m}$ ($l = 390$ nm) $\langle h \rangle = 15.41$ nm and $\langle R_a \rangle = 3.24$ nm, for the area of $50 \times 50 \mu\text{m}$ ($l = 195$ nm) $\langle h \rangle = 8.47$ nm and $\langle R_a \rangle = 1.99$ nm, for the area of $30 \times 30 \mu\text{m}$ ($l = 117$ nm) $\langle h \rangle = 6.59$ nm and $\langle R_a \rangle = 1.32$ nm, for the area of $10 \times 10 \mu\text{m}$ ($l = 39$ nm) $\langle h \rangle = 1.84$ nm and $\langle R_a \rangle = 0.21$ nm and for the area of $5 \times 5 \mu\text{m}$ ($l = 19.5$ nm) $\langle h \rangle = 0.65$ nm and $\langle R_a \rangle = 0.11$ nm (Figure 3a(III)).

Conversely, in the case of l decreasing through the increase of scan points number NN at the constant size of the area under investigation ($a = \text{Const} = 50 \mu\text{m}$), the average values $\langle h \rangle$ and $\langle R_a \rangle$

grow: at $N = 1256$ ($l = 50/1024 = 0.049 \mu\text{m} = 49 \text{ nm}$) $\langle h \rangle = 15.91 \text{ nm}$ and $\langle R_a \rangle = 3.51 \text{ nm}$, at $N = 512$ ($l = 98 \text{ nm}$) $\langle h \rangle = 9.13 \text{ nm}$ and $\langle R_a \rangle = 1.59 \text{ nm}$, at $N = 256$ ($l = 195 \text{ nm}$) $\langle h \rangle = 6.89 \text{ nm}$ and $\langle R_a \rangle = 1.44 \text{ nm}$, at $N = 128$ ($l = 391 \text{ nm}$) $\langle h \rangle = 2.73 \text{ nm}$ and $\langle R_a \rangle = 0.51 \text{ nm}$, and at $N = 64$ ($l = 781 \text{ nm}$) $\langle h \rangle = 2.68 \text{ nm}$ and $\langle R_a \rangle = 0.31 \text{ nm}$. These data reveal indefiniteness and relativity of the AlGaIn/GaN HES surface relief estimation results (e.g., $\langle h \rangle$ and $\langle R_a \rangle$) in the local limit at $l < L$, and this is one of the basic attributes of chaotic systems [6,7].

Another necessary attribute which gives an opportunity to consider the area surface of the structure under investigation as a chaotic system in local approximation is its strong dependence on environment conditions: Small changes in conditions of growth and processing lead to significant changes in the state of the surface, specifically in relief shape and surface potential.

The investigation of surface geometry by means of tracing shows that at every $r = l_i/l_{i+1}$ -fold decrease of the measurement scale l we obtain a discrete sequence of the embedded sets of non-overlapping contours (Figure 3a(II)) with a step-like increase of their number $N(l)$ —“freaking stairs” (Figure 2a). Figure 3a(II) shows that shapes of the embedded lateral contours of relief irregularities are not self-similar in a literal sense. It is only possible to speak about some statistical self-similarity. The existence of the self-similarity properties of the HES surface area under investigation is revealed by the linear dependence of $\ln(N(l))$ on $\ln(l_0/l)$ (Figure 2c), which makes it possible to consider the observed objects as fractal, or (at the presence of several kinks in linear dependence of $\ln(N(l))$ on $\ln(l_0/l)$) multifractal ones. These results are indicative of the topological mixing properties of the HES under investigation that is a third attribute of chaotic systems.

The average relief values $\langle h \rangle$ measured in the global approximation at $L < l_3 = 50 \mu\text{m}$, $l_2 = 100 \mu\text{m}$, $l_1 = 150 \mu\text{m}$ ($l_3 < l_2 < l_1$) reveal classic dependence: as the measurement scale l decreases, dispersion decreases too (Figure 3a(IV)). In global approximation, l is defined by a step of an AFM objective table along the axes x and y .

An initial check of surface potential irregularities $\Delta\varphi(x,y)$ (11) with the use of AFM techniques (Figure 3b(I)) shows the maximum spread of the values up to 90 mV.

Filling contours obtained after tracing the lateral irregularities of the surface potential form non-overlapping embedded sets (Figure 3b(II)), and these linear sizes in the (x,y) plane, just like the lateral sizes of the relief irregularities, depend upon the measurement scale l . In this case, the existence of the attributes of statistic self-similarity, and subsequently fractality, is proven by the linear dependence of $\ln(N(l))$ upon $\ln(l_0/l)$ (Figure 2d). As Figure 2b shows, the “freaking stairs” can be well approximated with the exponent.

The statistical analysis of the CPD = $\Delta\varphi(x,y)$ irregularities distribution (Figure 3b(III)) also reveals the relativity and indefiniteness of the measurement process. It was shown that a decrease of the size a of the investigated surface area side at $NN = \text{Const}$ leads to a decrease of the average values $\langle \Delta\varphi(x,y) \rangle$, and vice versa, the increase of NN at $a = \text{Const}$ leads to an increase of $\langle \Delta\varphi(x,y) \rangle$. This, in its term, reveals in the local approximation ($l < L$) the relativity and indefiniteness of the measurement process for the AlGaIn/GaN HEMT heteroepitaxial structure surface electrostatic potential.

The environmental conditions exert a strong influence over the average value and the shape of lateral irregularities $\Delta\varphi(x,y)$. Even a slight change in growth and processing conditions leads to significant changes in $\Delta\varphi(x,y)$.

As the result it becomes obvious that in local approximation, the distributions of lateral irregularities of the HES surface potential also have all of the attributes of chaotic systems, which makes fractal geometry a mathematical instrument suitable for their description.

The average values $\langle \Delta\varphi \rangle$ measured in global approximation also exhibit known classic dependence common to integer topological dimensions: as the measurement scale l decreases, the dispersion decreases too (Figure 3b(IV)).

The value of the local approximation limit L for relief irregularities and lateral surface potential irregularities is not less than $100 \mu\text{m}$ —due to AFM device limitations.

The direct investigation of the HES conductive capacity depending on the test element channel width d and length l_{ds} was conducted on the probing station M-150 (Cascade Microtech, Beaverton, Oregon, USA) with the use of the B-1500 semiconductor device parameter analyzer (Keysight Technologies, Santa Rosa, California, USA). To exclude serial resistance of the probe-to-surface contact, the four probes Kelvin technique was used. The resistance measurement error in the bias range from -5 to $+5$ V was not more than 0.04Ω .

Static current–voltage curves of test elements show that, while $d > 30 \mu\text{m}$ and $l_{ds} > 60 \mu\text{m}$, sheet resistance (resistance per square) $\rho_{\square} = (d \times R_{ds})/l_{ds} \approx \text{Const}$ (Figure 1b) within the scope of the admissible values, and channel resistance R_{ds} and drain saturation current I_{ds} have virtually linear dependence on d and l_{ds} . If d and l_{ds} are equal to or less than given values ($d \leq 30 \mu\text{m}$, $l_{ds} \leq 60 \mu\text{m}$), the value $\rho_{\square} = \rho_{\square}(d, l_{ds})$ becomes a function of d and l_{ds} (Figure 1b), and there is nonlinear dependence of values $R_{ds} = R_{ds}(d, l_{ds})$ and $I_{ds} = I_{ds}(d, l_{ds})$. As the result, it is possible to conclude that the given HES sheet resistance $\rho_{\square} = \rho_{\square}(d, l_{ds})$ depends largely on channel length l_{ds} and width d in the range of practically valuable measurement scales (linear sizes)—that is size effect.

Therefore, it was shown that for AlGaIn/GaN HEMT HES in local approximation, size effects were exerted in fractal dependence of its electrophysical parameters values on linear sizes n and d .

Based on the results obtained, it is possible to give the following physical explanation for size effects observed in a given case. The maximum size of irregularities (the level difference of 20–30 nm) and surface relief shape of the grown heteroepitaxial layers (Figure 3a(I)) virtually replicate the relief shape and irregularity size of buffer layer (Figure 3c(I)). It means that heteroepitaxial layers forming a HEMT structure replicate the buffer layer surface irregularities. The analysis of the contour relief image (Figure 3c(II)) shows that its scaling indices ζ and η , and ones of the HES, have close values. At the same time statistical analysis of relief irregularities reveals the same results both in local (Figure 3c(III)) and global (Figure 3c(IV)) approximations.

The facts stated above give the basis to assume that electrons of 2D electron gas move from the source to the drain, not in the two-dimensional, but in the three-dimensional plane, by-passing irregularities of the 2D channel or scattering on them. It is obvious that the number of scattering events is in a direct ratio with the number of scattering centers that are 3D irregularities of 2D channel spread throughout the (x,y) plane in a fractal way. In this case elastic scattering is the reason for the change of the electron pulse vector, and in the general case, it leads to the decrease of the absolute value of their group speed vector component. In the case of non-elastic scattering the quantity of energy lost by electrons is also in direct ratio with the number of scattering centers that are spread throughout the area of the heterojunction in a fractal way, as said above. In both cases, it leads to an increase of channel resistivity R_{ds} , and consequently to the growth of specific values R_{sg} and R_{dg} .

In addition to 2D channel irregularities, the point scattering centers can influence on the electron motion in two-dimensional electron gas. These centers are positive ions of shallow-level impurity N_{D+} , which concentration in the material is equal to $2 \times 10^{18} \text{ cm}^{-3}$. In order to study the geometry of the irregularity distribution $N_{D+}(x,y)$ by the Kelvin method with the use of AFM, the geometry of the CPD irregularity distribution $\Delta\varphi(x,y)$ was investigated. It is possible to estimate that at an average level of the doping of the AlGaIn epitaxial layer $N_{D+} = 2 \times 10^{18} \text{ cm}^{-3}$, the Debye shielding distance (space charge region, SCR) is equal to 17–20 nm that is comparable with the HES thickness. This means that SCR irregularities born by the unequal (fractal) distribution of the ionized shallow-level donor impurity N_{D+} come to the surface, forming there surface potential lateral irregularities distributed in a fractal way. It follows that positive ions of shallow-level donor impurity are also distributed in this fractal way throughout the area of the two-dimensional electron gas. At this, as it is shown in Figure 3b, the lateral sizes of such irregularities can exceed $50 \mu\text{m}$ (limited by the sizes of the area under investigation). The investigations of the $100 \times 100 \mu\text{m}$ surface areas reveal that in this case L can be significantly more than $100 \mu\text{m}$. The more precise definition of L is restricted by the technical capabilities of the used AFM. It is clear that the fractal distribution of the ionized shallow-level donor

impurity forming heteroepitaxial level capacity and consisting of the scattering centers has great influence virtually on all parameters $P_{k,i}$ of the internal transistors of the EC linear model.

In summary, it is possible to assume that the observed size effects can significantly influence on both statistic [28,30,31] and high-frequency (HF) [32–34] device characteristics of the HEMTs under investigation.

4. AlGaIn/GaN HEMT Manufacturing

The paper investigates a 4×5 transistor matrix (Figure 4a) manufactured on the basis of HESs described above (see Section 3). Mesa insulation of the active area with the thickness of 50 nm was formed by means of photoresist mask reactive ion etching. The ohmic contacts (OK) of the drain and the source (D–S) were built using the processes of explosive lithography, TiAlNiAu plating and consecutive annealing in a nitrogen atmosphere at the temperature of 780 °C during 30 ns. The channel length l_{ds} (the distance between the drain and the source) was equal to 5 μm . After that the whole structure was covered with SiO_2 dielectric film of the first mask having the thickness of 0.15 μm , and then with the resist. The openings were formed in the resist mask by means of lithography methods. The edges situated closer to the drain corresponded to the position of the Schottky gates of the fabricated transistors. Then, according to the resist mask, openings in the first dielectric mask were formed by means of chemical etching. The etching time was estimated by vision—until the total elimination of dielectric layer from the surface of the structure. After the resist elimination, the second lithography was conducted to create openings, surrounding the edges of the dielectric mask corresponding to the gate positions. The resist mask edge was situated closer to the drain located on the dielectric surface, and the second one was located on the surface of the channel active area. As a result, there was a gap with the length of $\sim 0.15 \mu\text{m}$ on the HES surface between the dielectric and the resist masks. Therefore, the opening length in the resist mask defined the size of the gate cap, and the offset of the mask relative to the dielectric edge defined the gate length l_g . After NiAu deposition and explosion we obtained the L-shaped “Field-plate” gate with the length $l_g = 0.15 \mu\text{m}$, which cap was shifted to the drain (Figure 4b). At final stage, the glassivation of the structure active area was performed using an SiO_2 dielectric. Then, the openings in this dielectric over the pads were created in order to connect the sources of all transistor sections by air bridges and to perform the galvanic thickening of the drain, source and gate pads.

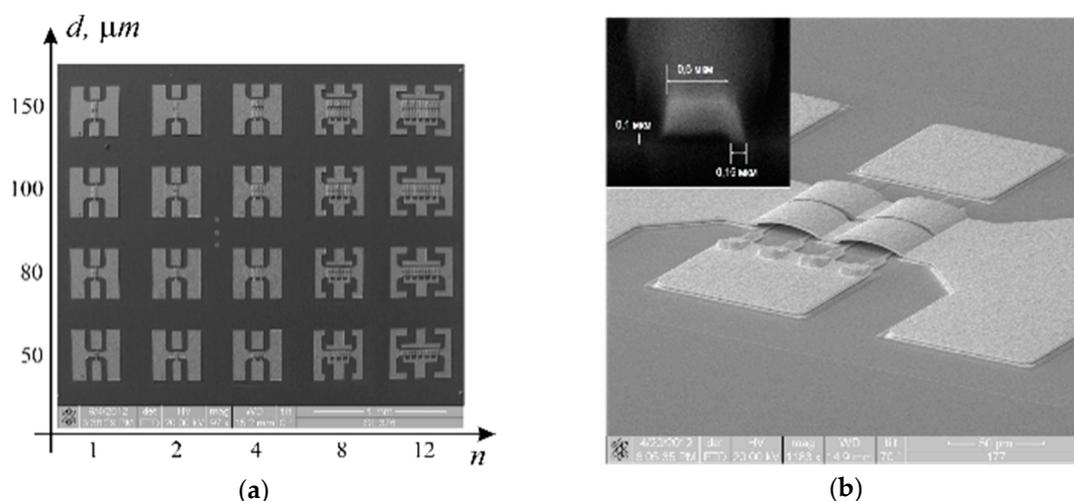


Figure 4. The electron microscopy images: the 5×4 matrix of AlGaIn/GaN HEMTs with section number $n = 1, 2, 4, 8$ and 12 and section width $d = 50, 80, 100$ and $150 \mu\text{m}$ —(a); the AlGaIn/GaN HEMT crystal $4 \times 100 \mu\text{m}$ with the L-shape gate length of $0.15 \mu\text{m}$ (insertion)—(b).

As the result, at the same chip a matrix of AlGaIn/GaN HEMTs with $l_g = 150$ nm was formed. HEMTs differ from each other, not only by section number $n = 1, 2, 4, 8$ and 12 , but also by channel width $d_{ds} = 50, 80, 100$ and 150 μm (Figure 4a).

5. The Methods of Measurement and Extraction of AlGaIn/GaN HEMT Linear Models

In order to validate the stated assumption about the HES fractal geometry impact on AlGaIn/GaN HEMT high-frequency parameters, the reconstruction of the values (measures $M_{k,i}$) of the equivalent circuit components $P_{k,i}$ of the SS model for all transistors in the matrix was performed.

For the purpose above, the measurement of small-signal S -parameters of the manufactured HEMTs in the frequency range of $0.1\text{--}40$ GHz was performed in accordance with the techniques described in [35–39]. The supply voltage was $U_{ds} = 20 \pm 1$ V. The insignificant spread of U_{ds} is due to the optimization of current operating points to achieve maximum values of H_{21} and G_{max} . The measurement was carried out with the use of the PNA-X N5245A 2-port vector network analyzer (Keysight Technologies, Santa Rosa, California, USA) at the Summit 12,000 semi-automatic probe station (Cascade Microtech, Beaverton, Oregon, USA). The calibration of measuring paths of the ports 1 and 2 of the vector network analyzer was performed by the Line-Reflect-Reflect-Match (LRRM) method [35] with measuring S -parameters of the gage structures: (1) microstrip line, per passing through (Thru); (2) $50\ \Omega$ load (Load); (3) open-circuit test element (Open); and (4) short-circuit test element (Short).

After that, the de-embedding procedure for the measured S -parameters was performed according to the technique described in [36–39]. This procedure involved elimination of the influence of the parasitic parameters of the pads, measuring probes and other elements of the measuring assembly, on the measurement results. For the de-embedding we used test elements which had the form of short-connected (“short”) and opened (“open”) pads of gates, drains and sources. Frequency dependences of power $G_{max}(f)$ (Figure 5a) and current $H_{21}(f)$ (Figure 5b) amplification factors of the manufactured HEMTs point out that in average they are compliant with the world analogs.

In this paper, the reconstruction (extraction) of the parameters $P_{k,i}$ of the SS model EC components by the measured S -parameters was carried out in accordance with the combined technique [40,41] incorporating advantages of analytic [26] and optimization [39] methods of the extraction.

According to [9,26,32], the procedure of HEMT small-signal EC extraction included three main stages.

At the first one, the external parasitic parameters of the transistor were defined from the transistor measurements obtained in “cold” modes (at drain zero voltage) and at direct current using techniques represented in [36–38].

At the second stage, the Y -parameters of the internal transistor were defined by means of the subtraction of the external components according to the technique described in [36]. The accuracy of the determination of the internal frequency-independent components is strongly dependent upon the accuracy of the external parameters definition. The solution of the equation system binding the Y -parameters obtained and the parameters of internal components makes it possible to reconstruct the parameter values for these components [37].

At the third stage, the optimization of the values of the internal transistor equivalent circuit components was performed in order to get the best correspondence between the measured and simulated S -parameters. Based on the frequency independence of the internal components [38], an additional objective function was formed—the minimum mean-square deviation of the parameters of the internal components from their constant values [39]. The optimizable values were the parameters of the external components L_g, L_s, L_d, R_g, R_s and R_d of the transistor equivalent circuit (Figure 6).

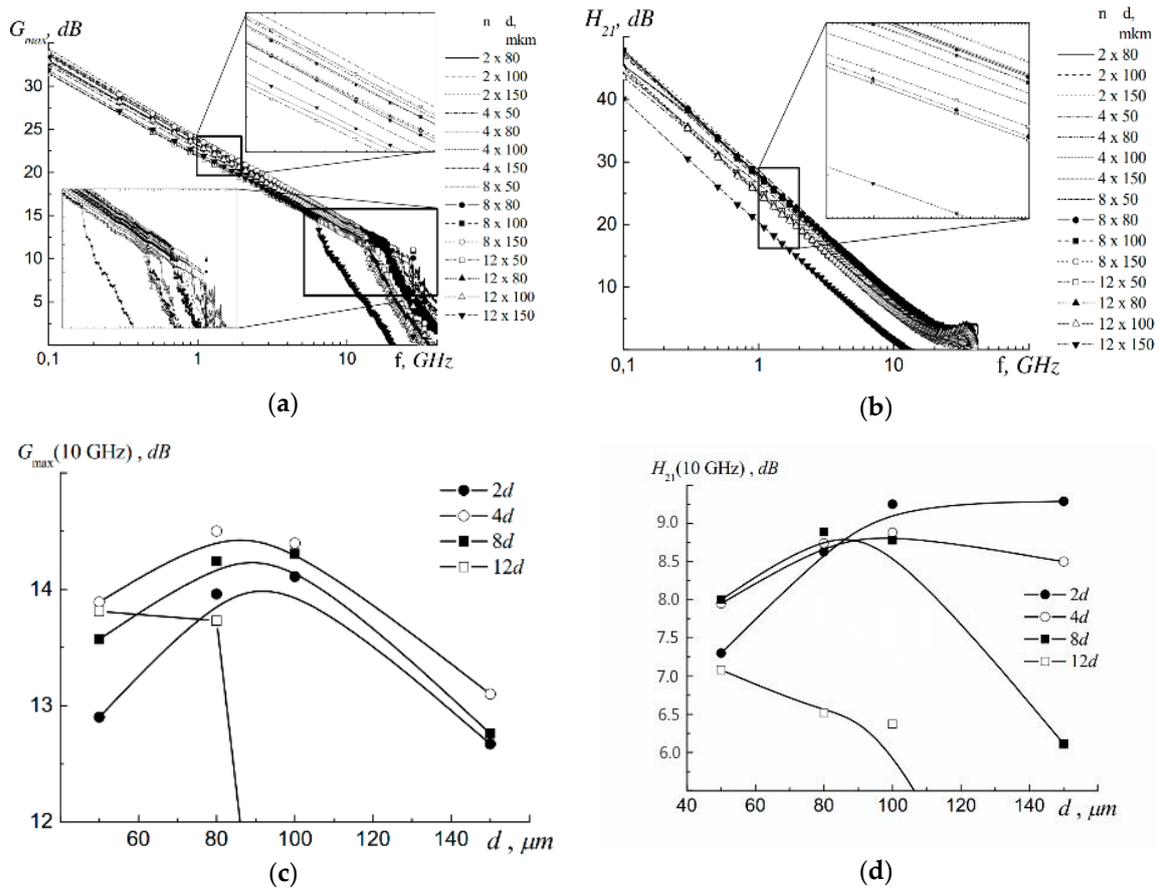


Figure 5. The frequency dependences of maximum values of the power and current amplification factors $G_{max}(f)$ —(a) and $H_{21}(f)$ —(b), correspondingly. The functional dependences $G_{max} = G_{max}(d)$ —(c) and $H_{21} = H_{21}(d)$ —(d), measured at different values of n at frequency $f = 10$ GHz.

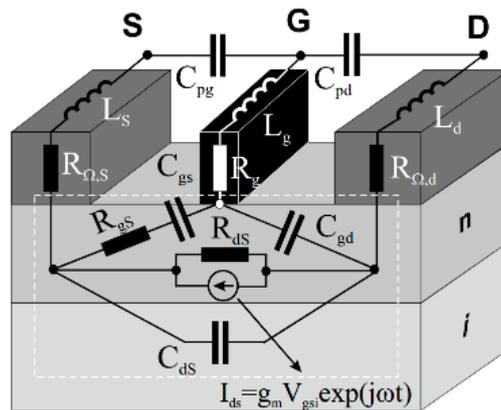


Figure 6. The equivalent circuit of the linear model of field-effect transistor. The components of the internal transistor are circled with a dotted line.

As it is seen in Figure 6, there are internal (encircled with the dotted line: R_{gs} is gate-source resistance; R_{ds} is drain-source resistance of the depletion region; C_{gs} is gate-source capacity; C_{gd} is gate-drain capacity; C_{ds} is drain-source capacity; I_{ds} is drain current source controlled by the gate-source voltage; g_{max} is transconductance; τ is a time constant of the frequency dependence of the current source) and external parts of the field-effect transistor small signal model EC.

In the general case, the values of the HEMT EC external parameters are defined by the parasitic parameters of the assembly components (e.g., pads, connection leads, plated holes). In this paper they are not considered.

The analysis of size effects i.e., the dependences of the measures M_k of the parameters P_k obtained after extraction of the internal transistor EC components on the number of sections n and their width d , was carried out using the notion of Hausdorff–Bezikovich non-integral dimension D_H (fractal D_f) [7] (see Section 2).

In order to estimate the field effect transistor operation at high frequencies, we use their peak characteristics: maximum current amplification factor H_{21} at short-circuit termination, maximum power amplification factor $G_{max} \equiv MUG$ (maximum unilateral gain) in the absence of the feed-back inside the transistor, current-amplification cutoff frequency f_T , and power-amplification cutoff frequency f_{max} .

Conventionally the maximum amplification factors H_{21} and G_{max} are defined through the parameters of scattering matrix (S -matrix) using the following known expressions:

$$G_{max} = \frac{1}{2} \frac{\left| \frac{S_{21}}{S_{12}} - 1 \right|^2}{k \left| \frac{S_{21}}{S_{12}} \right| - R_e \frac{S_{21}}{S_{12}}}, \quad (14)$$

where k is the parameter of stability defined as:

$$k = \frac{1 + |\det S|^2 - |S_{11}|^2 - |S_{22}|^2}{2|S_{12}S_{21}|} > 1, \quad (15)$$

$$H_{21} = \frac{-2S_{21}}{1 - S_{11} + S_{22} - \det S}, \quad (16)$$

where $\det S = S_{11}S_{22} - S_{12}S_{21}$ is determinant of the scattering matrix.

The physical sense of the frequency f_T is specified by the time of electron transmission through the distance between source and drain l_{ds} with the group saturation velocity v_{sat} :

$$f_T = \frac{g_{max}}{2\pi C_{gs}} = \frac{v_{sat}}{2\pi l_{ds}}. \quad (17)$$

The physical sense of the frequency f_{max} characterizes the maximum oscillation frequency up to which the transistor acts as the active component. At the extrapolation of this frequency the transistor begins to act as the passive one.

Power- and current-amplification cutoff frequencies f_{max} and f_T are determined from the following clause: amplification factors $[G_{max}] = \text{dB}$ and $[H_{21}] = \text{dB}$ are equal to the one.

$$H_{21}|_{f=f_T} = 1 \quad G_{max}|_{f=f_{max}} = 1 \quad (18)$$

The value of f_{max} can be both higher and lower than f_T , which is defined by transistor structure peculiarities in every individual case.

6. Experimental Results (Size Effects in AlGaIn/GaN HEMT HF Characteristics)

From (14)–(17) it is seen that H_{21} , G_{max} , f_T , and f_{max} do not explicitly depend upon n and d , as these expressions describe the behavioral structureless model of the field-effect transistor.

At the same time, HF characteristics of the HEMTs under investigation reveal that $G_{max}(f)$ (Figure 5a) and $H_{21}(f)$ (Figure 5b), as well as f_T and f_{max} (Table 1), have non-monotonous dependence on the number n and the width d of the transistor sections. The comparison of the measured values of

current- and power-amplification cutoff frequencies f_T and f_{max} for all n and d shows that they cannot be uniquely described by the known expressions (17) and (18).

Therefore, the analysis of experimental small-signal parameters G_{max} and H_{21} at the frequency $f = 10$ GHz was carried out for all test structures. The experimental dependences $H_{21} = H_{21}(n, d) \Big|_{f=10 \text{ GHz}}$ (Figure 5c) and $G_{max} = G_{max}(n, d) \Big|_{f=10 \text{ GHz}}$ (Figure 5d) are non-monotonous and have maxima over both n and d . Thus, for G_{max} the maximum is observed at $d = 80\text{--}100 \mu\text{m}$ and $n = 2$, and for H_{21} at $n = 4$ at the same values of d , which agrees with the results stated in [34].

Therefore, the experimental results clearly demonstrate the size effect revealed in the dependence of HEMT HF characteristics upon both the number of sections n and their width d .

In order to find out the reasons for dependence of $H_{21}(f)$ and $G_{max}(f)$, and correspondingly, f_T and f_{max} , on n and d , let us compare the internal transistor linear model parameter values $P_{k,i}$ reconstructed in accordance with the technique described above. In the general case, the increase of n and d leads to the proportional (linear) increase of the total width of the transistor channel $W = n \times d$. In the context of transistor EC, it is identical to a parallel connection of elementary resistances and capacitances in the internal transistor model. In accordance with Ohm's law, it should result in a linear change of the resulting EC resistances and capacitances, i.e., the measures M_k of transistor parameter values $P_{k,i}$. However, as it is shown below, in real transistors this situation takes places only in particular cases.

Table 2 contains extracted parameter values $P_{k,i}$ (measures $M_{k,i}$) of internal transistor EC components, grouped by a common feature—the width of the individual section d of the transistor. Thus, data selection 1-1 includes the transistors with $d = 50 \mu\text{m}$ ($n = 4, 8$ and 12), data selection 1-2—the transistors with $d = 80 \mu\text{m}$ ($n = 2, 4, 8$ and 12), data selection 1-3—the transistors with $d = 100 \mu\text{m}$ ($n = 2, 4, 8$ and 12), and data selection 1-4—the transistors with $d = 150 \mu\text{m}$ ($n = 2, 4, 8$ and 12).

The grouping by channel width d reveals the dependence of specific parameter values $P_{k,i}^* = P_{k,i}/W_i$ on section number n : $P_{k,i}^* = P_{k,i}^*(n)$. In each selection the increase of n leads to a slight decrease of specific capacity C/W and transconductance g_m/W and to a significant decrease of specific resistance R/W . The dimensions $D_H(C_{ds})$ and $D_H(C_{gd})$ show the same behavior—their values decrease as n increases. Vice versa, the values $D_H(C_{gs})$ and $D_H(R_{ds})$ increase as n increases. With the n growth the value $D_H(R_{gs})$ increases for the selection m_1 , decreases for the selection m_2 , increases for the selection m_3 and decreases again for the selection m_4 . The comparability of the dimension values $D_H(C_{gs})$ with $D_H(R_{ds})$, as well as $D_H(C_{gd})$ with $D_H(R_{gs})$ and $D_H(g_{max})$ means that the physical mechanisms of the operation of internal transistor parameters C_{gs} and R_{ds} , as well as C_{gd} , R_{gs} and g_{max} are defined by similar geometric dependences.

Table 3 shows the same parameter measure values $P_{k,i}$, grouped by another common feature—the number of sections n in the transistor—for the EC components of internal transistors of the same test structure. Data selection 2-1 consists of transistors with $n = 2$ ($d = 80 \mu\text{m}, 100 \mu\text{m}$ and $150 \mu\text{m}$), data selection 2-2 includes transistors with $n = 4$ ($d = 50 \mu\text{m}, 80 \mu\text{m}, 100 \mu\text{m}$ and $150 \mu\text{m}$), data selection 2-3 incorporates transistors with $n = 8$ ($d = 50 \mu\text{m}, 80 \mu\text{m}, 100 \mu\text{m}$ and $150 \mu\text{m}$) and data selection 2-4 gathers transistors with $n = 12$ ($d = 50 \mu\text{m}, 80 \mu\text{m}, 100 \mu\text{m}$ and $150 \mu\text{m}$).

Table 1. The cutoff frequencies f_T and f_{max} of the manufactured AlGaIn/GaN HEMTs with the gate length $l_g = 0.15 \mu\text{m}$, at various n and d .

The Total Gate Width: $n \times d = W, \mu\text{m}$															
	4×50 = 200	8×50 = 400	12×50 = 600	2×80 = 160	4×80 = 320	8×80 = 640	12×80 = 960	2×100 = 200	4×100 = 400	8×100 = 800	12×100 = 1200	2×150 = 300	4×150 = 600	8×150 = 1200	12×150 = 1800
f_T , GHz calc. (17)	60.27	61.69	54.99	72.32	71.03	81.49	56.15	84.59	79.82	65.08	54.51	77.71	109.13	82.08	62.74
f_T , GHz calc. (21)	81.58	82.69	73.71	132.95	129.88	75.56	86.51	144.06	119.14	96.35	78.15	115.06	65.65	99.26	61.17
f_{max} , GHz calc. (22)	69.46	70.28	57.16	108.57	103.60	50.08	65.41	95.83	86.01	67.89	63.13	80.68	46.88	42.46	43.80
f_T , GHz experiment	23.32	24.98	24.45	27.01	25.56	24.79	20.01	30.01	25.51	22.34	19.98	27.48	27.19	18.61	12.47
f_T , GHz calc. (10)	28.20	25.77	24.44	29.15	25.20	21.79	20.01	29.63	25.44	21.84	19.98	32.81	22.57	15.52	12.46
f_{max} , GHz experiment	58.01	45.20	35.03	63.40	56.37	32.11	25.50	62.03	55.08	30.00	25.13	51.00	50.03	28.10	20.01
f_{max} , GHz calc. (10)	71.53	47.85	37.82	63.91	45.82	32.85	27.04	79.84	51.59	33.34	25.82	58.92	38.79	25.54	20.00

Table 2. The parameters $P_{k,i}$ of the AlGaIn/GaN HEMT internal transistor EC components, grouped by channel width $d = \text{Const}$.

Group №	n	$d, \mu\text{m}$	$W = n \times d, \mu\text{m}$	C_{ds}, pF	$D_H(C_{ds})$	C_{gd}, pF	$D_H(C_{gd})$	C_{gs}, pF	$D_H(C_{gs})$	$g_{max}, \text{A/V}$	$D_H(g_{max})$	R_{gs}, Ω	$D_H(R_{gs})$	R_{ds}, Ω	$D_H(R_{ds})$	R_s, Ω	R_d, Ω	R_g, Ω
1-1	4	50	200	0.107	1.81	0.0267	1.81	0.202	2.23	0.0405	1.83	6.42	1.85	1300	2.24	3.95	9.10	1.60
	8	50	400	0.175	1.86	0.0437	1.86	0.338	2.45	0.0678	1.87	2.85	1.69	482	2.15	2.85	4.98	0.91
	12	50	600	0.267	1.79	0.0667	1.79	0.523	2.59	0.0922	1.90	3.48	2.14	359	2.30	1.8	4.21	1.39
$\langle D_H \rangle$					1.82		1.82		2.42		1.87		1.89		2.23			
1-2	2	80	160	0.0676	1.92	0.0169	1.92	0.155	2.22	0.0307	1.87	6.78	1.79	1200	2.09	1.65	7.85	3.04
	4	80	320	0.128	1.93	0.0319	1.93	0.291	2.35	0.0571	1.85	4.07	1.81	385	1.87	0.72	4.52	2.12
	8	80	640	0.254	1.88	0.0635	1.88	0.571	2.60	0.130	1.63	1.44	1.35	278	2.14	2.06	4.05	5.21
	12	80	960	0.397	1.74	0.0992	1.76	0.835	2.91	0.140	1.81	4.00	1.29	295	2.95	0.23	3.01	0.81
$\langle D_H \rangle$					1.87		1.87		2.52		1.79		1.56		2.26			
1-3	2	100	200	0.0736	1.98	0.0184	1.98	0.183	2.27	0.0391	1.84	6.82	1.87	696	1.95	1.94	5.53	3.47
	4	100	400	0.160	1.91	0.0400	1.91	0.383	2.37	0.0802	1.75	4.36	1.98	557	2.24	0.75	4.13	2.55
	8	100	800	0.345	1.75	0.0863	1.75	0.618	2.94	0.141	1.70	3.54	2.56	301	2.55	0.30	2.88	1.65
	12	100	1200	0.520	1.48	0.130	1.48	1.09	1.88	0.178	1.83	2.04	2.76	230	2.96	0.10	2.75	1.95
$\langle D_H \rangle$					1.78		1.78		2.37		1.78		2.29		2.43			
1-4	2	150	300	0.117	1.94	0.0293	1.94	0.269	2.34	0.0571	1.82	4.14	1.79	510	1.99	1.61	5.47	5.01
	4	150	600	0.232	1.91	0.0579	1.91	0.791	2.21	0.159	1.41	1.12	1.10	298	2.13	3.71	4.35	3.29
	8	150	1200	0.452	1.83	0.113	1.83	1.54	2.64	0.233	1.16	3.48	1.19	164	1.60	1.22	1.10	1.20
	12	150	1800	0.632	—	0.158	—	2.99	—	0.249	—	1.00	—	85.8	—	1.24	2.00	1.56
$\langle D_H \rangle$					1.89		1.89		2.39		1.46		1.36		1.91			

Table 3. The parameters $P_{k,i}$ of the AlGaIn/GaN HEMT internal transistor EC components, grouped by section number $n = Const.$

Group №	n	$d, \mu\text{m}$	$W = n \times d, \mu\text{m}$	C_{ds}, pF	$D_H(C_{ds})$	C_{gd}, pF	$D_H(C_{gd})$	C_{gs}, pF	$D_H(C_{gs})$	$g_{max}, \text{A/V}$	$D_H(g_{max})$	R_{gs}, Ω	$D_H(R_{gs})$	R_{ds}, Ω	$D_H(R_{ds})$
2-1	2	80	160	0.0676	1.92	0.0169	1.92	0.155	2.22	0.0307	1.87	6.78	1.79	1200	2.09
	2	100	200	0.0736	1.98	0.0184	1.98	0.183	2.27	0.0391	1.84	6.82	1.87	696	1.95
	2	150	300	0.117	1.94	0.0293	1.94	0.269	2.34	0.0571	1.82	4.14	1.79	510	1.99
$\langle D_H \rangle$				1.95		1.95		2.28			1.84		1.82		2.01
2-2	4	50	200	0.107	1.81	0.0267	1.81	0.202	2.23	0.0405	1.83	6.42	1.85	1300	2.24
	4	80	320	0.128	1.93	0.0319	1.93	0.291	2.35	0.0571	1.85	4.07	1.81	385	1.87
	4	100	400	0.160	1.91	0.0400	1.91	0.383	2.37	0.0802	1.75	4.36	1.98	557	2.24
	4	150	600	0.232	1.91	0.0579	1.91	0.791	2.21	0.159	1.41	1.12	1.10	298	2.13
$\langle D_H \rangle$				1.89		1.89		2.29			1.71		1.69		2.12
2-3	8	50	400	0.175	1.86	0.0437	1.86	0.338	2.45	0.0678	1.87	2.85	1.69	482	2.15
	8	80	640	0.254	1.88	0.0635	1.88	0.571	2.60	0.130	1.63	1.44	1.35	278	2.14
	8	100	800	0.345	1.75	0.0863	1.75	0.618	2.94	0.141	1.70	3.54	2.56	301	2.55
	8	150	1200	0.452	1.83	0.113	1.83	1.54	2.64	0.233	1.16	3.48	2.94	164	2.60
$\langle D_H \rangle$				1.83		1.83		2.66			1.59		2.14		2.36
2-4	12	50	600	0.267	1.79	0.0667	1.79	0.523	2.59	0.0922	1.90	3.48	2.14	359	2.30
	12	80	960	0.397	1.74	0.0992	1.76	0.835	2.91	0.140	1.81	4.00	2.90	295	2.95
	12	100	1200	0.520	1.48	0.130	1.48	1.09	—	0.178	1.83	2.04	2.76	230	2.96
	12	150	1800	0.632	—	0.158	—	2.99	—	0.249	—	1.00	—	85.8	—
$\langle D_H \rangle$				1.67		1.68		2.75			1.85		2.60		2.74

The grouping by the parameter n in some cases leads to the similar changes of parameter values $P_{k,i}$ depending on d (Table 3). Thus, the increase of d per data selection results in the slight decrease of specific values $C_{gd}^* = C_{gd}/W$ and more significant decrease of the values $R_{gd}^* = R_{gd}/W$ and $R_{ds}^* = R_{ds}/W$. At this, the values $C_{ds}^* = C_{ds}/W$ for the data selections 2-1 and 2-2 decrease, and for the data selections 2-3 and 2-4, having greater values of d , do not virtually change. With the increase of d the specific values C_{gs}^* decrease for the data selection 2-1, have minimum value for the data selection 2-2, decrease again for the data selection 2-3 and have maximum value for the data selection 2-4. The behavior of specific values $g_m^* = g_m/W$ in this case has also some peculiarities. The increase of the parameter d virtually in no way influence on the values $g_{m,max}^*$ in data selection 2-1, leads to the growth of $g_{m,max}^*$ values in data selections 2-2 and 2-3, results in slight change of $g_{m,max}^*$ in data selection 2-4. The dimensions $D_H(C_{gd})$ and $D_H(C_{ds})$ have almost the equal values slightly changing at the change of d . The values $D_H(C_{gs})$ change inappreciably for data selections 2-1 and 2-2, and grow with d growth for data selections 2-3 and 2-4. The $D_H(g_m)$ values decrease as d increase.

There is an illustrative variant of the comparison when transistors have the same total (full) channel width $W = n \times d = Const$ at different values of n and d . Table 4 shows the same parameter values $P_{k,i}$ of the internal transistor linear model components, grouped by one more common feature—a channel total width $W = n \times d$. Each data selection includes parameters $P_{k,i}$ of the internal transistors having the same channel total width W , but different values of n and d . Data selection 3-1 consists of transistors with $W = 200 \mu\text{m}$ ($n \times d = 4 \times 50 \mu\text{m}$ and $n \times d = 2 \times 100 \mu\text{m}$), data selection 3-2 includes transistors with $W = 400 \mu\text{m}$ ($n \times d = 8 \times 50 \mu\text{m}$ and $n \times d = 4 \times 100 \mu\text{m}$), data selection 3-3 incorporates transistors with $W = 600 \mu\text{m}$ ($n \times d = 12 \times 50 \mu\text{m}$ and $n \times d = 4 \times 150 \mu\text{m}$) and data selection 3-4 gathers transistors with $W = 1200 \mu\text{m}$ ($n \times d = 12 \times 100 \mu\text{m}$ and $n \times d = 8 \times 150 \mu\text{m}$).

According to the classical concepts, the values of the parameter measures $P_{k,i}$ of the internal transistors with the same W must be equal or at least close to each other. However, as it follows from the Table 4, for the selections 3-1, 3-3 and 3-4 ($W = 200, 600$ and $1200 \mu\text{m}$) the decrease of d leads to the monotonous increase of R_{ds} . For the selection 3-2 ($W = 400 \mu\text{m}$) the decrease of d results in a decrease of R_{ds} . In the general case, changing R_{ds} can exceed tens of percent.

The values $R_i \equiv R_{gs}$ in most cases (for $W = 200, 400$ and $1200 \mu\text{m}$, except $W = 600 \mu\text{m}$) decrease as d decreases. Here, the changing of R_{gd} can be up to 40 percent.

The values C_{gd} and C_{ds} increase with the decrease of d at all values $W = 200, 400, 600$ and $1200 \mu\text{m}$, and the value C_{gs} have such behavior at $W = 400, 600$ and $1200 \mu\text{m}$ (except $W = 200 \mu\text{m}$).

The decrease of d virtually in all cases (at $W = 400, 600$ and $1200 \mu\text{m}$, except $W = 200 \mu\text{m}$) leads to decrease of transconductance g_{max} , which corresponds to the results in [28,32].

Therefore, the results obtained prove that the way of change of internal transistor parameters is defined in most cases by the way of channel total width $W = n \times d$ derivation. As it is shown below, in the studied range of measurement scales, this is largely owing to the HES fractal geometry.

Table 4. The parameters $P_{k,i}$ of the AlGaIn/GaN HEMT internal transistor EC components, grouped by channel total width $W = \text{Const}$.

Group №	n	$d, \mu\text{m}$	$W = n \times d, \mu\text{m}$	C_{ds}, pF	$D_H(C_{ds})$	C_{gd}, pF	$D_H(C_{gd})$	C_{gs}, pF	$D_H(C_{gs})$	$g_{max}, \text{A/V}$	$D_H(g_{max})$	R_{gs}, Ω	$D_H(R_{gs})$	R_{ds}, Ω	$D_H(R_{ds})$
3-1	4	50	200	0.107	1.81	0.0267	1.81	0.202	2.13	0.0405	1.98	6.42	1.34	1300	2.16
	2	100	200	0.0736	2.01	0.0184	2.01	0.183	2.19	0.0391	1.99	6.82	1.38	696	1.81
$\langle D_H \rangle$					1.91		1.91		2.16		1.985		1.36		1.985
3-2	8	50	400	0.175	1.87	0.0437	1.87	0.338	2.38	0.0678	2.12	2.85	1.18	482	1.98
	4	100	400	0.160	1.95	0.0400	1.95	0.383	2.27	0.0802	1.97	4.36	1.21	557	2.11
$\langle D_H \rangle$					1.91		1.91		2.325		2.045		1.195		2.045
3-3	12	50	600	0.267	1.76	0.0667	1.76	0.523	2.56	0.0922	2.34	3.48	1.03	359	2.13
	4	150	600	0.232	1.97	0.0579	1.97	0.791	1.96	0.159	1.55	1.12	2.64	298	1.86
$\langle D_H \rangle$					1.865		1.865		2.26		1.945		1.835		1.995
3-4	12	100	1200	0.520	—	0.130	—	1.09	—	0.178	—	2.04	—	230	—
	8	150	1200	0.452	—	0.113	—	1.54	—	0.233	—	3.48	—	164	—

7. Discussion of the Experimental Results

In literature it has already been noted [13] that in the general case the expressions (14)–(17), defining the limit values of high frequency parameters of field-effect transistors, do not work for the real transistors with various n and d values. The authors in [13] suppose that it can be associated with not taking into account the influence of some (so-called parasitic) components R_s and R_d of the external transistor EC (Table 2). With these corrections, according to [13], the expressions for f_T and f_{max} have the following form:

$$f_T = \left[2\pi \left(\frac{L_g}{v_e} + C_{gs}(R_s + R_d) \right) \right]^{-1}, \quad (19)$$

$$f_{max} = \frac{f_T}{\sqrt{(R_g + R_s + R_i) \left(\frac{1}{R_D} + g_m \frac{C_{gd}}{C_{gs}} \right)}}. \quad (20)$$

However, these formulae, as well as many others (e.g., [15,16]) in most cases also do not constitute a satisfactory match with experimental results, which is highlighted by the comparison of the experimental values of f_T and f_{max} for the manufactured transistors with the ones calculated according to the expressions (19) and (20) (Table 1) with the use of the external transistor EC parameters (parasitic parameters) R_s , R_d and R_g (Table 2). It should be pointed out that external parasitic parameters are commonly believed to directly depend on linear sizes, so they cannot explain any non-linear effects observed.

Therefore, at the present time, the above noted non-linear behavior of amplification factor experimental values $G_{max} = G_{max}(n, d)$ and $H_{21} = H_{21}(n, d)$, as well as the ones of cutoff frequencies $f_T = f_T(n, d)$ and $f_{max} = f_{max}(n, d)$ depending on n and d have no necessary physical explanation. In our opinion, the reason for it consists in a failure to take into account the relation of the internal transistor linear model parameters $P_{k,i}$ and non-linear—fractal in particular—parameters of HES electrophysical properties. It is possible to assume, that the reason of the divergences of the experimental and calculated results observed in [13–16,33] is associated with the fact that (19) and (20) do not take into account such important parameters $P_{k,i}$ of the internal transistor linear model as C_{ds} and C_{gd} , C_{gs} , g_{max} , $R_{gs} = R_i$ and R_{ds} , which in local approximation are totally defined by non-linear (fractal) HES electrophysical parameters $p_{k,i}$ depending on n and d .

In the general case, functions describing both HES physical parameters $p_{k,i}$ and parameters $P_{k,i}$ do not immediately (directly) reveal size effects by themselves (they are not fractal). The fractality occurs only after taking into account the dependence of these functions upon spatial coordinates (x, y, z) , which gives the opportunity to do a quantitative description of the observed size effects.

As a result, the values C_{ds} and C_{gd} , C_{gs} , g_{max} , $R_{gs} = R_i$ and R_{ds} of the parameters $P_{k,i}$ (Table 2) and consequently H_{21} , G_{max} , f_T and f_{max} (Table 1), become functions of the parameters n and d , which in accordance with that which is stated above makes it possible to operate with these values as with subspace elements S_M .

In order to quantitatively describe size effects in subspaces S_M formed by the measures $M_{k,i}$ of the parameters $P_{k,i}$, i.e., in order to find out the dependences of the measures $M_{k,i}$ on n and d with the use of (8) and (9), it is necessary to determine Hausdorff dimensions D_H of the subspaces M_k . It is worth to remember that functional spaces M_k defined at the metric space R have all its geometrical properties, which allow using the expression (8) to determine their D_H .

For this purpose, let us use the measures $M_{k,i}$ of the parameters $P_{k,i}$ in the expression (8), which correspond to different total widths of the transistor channel $W = n \times d$. For example, if the initial value M_0 in every such subspace $S_M(M_{k,i})$ of the transistor parameter values $P_{k,i}$ is equal to the parameter value $P_{k,0}$ corresponding to the maximum channel total width $W_0 = 1800 \mu\text{m}$, then $\eta = P_{k,i}(W_i)/P_{k,0}(W_0)$, and $\zeta = W_i/W_0$. The dependences $\ln(P_{k,i}/P_{k,0})$ on $\ln(W_0/W_i)$ written in log–log scale are well approximated by the straight lines (Figure 7), which in accordance with that stated

above (see Section 3) point out the presence of self-similarity properties of subspaces S_M . This make it possible to operate with the studied subspaces of the measures of the parameter $P_{k,i}$ as with the fractal or multifractal objects, and use the expression (8) to define their $D_{H(f)}$ (Table 2). The values $D_{H(f)}$ of the resistances R_i and R_{ds} are calculated by their inverse values—conductivities $G_i = 1/R_i$ and $G_{ds} = 1/R_{ds}$. Such substitution does not change the absolute values $|D_f|$, but keeps its positive sign and dimensional subsequence.

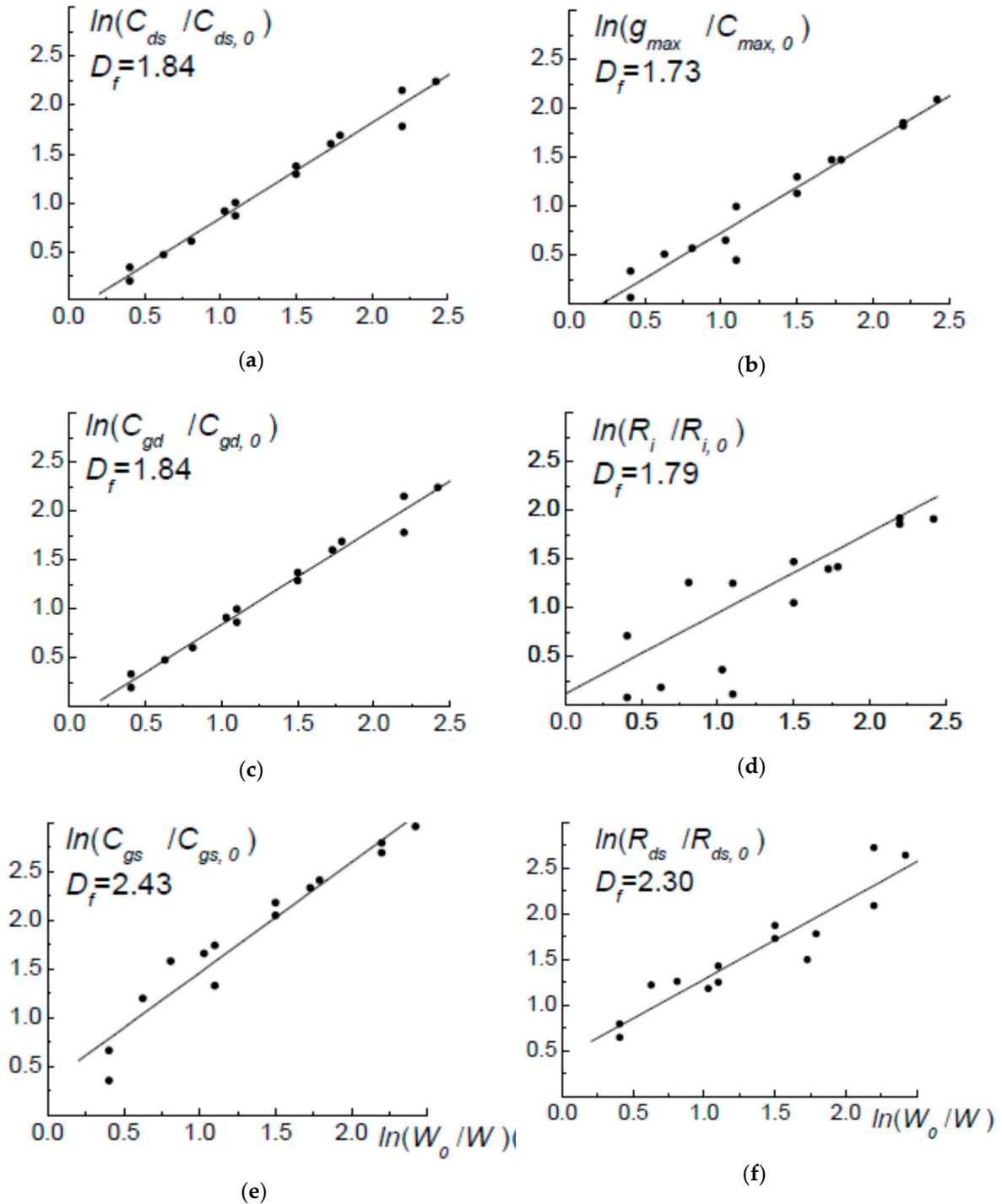


Figure 7. The linear dependences on $\ln(W_0/W)$ written in log–log scale: $\ln(C_{ds}/C_{ds,0})$ —(a); $\ln(C_{gd}/C_{gd,0})$ —(b); $\ln(C_{gs}/C_{gs,0})$ —(c); $\ln(g_{max}/g_{max,0})$ —(d); $\ln(R_i/R_{i,0})$ —(e); $\ln(R_{ds}/R_{ds,0})$ —(f).

According to Figure 7 and Table 2, every subspace $S_M(M_{k,i})$ of the selections of the parameters $P_{k,i}$ has its own value $D_{H(f)}$ in the defined ranges. This points out the multifractality of the subspaces $S_M(M_{k,i})$, characterized by several values $D_{H(f)}$ depending on linear sizes (in our case on n and d). For instance, for the selection 1-1, the range of the Hausdorff dimension D_H values of the subspaces of capacities C_{ds} and C_{gd} measures is 1.79–1.86, of the subspace of capacity C_{gs} measures it is 2.23–2.59, of the subspace of resistance R_{ds} measures this is 2.15–2.30, and so on. As it is reported in Section 2.2 and below, in this case it is convenient to use average values of the dimensions $\langle D_H \rangle$ for each selection (Tables 2–4).

Thus, the average values $\langle D_H \rangle = 1.82, 1.87, 1.78$ and 1.89 for C_{ds} (Figure 7a) and C_{gd} (Figure 7b) capacity parameters subspace—close to 2—mean that in spite of the linearity of the channel the law of change of their values is close to the square one. The proximity of the spatial dimension values $\langle D_H \rangle(C_{ds})$ and $\langle D_H \rangle(C_{gd})$ to the spatial dimension value $D_H = 1.85$ of the lateral irregularities of the subsurface potential point out the connection between the geometry of the functional space of the used HES electrophysical characteristics $p_{k,i}$ and the device characteristics of the transistor linear model EC components. The fractal values $\langle D_H \rangle(g_{max})$ and $\langle D_H \rangle(R_i)$ of parameter measure subspaces g_{max} (Figure 7d) and R_i (Figure 7e), which values are also close to $\langle D_H \rangle = 1.87, 1.79$ and 1.78 , behave in the similar way. In such cases, the values $\langle D_H \rangle$ close to 2 are probably defined by the assembly of one-dimensional transistor channels distributed in HES two-dimensional surface—the more n , the closer to two values D_H .

The values of the fractal dimensions of the capacity C_{gs} parameter subspaces (Figure 7c) exceeding 2— $\langle D_H \rangle(C_{gs}) = 2.42, 2.52, 2.37$ and 2.39 —point out the fact that their physical nature is defined not only by two-dimensional effects, but in some degree also by three-dimensional ones (Table 2). The resistive parameter measure subspaces behave the similar way— $\langle D_H \rangle(R_{ds}) = 2.23, 2.26$ and 2.43 (Figure 7f). This means that their behavior is also strongly influenced by the third dimension. The D_H values proximity to three can be explained by the fact that the R_{ds} and C_{gs} parameters are defined by flowing 2D-electrons of the HEMT structure channel, not through the 2-dimensional, but through the 3-dimensional ($D_T = 3$) plane, which corresponds to the results represented in [12,30].

Because of the foregoing, it is possible to give the following interpretation of the size effects observed in linear models, and subsequently, in the HF characteristics of AlGaIn/GaN HEMT.

The parameter values $p_{k,i}$ of the studied HES electrophysical characteristics in the local approximation have non-linear dependence on linear sizes l and d (e.g., Figure 1b). Subsequently, the parameter values $P_{k,i} = P_{k,i}(n,d)$ of the internal transistor EC components defined by them have non-linear dependence on n and d with all that it implies.

Specifically, the dependence of $P_{k,i} = P_{k,i}(W)$ on the way of $W = n \times d$ obtaining—the variant of comparison—leads to the fact that every such variant of comparison belongs to the individual subspace S_M , each of which can be characterized by the own average value $\langle D_H \rangle$ (Tables 2–4). The usability of one or another comparison variant depends on the individual case.

Thus, being aware of the corresponding dimension $D_{f(H)}(n,d)$ of the subspace S_M and the known values of the measures $M_{k,0}$ of the parameters $P_{k,0}$ of the initial transistor, from (9) it is possible to reconstruct the values $M_{k,i}$ belonging to this subspace of the parameters $P_{k,i}$ of another transistor with some predefined n and d . For example, for the first comparison variant ($d = Const$):

$$M_{k,i+1} = M_{k,i} \left(\frac{n_i}{n_{i+1}} \right)^{2D_T - D_f}, \quad (21)$$

where D_f is fractal dimension of the metric subspace with the measure of the parameter $P_{k,i}$. The similar expressions can be written for other comparison variants too. In the expression (21) the relation of electrophysical parameters $P_{k,i}$ of the HES with the values (measures) of the EC small-signal model parameters $P_{k,i}$ is realized through the HES fractal dimension (in general case, Hausdorff dimension).

These actions, together with the reconstruction of the values of the desired transistor linear model parameters, lead to obtaining necessary sizes of its structural elements. This significantly speeds up the development, optimization and approbation processes for the HEMTs with predefined device characteristics.

Therefore, the results obtained point out the existence of the similarity phenomenon, not only in HES metric spaces R , but also in corresponding to them functional spaces (metric spaces with the measure $S_M = \{R, M_k\}$) of their electrophysical characteristic measures, which in the general case is not obvious. At this, the similarity dimensions $D_S(R)$ and $D_S(S_M)$ of subspaces R and S_M have the values close enough. Probably, it can reveal the existence of some transformation Π between HES metric space R and the defined on it metric space with the measure $S_M(p_{k,i})$:

$$S_M(p_{k,i}) = \Pi \times R. \quad (22)$$

In a similar way to (22), it is possible to make another conclusion: there must be a transformation Π^* which associates the metric space $S_M(P_{k,i})$ of linear (or non-linear) transistor model parameters $P_{k,i}$ with metric space $S_M(p_{k,i})$ of electrophysical parameters $p_{k,i}$ of the transistor's materials and structural elements (HES structure, ohmic contacts, gates, passivating coatings, connections, etc.):

$$S_M(P_{k,i}) = \Pi^* \times S_M(p_{k,i}). \quad (23)$$

For example, the transformations Π and Π^* can be represented in the form of a physical or mathematical law. Thus, being aware of the spatial dimension of the semiconductor structure geometry and its additive electrophysical characteristics depending on it in local approximation, it is possible to reconstruct, for the individual n and d from the set R , the parameter values of the linear model internal transistor EC components through the transformations Π and Π^* . The further investigations are necessary to find out and analyze Π and Π^* .

The recently found size effects in the electrical characteristics of ohmic contact [30] and thin-film metallization [42–44] point out that in the general case it is necessary to take into account external (parasitic) components of the compact model EC to give a full description of size effects in field-effect transistors. The values of such parasitic components also depend in local approximation on the linear sizes of external transistor structural elements.

The proposed size approach (9) and (21) in most cases gives the opportunity to predict the behavior of f_T and f_{max} depending on n and d , even without using compact models. As it is shown in Table 1, the values f_T and f_{max} calculated from (21) have the best correspondence to the experimental ones. In this case, the cycle of high-frequency oscillations $T = 1/f$ is used as the measure, for it is additive value. Thus, the increase of channel length leads to the increase of the time τ during which electrons pass through it and, consequently, to the growth of the value T . At this, the average values of fractal dimensions $\langle D_H(g_{max}) \rangle$ are used to calculate f_T : 1.87 for the selection 1-1, 1.79 for the selection 1-2, 1.78 for the selection 1-3, 1.46 for the selection 1-4. The average values $\langle D_H(C_{gs}) \rangle$ are used to calculate f_{max} : 2.42 for the selection 1-1, 2.52 for the selection 1-2, 2.37 for the selection 1-3, 2.39 for the selection 1-4. The initial values M_0 are equal to the experimental values of the oscillation cycles T corresponding to the maximum value W .

In spite of the channel and gate linear geometry ($D_T = 1$), the values $\langle D_H \rangle$ used for cutoff frequency f_T and maximum frequency f_{max} calculation have the average dimension values $\langle D_H(R_{ds}) \rangle$ and $\langle D_H(C_{gs}) \rangle$ close to two and three, correspondingly (Tables 2–4). This fact reveals the strong influence of the two-dimensional effects on f_T values (defined by the pattern of linear channels' distribution in the two-dimensional plane of the transistor) and three-dimensional effects on f_{max} values (the peculiarities of the scattering 2D-electrons during their motion in three-dimensional plane), that is not obvious.

In addition to this stated above, the results represented in Tables 2–4 give the opportunity to find out a geometrical attribute which implies the following: in local approximation the fractal dimension of the studied object can by itself depend on reference coordinates (or point of view) of the observer.

Thus, at HEMT structure optimization by means of selecting section number n , it is necessary to use certain average values $\langle D_H \rangle$ (Tables 2 and 3), and by means of selecting the values d and W —the other average values (Tables 3 and 4). This fact can probably point out the existence of some superior attribute of chaotic systems.

The results obtained make it possible to form some hypothesis for the relationship between the measure of the fractal object and the Hausdorff dimension of its space in a local approximation: “If the metric space dimension of the object under investigation depends on the point of view (the state) of the observer, then the measure value (functional) of this object will also depend on the observer’s point of view.” In other words: “In order for the measure (functional) of the object under investigation to depend on the point of view (state) of the observer, it is necessary and sufficient for this object’s metric subspace dimension to depend on the observer’s point of view.”

8. Conclusions

The investigation of size effects manifested themselves in the dependence of AlGaIn/GaN HEMT high-frequency characteristics on the channel width d and section number n , was carried out with the use of the notions of measure, metric and normalized functional (linear) spaces, which gave the opportunity to consider scalar and vector values, continuous functions and various numerical sequences from the same point of view.

According to the results obtained, the similarity phenomenon can exist, not only in HES metric spaces, but also in the defined on them functional spaces (metric spaces with a measure) of the measures of their additive electrophysical characteristics and parameter values of internal transistor equivalent circuit components, which is not obvious in the general case. Thereafter, the nature of size effects is in large measure defined by the dependence of AlGaIn/GaN HEMT high-frequency characteristics on the scale dependence in local approximation (at $l < L$) of additive electrophysical properties of the material used for their manufacturing.

In the particular case, the law of mutual transformation of the internal transistor equivalent circuit component parameters belonging to the same functional space determined on R has been found. Thus, being aware of $D_{f(H)}$ and initial values of the measures $M_{k,0}$ of the parameters $P_{k,0}$ for the initial transistor, it is possible to find out the values $M_{k,i}$ of the parameters $P_{k,i}$ of any other transistor made from the same material (with the same value $D_{f(H)}$) and with predefined n and d . The proposed approach makes it possible, not only to define the sizes of the desired transistor structural elements, but to also reconstruct simultaneously the parameters of its linear (and may be non-linear) model, which significantly speeds up the development, optimization and approbation processes for the HEMT with predefined device characteristics.

If the parameters of the linear model equivalent circuit components belong to the same metric space R , the proposed approach gives an opportunity to predict accurately enough—even without using compact models—the behavior of the cutoff frequencies f_T and f_{max} (and probably H_{21} and G_{max}) depending on n and d .

At transferring to a global approximation ($l \geq L$), when topological and fractal dimensions coincide $D_T = D_f$, the behavior of HES electrophysical parameters, and consequently, AlGaIn/GaN HEMT internal transistor equivalent circuit parameters, as well as their HF characteristics, have linear (classic) dependences upon n and d , which are peculiar to the spaces of integer topological dimensions D_T .

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