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Development and Implementation of LED Street Lights with Bright and Extinguishable Controls and Power Converters

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Abstract: This study developed and implemented a driving power supply for light-emitting diode (LED) array streetlamps. The power stage was a quasi-resonant (QR)-flyback converter, its input power was the alternating-current power, and the LED array streetlamp was driven by the direct-current output power. The developed QR-flyback converter was operated in discontinuous conduction mode, and the pulse-width modulation (PWM) control chip was used to switch and conduct at the resonant valley of the drain-source voltage on the metal-oxide-semiconductor field-effect transistor (MOSFET) switch to reduce the switching loss. Moreover, the PWM control chip had a disable function, which was connected with a bright and extinguishable control circuit, and the high/low voltage level signal output by the Arduino development board can be used to control the output power of the QR-flyback converter, achieving bright and extinguishable controls for the LED array streetlamp.

Keywords: light-emitting diode; quasi-resonant (QR)-flyback converter; resonant valley



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1. Introduction

Street lighting is important equipment for sidewalks and roadways, and can impact traffic safety and the quality of the human environment to serve a sense of conformability and security. Moreover, street lighting can also improve the daytime and night appearance of the road environment. To ensure a good installation, street lighting standards require several performance indexes, such as illuminance, luminance, power qualities, and electrical conversion efficiencies [1–3].

The light-emitting diode (LED) can be used in indoor and outdoor environments including roadways, sidewalks, streets, building interiors, advertisement signboards, and ambient lighting. In street lighting applications, LEDs have more lifetime (50–100 k hours) compared with that of fluorescent or gas-discharge lamps, substantially reducing maintenance and replacement costs [4].

General LED drivers are composed of a power factor correction (PFC) circuit, DC–DC converter, and current control circuit. To promote the conversion efficiency and hardware reliability of the LED driver, the single-stage AC–DC converter as an alternative to the PFC circuit and the DC–DC converter has been developed and implemented [5].

Due to the greenhouse effect and climate change influences, science and technology development is placing more and more focus to energy saving and carbon reduction. Therefore, switching-mode power supplies (SMPSs) play a critical role in power conversion. SMPSs have isolated and non-isolated topologies. Non-isolated SMPSs include buck converters, boost converters, and boost-buck converters. Isolated SMPSs include full-bridge converters, half-bridge converters, forward converters, and flyback converters.

An isolated power converter separates the input alternating-current (AC) power from the output direct-current (DC) power by electrically and physically dividing the circuit into two sections, in order to prevent the AC power from influencing the load. The isolated AC–DC converter uses a high-frequency transformer to achieve galvanic isolation between the AC inlet and DC outlet.

Several benefits of isolated AC–DC converters include:

- Providing safety to humans and sensitive instruments against the high and potentially dangerous AC input source.
- Breaking ground loops.
- Avoiding floating outputs and voltage level shifting.

Therefore, isolated AC–DC converters have been used in medical, industrial, instrumentation, smart home, commercial electronic equipment, internet of things (IoT), telecommunication, battery charger, cell phone charger, vehicle or aircraft powertrains, military, and home applications [6].

Comparing the forward converter and the flyback converter, the transformer of the flyback converter dispenses with an additional demagnetization winding, hence the design difficulty and transformer winding cost can be reduced. Moreover, a quasi-resonant (QR)-flyback converter can achieve soft-switching for the power switch using the transformer's primary inductance and the switch's and circuit board's parasitic capacitances; therefore, the conducted and radiated electromagnetic interferences can be reduced [7,8]. Otherwise, in order to improve the power conversion efficiency, retain the advantages of simple circuit configuration, and reduce the hardware cost, QR-flyback converters have become popular.

QR-flyback converters use the parasitic capacitance on the power switch and leakage inductance on the transformer to generate a resonant voltage when the power switch is turned off, and then the power switch can be turned on at the resonant voltage valley; therefore, soft-switching can be achieved to reduce switching losses, and electromagnetic interference can be effectively diminished. Moreover, the QR-flyback converter is an isolated SMPS because it possesses a transformer; furthermore, the QR-flyback converter can use a pulse-width modulation (PWM) control chip to correct the power factor of the input AC power; in summary, the QR-flyback converter is suitable as a driving power supply for LED array streetlamps [9,10]. Table 1 lists the characteristic differences between a hard-switching traditional flyback and soft-switching QR-flyback [7,8,11].

Table 1. Characteristic differences between a hard-switching traditional flyback and soft-switching QR-flyback.

	Traditional Flyback in the Discontinuous Conduction Mode Operation	QR-Flyback
Conduction losses of power switch and output rectification diode	High	High
Reversed recovery loss of output rectification diode	Low	Low
Switching loss of power switch	Low	Low
Output filter capacitance	Large	Large
Feedback and stability designs	Simple	General
Switching frequency	Constant	Adjustable
Average efficiency	Low	High

This study developed and implemented a QR-flyback converter driving an LED array streetlamp. Using a PWM control IC, the QR-flyback converter can achieve the power factor correction and drive the LED array streetlamp; moreover, the bright and extinguishable

control circuit incorporating the PWM control IC could control the LED array streetlamp's brightness and extinguishing operations.

2. Design Consideration of QR-Flyback Converter

The circuit block diagram of the QR-flyback converter is depicted in Figure 1, including the full-wave rectifier, input filter capacitor C_{in} , snubber circuit, rectification and filter circuit, PWM control chip, n -channel metal-oxide-semiconductor field-effect transistor (MOSFET) switch Q , drain-source terminal capacitor C_{ds} , bright and extinguishable control circuit, transformer T (including the magnetizing inductance L_p , primary-side winding n_{pri} , secondary-side winding n_{sec} , and auxiliary winding n_{aux}), diode D , and output filter capacitor C_{out} ; the input source is the AC voltage v_{ac} , and the output power drives the load. The QR-flyback converter specification, transformer design, MOSFET switch specification, snubber design, secondary-side rectifier diode design, and input and output filter capacitor designs are described as follows:

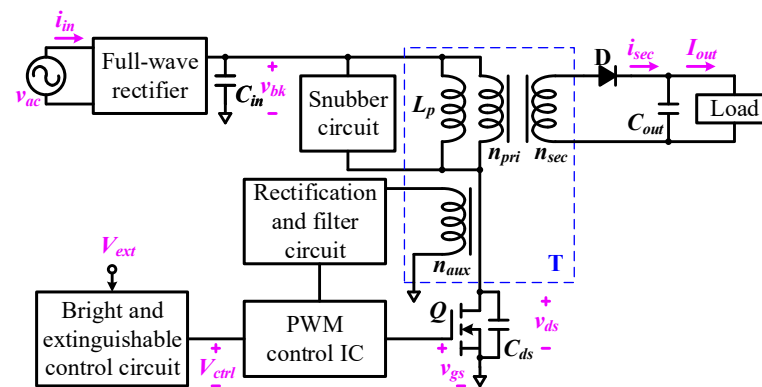


Figure 1. Circuit block diagram of QR-flyback converter.

2.1. QR-Flyback Converter Specification

The specifications of the developed QR-flyback converter are listed in Table 2.

Table 2. QR-flyback converter specification.

Description and Notation	Specification
Input AC voltage (v_{ac})	85 to 140 V _{rms}
Output voltage (V_{out})	35 V
Output current (I_{out})	1.5 A
Output power (P_{out})	52.5 W
Maximum duty cycle ratio (D_{max})	0.47
Efficiency	>80%

2.2. Transformer Design

The terminal voltage across the transformer's secondary side can be reflected to the transformer's primary side, becoming a reflected voltage V_R . The minimum peak value of the input AC voltage is $v_{ac(pk,min)}$; a variable kv can be obtained and expressed as [12]:

$$kv = v_{ac(pk,min)} / V_R, \quad (1)$$

Substitution of $V_R = 100$ V and $v_{ac(pk,min)} = 85\sqrt{2} = 120$ V into (1) can obtain $kv = 1.2$. Using the characteristic equation: $f(kv) = (0.5 + kv \times 1.4 \times 10^{-3}) / (1 + 0.82 \times kv)$ [12], $f(kv) = 0.25$ can be obtained.

N27 and EF25 are the model numbers of the magnetic core material and transformer bobbin in the study application. The effective magnetic path length $l_e = 57.5$ mm, effective magnetic cross-sectional area $A_e = 52.5$ mm², and effective volume $V_e = 3020$ mm³ [13]. To ensure that the designed transformer is not operated in saturation, it is necessary to calculate the minimum magnetizing inductance $L_{p(\min)}$ in the primary-side winding of the transformer. $L_{p(\min)}$ can be expressed as [12]:

$$L_{p(\min)} = \frac{4.3 \times 10^{-6} \times V_R}{i_{p(pk)} \times 0.93}, \quad (2)$$

where $i_{p(pk)}$ is the peak current passing the magnetizing inductance, which can be expressed as [12]:

$$i_{p(pk)} = \frac{2 \times P_{in(\max)}}{v_{ac(pk,\min)} \times f(kv)}, \quad (3)$$

where $P_{in(\max)}$ is the maximum input power. In Table 1, the output power of the QR-flyback converter is $P_{out} = 52.5$ W, and the conversion efficiency is set at 80%, hence $P_{in(\max)}$ can be calculated as 65.63 W; in this study, $P_{in(\max)} = 70$ W was used. Substitution of $P_{in(\max)} = 70$ W, $v_{ac(pk,\min)} = 120$ V, and $f(kv) = 0.25$ into (3) can yield $i_{p(pk)} = 4.7$ A. Substitution of the aforementioned parameters and $V_R = 100$ V into (2) can obtain $L_{p(\min)} = 99$ μH. According to [14], another magnetizing inductance equation can be expressed as:

$$L_p = \frac{v_{ac(pk,\min)}}{(1 + kv) \times f_{sw(\min)} \times i_{p(pk)}}, \quad (4)$$

where $f_{sw(\min)}$ is the minimum operating frequency of the MOSFET switch, and its value is $f_{sw(\min)} = 80$ kHz. Substitution of $v_{ac(pk,\min)} = 120$ V, $kv = 1.2$, $f_{sw(\min)} = 80$ kHz, and $i_{p(pk)} = 4.7$ A into (4) can yield $L_p = 145.07$ μH, which is greater than $L_{p(\min)} = 99$ μH.

The calculating expression of n_{pri} can be expressed as [12,14]:

$$n_{pri} = \frac{L_p \times i_{p(pk)} \times 10^3}{B_{max} \times A_e}, \quad (5)$$

Substitution of $L_p = 145.07$ μH, $i_{p(pk)} = 4.7$ A, $B_{max} = 0.3$ mT, and $A_e = 52.5$ mm² into (5) can obtain $n_{pri} = 43.29 \cong 44$, because the winding turns in practical applications are the positive integer.

The turn ratio n of transformer can be expressed as [12]:

$$n = \frac{V_R}{(V_{out} + V_f)} = \frac{n_{pri}}{n_{sec}}, \quad (6)$$

where V_f is the forward bias voltage of D. Substitution of $V_f = 0.8$ V, $V_R = 100$ V, and $V_{out} = 35$ V into (6) can obtain $n_{sec} = 15.75 \cong 16$. Using both $n_{pri} = 44$ and $n_{sec} = 16$, $n = 2.75$ can be obtained.

The calculating expression of n_{aux} can be expressed as [12]:

$$n_{aux} = \frac{V_{aux} \times n_{sec}}{V_{out}}, \quad (7)$$

where V_{aux} is a voltage across the auxiliary winding; in this study, the operating power of the PWM control chip was set to 15 V. Substitution of $V_{aux} = 15$ V, $V_{out} = 35$ V, and $n_{sec} = 16$ into (7) can obtain $n_{aux} = 6.86 \cong 7$. Aforementioned parameters include $n = 2.75$, $n_{pri} = 44$, $n_{sec} = 16$, $n_{aux} = 7$, and $L_p = 145.07$ μH.

2.3. Metal-Oxide-Semiconductor Field-Effect Transistor (MOSFET) Specification

The withstand voltage and current are the important specifications for MOSFET switch selection. When the MOSFET switch is turned off, the leakage inductance on the transformer and paratactic capacitance on the MOSFET switch cause the resonant voltage spike v_{spike} , hence the withstand voltage of the MOSFET switch must be greater than v_{spike} , which can be expressed as [15]:

$$v_{spike} = i_{p(pk)} \sqrt{\frac{L_{leak}}{C_{ds}}}, \quad (8)$$

where L_{leak} is the leakage inductance on the primary side of the transformer. It is usually 1% to 3% of L_p , hence $L_{leak} = L_p \times 1\% = 145.07 \mu\text{H} \times 1\% = 1.45 \mu\text{H}$ in this study. Moreover, C_{ds} is the drain-source terminal capacitance on the MOSFET switch, and the $C_{ds} = 470 \text{ pF}$ was used in this study. Substitution of $i_{p(pk)} = 4.7 \text{ A}$, $L_{leak} = 1.45 \mu\text{H}$, and $C_{ds} = 470 \text{ pF}$ into (8) can obtain $v_{spike} = 261.06 \text{ V}$. The withstand voltage of the MOSFET switch can be expressed as [12]:

$$V_{break} = v_{ac(pk,max)} + V_R + v_{spike}. \quad (9)$$

Substitution of $v_{ac(pk,max)} = 198 \text{ V}$, $V_R = 100 \text{ V}$, and $v_{spike} = 261.06 \text{ V}$ into (9) can obtain $V_{break} = 559.06 \text{ V}$, hence the withstand voltage of the MOSFET switch must be greater than 559.06 V . Moreover, the withstand current of the MOSFET switch must be greater than $i_{p(pk)} = 4.7 \text{ A}$. Furthermore, the small C_{ds} and gate terminal charge Q_g can be selected for switching loss reduction. The model number STF10N80K5 of the MOSFET switch [16] was used in this study, its specifications listed in Table 3.

Table 3. MOSFET switch (STF10N80K5) specification [16].

Description	Specification
Gate terminal charge	22 nC
Gate-drain terminal charge	5.5 nC
Gate-source terminal charge	13.2 nC
Input paratactic capacitance	74 pF
Output paratactic capacitance	20 pF
Withstand voltage	800 V
Withstand current	9 A
Turn-on resistance	0.6 Ω

2.4. Snubber Circuit

The resonant voltage spike generated by the QR-flyback converter exceeds the withstand voltage of the MOSFET switch, resulting in device damage. The voltage spike can be reduced by the snubber circuit. The snubber circuit elements include a resistor R_{snub} , C_{snub} , and D_{snub} , as shown in Figure 2. R_{snub} and C_{snub} can be expressed as [17]:

$$C_{snub} = \frac{L_{leak} \times i_{p(pk)}^2}{v_{spike} \times (v_{spike} + 2V_R)}, \quad (10)$$

$$R_{snub} \geq \frac{1}{f_{sw(min)} \times C_{snub} \times \ln\left(1 + \frac{v_{spike}}{V_R}\right)}, \quad (11)$$

Substitution of $L_{leak} = 1.45 \mu\text{H}$, $i_{p(pk)} = 4.7 \text{ A}$, $v_{spike} = 261.06 \text{ V}$, and $V_R = 100 \text{ V}$ into (10) can obtain $C_{snub} = 266.11 \text{ pF}$. Substitution of the aforementioned parameters and

$f_{sw(\min)} = 80$ kHz into (11) can yield $R_{snub} = 36.59$ k Ω . The diode D_{snu} of the snubber circuit can use a fast recovery diode, whose recovery time can reduce the switching loss of D_{snu} .

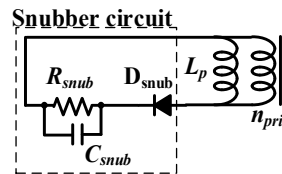


Figure 2. Snubber circuit.

2.5. Rectification Diode

The withstand voltage calculation of the rectification diode D can be expressed as [12]:

$$V_d = V_{out} + v_{ac(pk,\max)} \times \frac{n_{sec}}{n_{pri}}. \quad (12)$$

Substitution of $V_{out} = 35$ V, $v_{ac(pk,\max)} = 198$ V, $n_{pri} = 44$, and $n_{sec} = 16$ into (12) can obtain $V_d = 107$ V.

According to the transformer reflection law, the peak current calculation of the D on the secondary-side of the transformer can be expressed as:

$$i_{sec(pk)} = i_{p(pk)} \times n. \quad (13)$$

Substitution of $n = 2.75$ and $i_{p(pk)} = 4.7$ A into (13) can obtain $i_{sec(pk)} = 12.93$ A $\cong 13$ A. Therefore, the withstand voltage and current of the D must be selected that are greater than 107 V and 13 A, respectively.

2.6. Output Filter Capacitor

The filter capacitor can be used to stabilize the output voltage of the SMPS. When the load was changed, the current ripple magnitude was related to the equivalent series resistor (ESR) of the filter capacitor [18]; the low ESR can reduce the current ripple when the load changes. The output filter capacitor calculation can be expressed as [19]:

$$C_{out} \geq \frac{I_{out} \times ncp}{f_{sw(\min)} \times \Delta V_{out}}, \quad (14)$$

where ΔV_{out} is the peak-to-peak value of the output voltage, the and ncp is the number of the internal clock cycle for the PWM control chip needed by the control loop to reduce the duty cycle from maximum to minimum value. The ncp can be set at 10 to 20 [19].

Substitution of $\Delta V_{out} = V_{out} \times 1\% = 35 \times 1\% = 0.35$ V, $I_{out} = 1.5$ A, $f_{sw(\min)} = 80$ kHz, and $ncp = 20$ into (14) can obtain $C_{out} = 1071.43$ μ F.

Because the ESR of the single electrolytic capacitor was of high value, this study used the two electrolytic capacitors of 680 μ F and the ceramic capacitor of 220 pF in parallel connection to reduce the ESR of C_{out} . Due to the fact that the capacitance value (680 μ F + 680 μ F + 220 pF) was higher than the calculating result (1071.43 μ F), the influence of the capacitance value error in the practical application can also be eliminated.

2.7. Input Filter Capacitor

When the QR-flyback converter is used as a DC–DC converter, the input AC power can be filtered by a capacitor after passing through a full-wave rectifier to obtain an input DC voltage. However, input DC voltage has a voltage ripple $v_{bk(ripple)}$, as shown in Figure 3. In Figure 3, the cycle ratio $D_{bulk} = t_1/t_2$ during the input filter capacitor charging is set to 0.2 [20].

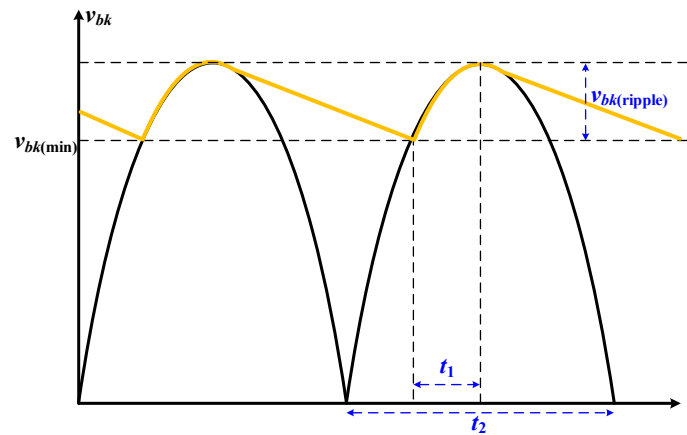


Figure 3. Input voltage ripple on C_{in} .

Moreover, the voltage ripple on the input filter capacitor was set to the maximum input AC voltage of 10% ($v_{ac(max)} = 140\sqrt{2} \times 10\% \cong 20$ V); therefore, the minimum voltage across the filter capacitor $v_{bk(min)} = v_{ac(pk,min)} - 20$ V = 120 V – 20 V = 100 V. Furthermore, according to [21], the minimum voltage of the input filter capacitor can be expressed as:

$$v_{bk(min)} = \sqrt{2 \times v_{ac(pk,min)}^2 - \frac{P_{in} \times (1 - D_{bulk})}{C_{in} \times f_{line}}}, \quad (15)$$

which can be written as:

$$C_{in} = \frac{P_{in(max)} \times (1 - D_{bulk})}{(2 \times v_{ac(pk,min)}^2 - v_{bk(min)}^2) \times f_{line}}, \quad (16)$$

where f_{line} is the frequency of the input AC power source. Substitution of $v_{bk(min)} = 100$ V, $D_{bulk} = 0.2$, $P_{in(max)} = 70$ W, $v_{ac(pk,min)} = 120$ V, and $f_{line} = 60$ Hz into (16) can obtain $C_{in} = 49.65$ μ F. However, the QR-flyback converter in this study was used as an AC/DC converter, and C_{in} was used as a high-frequency filter; therefore, C_{in} can choose a capacitance value 200 times smaller than 49.65 μ F [16]. In this study, the $C_{in} = 220$ nF was chosen with the withstand voltage of 630 V (this withstand voltage was greater than $v_{ac(pk,max)} = 198$ V) in the practical application.

3. Experimental Results

In this study, the experimental voltage and current measurements are shown in Figure 1, including input AC voltage v_{ac} , input AC current i_{in} , full-wave rectification voltage v_{bk} , transformer secondary-side current i_{sec} , output voltage V_{out} , output current I_{out} , MOSFET drain-source voltage v_{ds} , MOSFET gate source-voltage v_{gs} , and control voltage V_{ctrl} .

To confirm that the QR-flyback converter can output the rated voltage and current under the input AC condition, the peak value of v_{bk} was 155 V (110 V_{rms}), $V_{out} = 35$ V, and $I_{out} = 1.5$ A, as shown in Figure 4.

Under the full-load operation and $v_{ac} = 110$ V_{rms}, v_{ac} and i_{in} measurement waveforms are shown in Figure 5. Waveforms of v_{ac} and i_{in} were in-phase, which verified that the QR-flyback converter designed in this study achieved the power factor correction.

The power analyzer (PW3390, HIOKI E.E. Corp., Nagano, Japan) was used to measure the harmonic distortion rate. Under the full-load operation and $v_{ac} = 110$ V_{rms}, the fifth-order harmonic histogram and harmonic record table are shown in Figure 6. In Figure 6a, the voltage, current, and power generated the maximum harmonic in the first (1st)-order; the current produced odd harmonics above the third (3rd)-order, whose values were low. Figure 6b recorded that the THD percentage was 0.06%, which addressed the IEC 61000-3-2 Class-C standard [22].

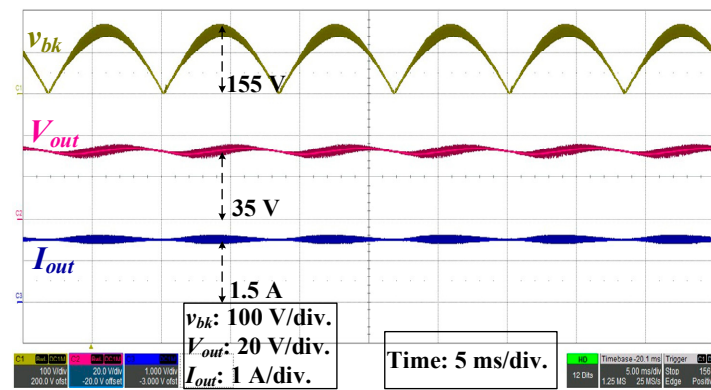


Figure 4. Waveforms of full-wave rectification, output voltage and current.

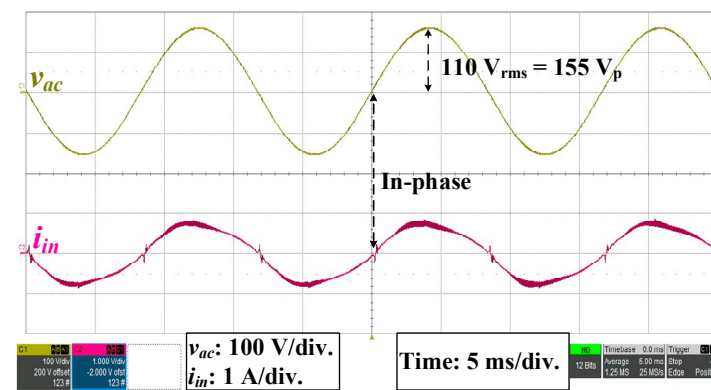


Figure 5. v_{ac} and i_{in} were in-phase.

Under the full load operation and $v_{ac} = 85 \text{ V}_{\text{rms}}$, the experiment and simulation waveforms of v_{gs} , v_{ds} and i_{sec} are shown in Figure 7. The operating frequencies of v_{gs} were 76.92 kHz (experiment) and 78.13 kHz (simulation), and the highest voltages of v_{ds} were 300 V (experiment) and 300 V (simulation), respectively. v_{gs} was changed at the resonant valley of v_{ds} , and the MOSFET switch was turned on. The PSIM software was used for the simulation. Moreover, the peak currents of i_{sec} were 18 A (experiment) and 17 A (simulation), respectively; the result of i_{sec} based on (13) was 13 A.

Under the full load operation and $v_{ac} = 140 \text{ V}_{\text{rms}}$, the experiment and simulation waveforms of v_{gs} , v_{ds} and i_{sec} are shown in Figure 8. The operating frequencies of v_{gs} were 97.1 kHz (experiment) and 97 kHz (simulation), respectively; the highest voltages of v_{ds} were 390 V (experiment) and 380 V (simulation), respectively. v_{gs} was changed at the resonant valley of v_{ds} , and the MOSFET switch was turned on. Moreover, the peak currents of i_{sec} were 16 A (experiment) and 17 A (simulation), respectively.

The external control signal V_{ext} (Figure 3) was used to control the V_{ctrl} (Figure 3) voltage level of the PWM control chip, and then V_{out} of the QR-flyback converter can be controlled, as shown in Figure 9. In Figure 9a, when $V_{ext} = 0$, $V_{ctrl} = 2 \text{ V}$, and $V_{out} = 35 \text{ V}$, this experiment represented the fact that the bright and extinguishable control circuit enabled the QR-flyback converter to drive the LED array streetlamp at $V_{out} = 35 \text{ V}$; therefore, the LED array streetlamp could be lighted. In Figure 9b, when $V_{ext} = 5 \text{ V}$, $V_{ctrl} = 0$, and $V_{out} = 0$, this experiment represented the fact that the bright and extinguishable control circuit disabled the output voltage of the QR-flyback converter; therefore, the LED array streetlamp was dimmed.

At the different v_{ac} (85 to 140 V_{rms}), I_{out} was changed from 0.1 to 1.5 A, the efficiency measurements were recorded in Figure 10. The minimum efficiency was about 32% under the $v_{ac} = 140 \text{ V}_{\text{rms}}$ and $I_{out} = 0.1 \text{ A}$; the maximum efficiency was about 85% under the $v_{ac} = 140 \text{ V}_{\text{rms}}$ and $I_{out} = 1.5 \text{ A}$.

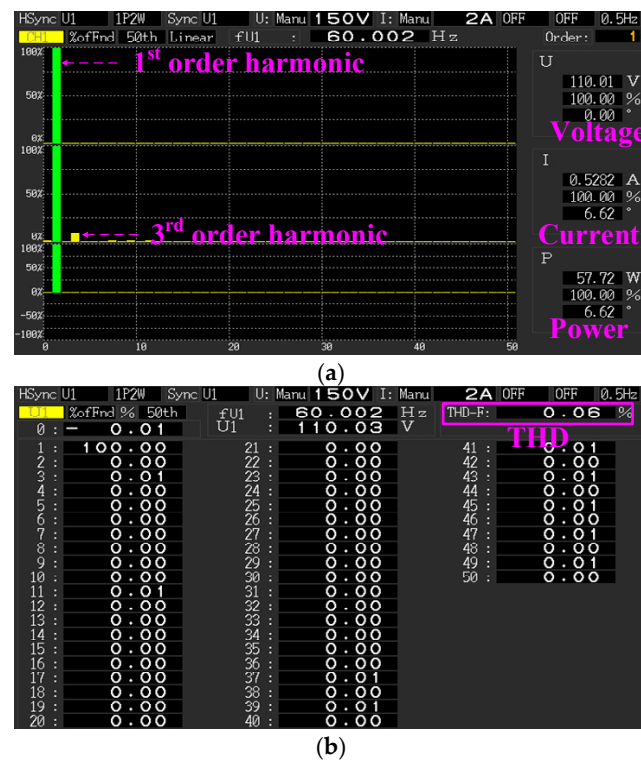


Figure 6. Power analyzer measured harmonic distortion: (a) Harmonic histogram; (b) Harmonic record table.

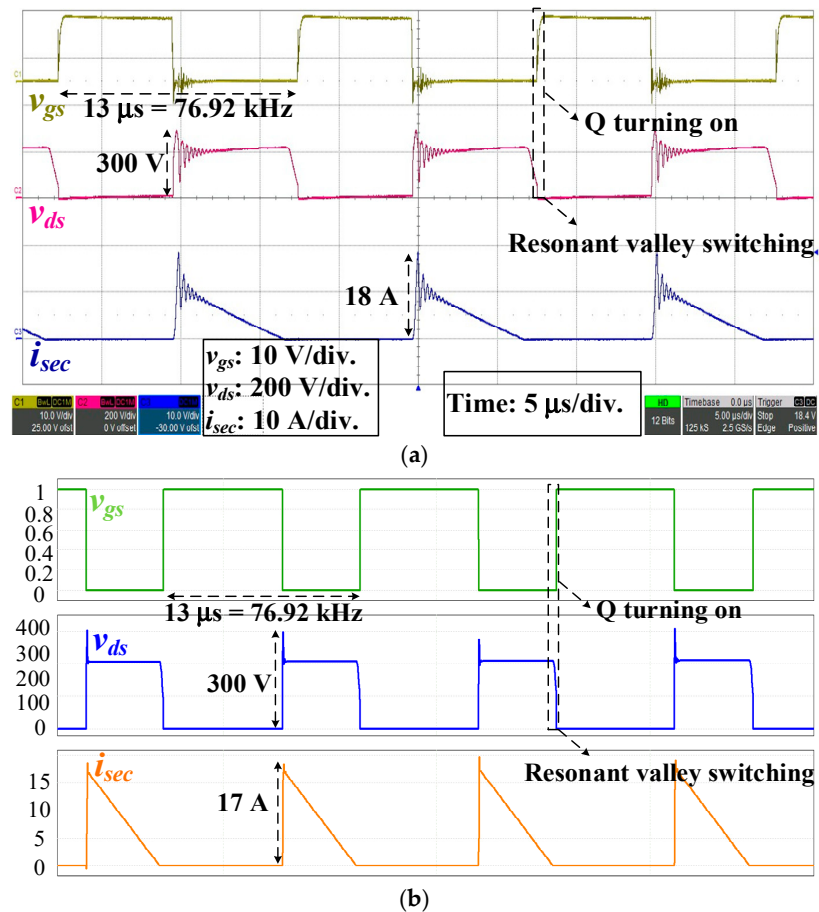


Figure 7. v_{gs} , v_{ds} , and i_{sec} at the AC input of 85 V_{rms}: (a) Experiment; (b) Simulation waveforms.

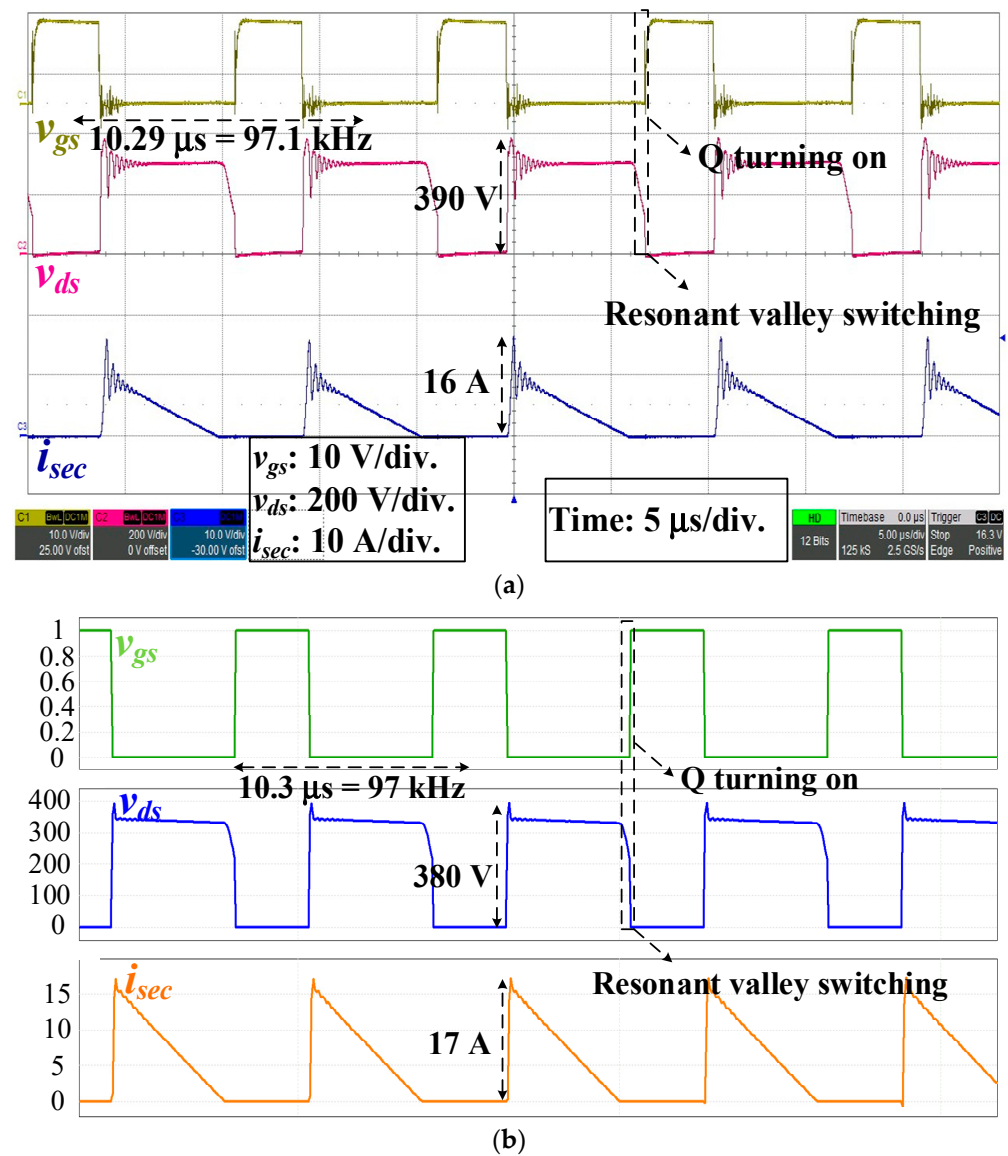


Figure 8. v_{gs} , v_{ds} , and i_{sec} at the AC input of 140 V_{rms}: (a) Experiment; (b) Simulation waveforms.

V_{ext} (Figures 1 and 9) was generated by the Arduino development board and combined with the QR-flyback converter to drive the LED array streetlamp system, as shown in Figure 11. In Figure 11, the three LED array streetlamps were controlled achieving bright and extinguishable operations at different times, when the model car moved to different positions.

The implement block diagram of the LED array streetlamp is depicted in Figure 12, its operation described as follows:

1. The QR-flyback converter was started up.
2. The external signal V_{ext} was detected to control the bright and extinguishable control circuit.
3. When V_{ext} was a low voltage level, the LED array streetlamp employed the bright operation; when V_{ext} was a high voltage level, the LED array streetlamp employed the extinguishable operation.

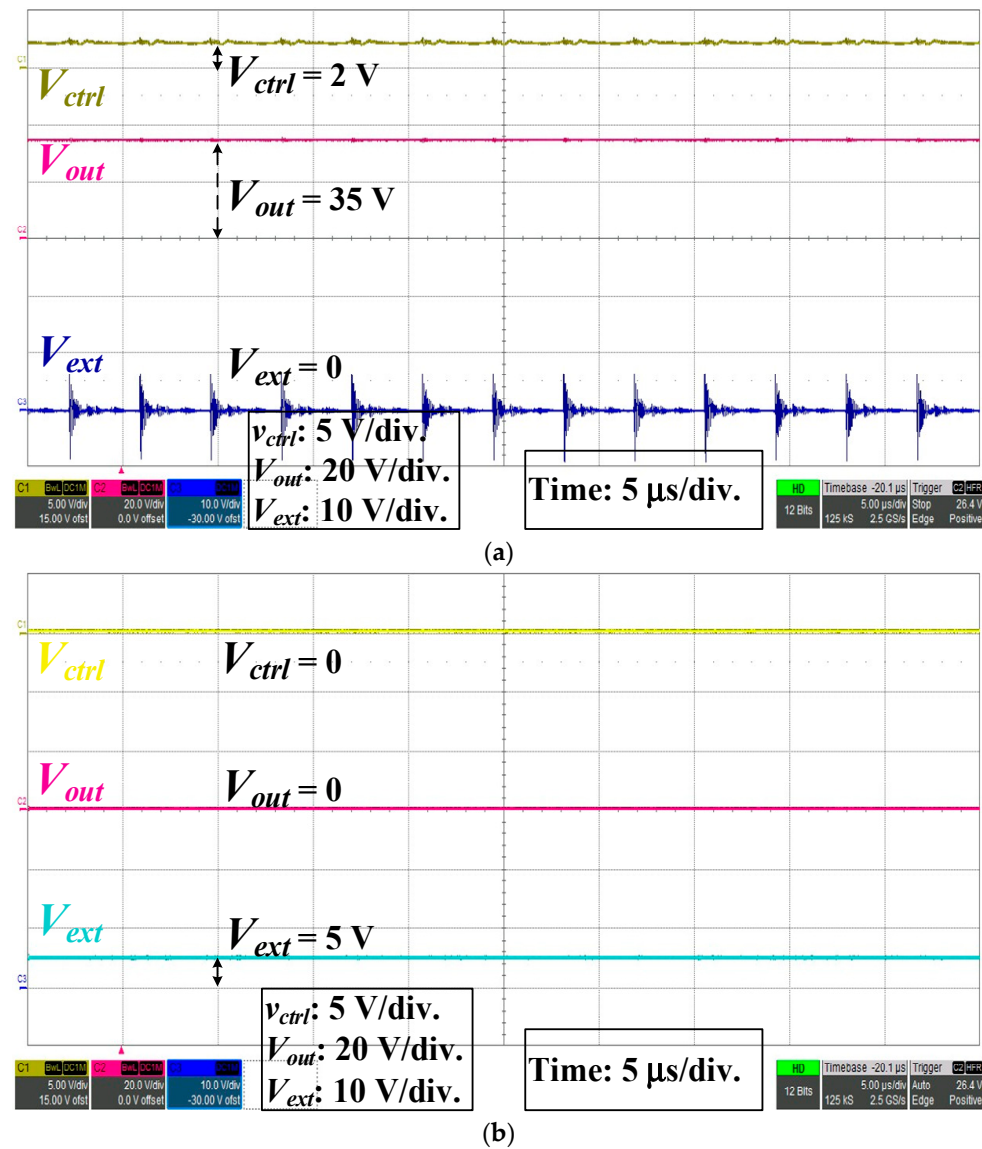


Figure 9. V_{ext} , V_{ctrl} , and V_{out} measurements of bright and extinguishable control circuit: (a) Light bright; (b) Light extinguish.

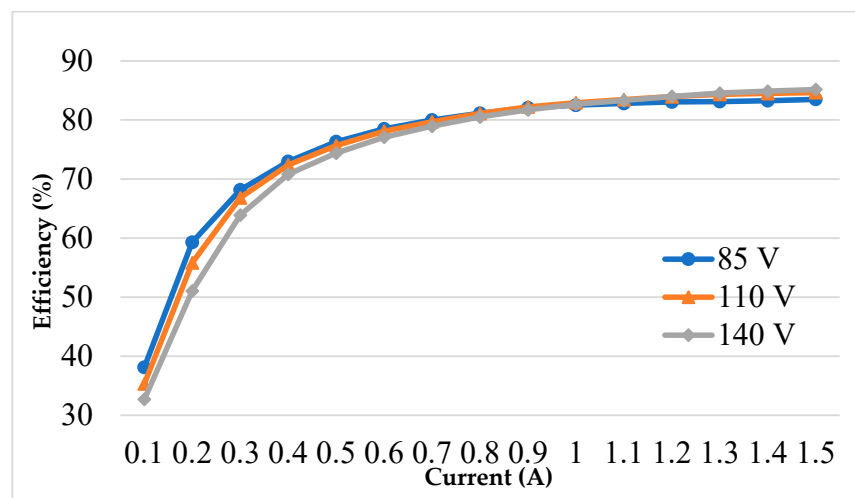


Figure 10. Efficiency measurement.



Figure 11. Arduino development board combined with QR-flyback converter to drive LED array streetlamp.

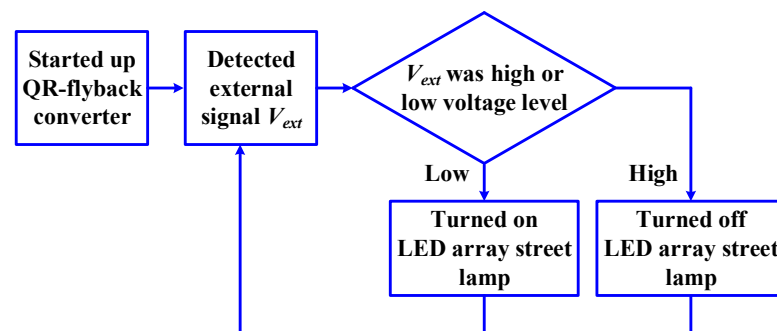


Figure 12. Implement block diagram of the LED array streetlamp.

4. Conclusions

This study developed a driving power supply for LED array streetlamps. The driving power supply was a QR-flyback converter, which combined with a PWM control chip to achieve the power factor correction of the input AC power, and output to drive the LED array streetlamps. This study provided detailed design considerations in the parameter calculations of the power stage devices and verified their correctness with experimental results. The Arduino development board was combined with the QR-flyback converter to drive the LED array streetlamp system.

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