

Supplementary Materials: Single-cell Electroporation with Real-time Impedance Assessment USING a Constriction Microchannel

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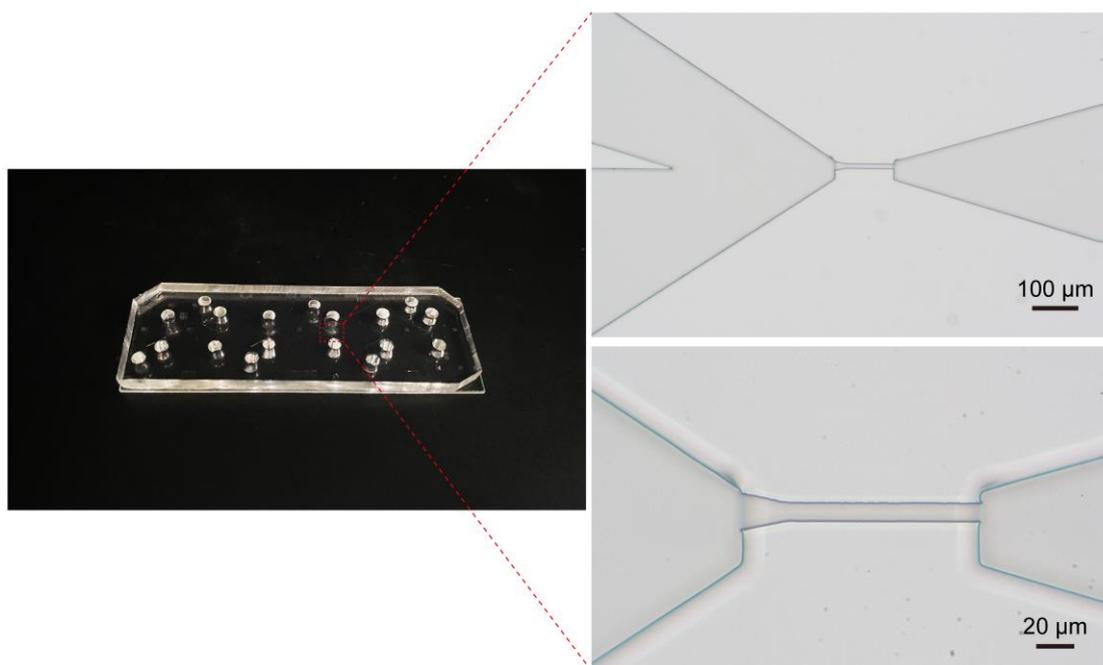


Figure S1. Photograph of the microfluidic chip and the micrographs of the loading channel, bypass channel, constriction channel and release channel.

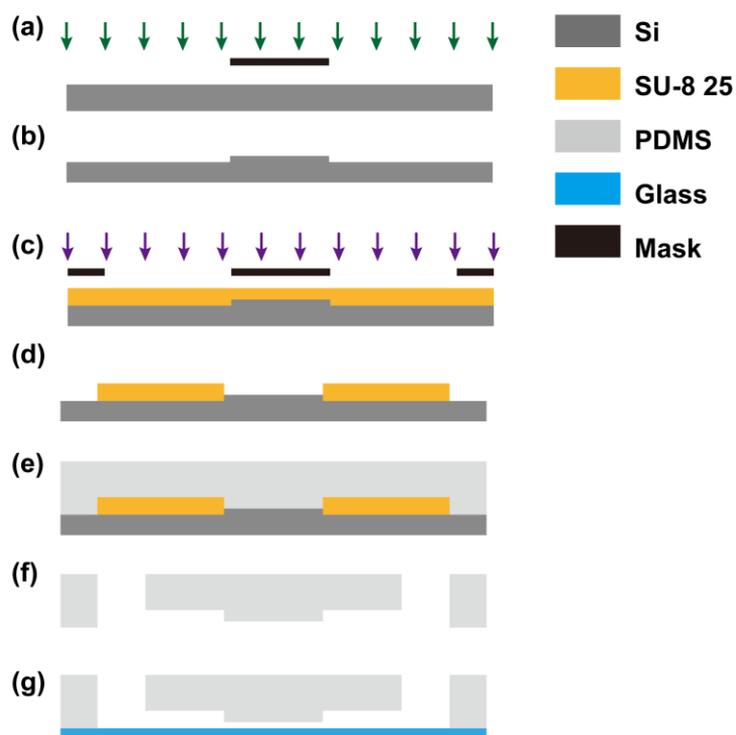


Figure S2. The microfluidic device was fabricated based on the key steps of (a,b) deep etch of Si, (c) SU-8 25 spin coating, exposure with alignment, (d) development, (e) PDMS molding and (f) peeled PDMS with holes punched. After plasma treatment, the PDMS layer and the glass substrate were bonded together (g).