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Optimized Fault-Tolerant Control of Dual Three-Phase PMSM Under Open-Switch Faults

Lei Chen, Min Chen *, Bodong Li D, Xinnan Sun and Feng Jiang

Department of Applied Electronics, Zhejiang University, Hangzhou 310027, China; leichan@zju.edu.cn (L.C.); bodong_li@zju.edu.cn (B.L.); sxnan@zju.edu.cn (X.S.); jiangfeng@zju.edu.cn (F.J.)

* Correspondence: calim@zju.edu.cn

Abstract: In this article, an optimized fault-tolerant control (FTC) method without current judgement is proposed for open-switch faults (OSFs) in dual three-phase permanent magnet synchronous motor (DTPMSM) drives. The reason for the torque ripple under OSFs has been investigated. The theoretical analysis reveals a significant increase in torque ripple under OSFs. Then, an optimized FTC method is proposed for a DTPMSM with two isolated neutral points. The proposed method maintains the original control scheme, enabling the smooth transitions of current and torque between faulty operation and FTC without introducing noticeable torque ripples. In addition, the universality and robustness are enhanced by eliminating the need for current judgement, thereby avoiding misjudgments due to sinusoidal current zero crossings, sudden load, or speed changes. The experimental results are presented to validate the effectiveness of the proposed FTC strategy under OSFs on a laboratory DTPMSM.

Keywords: dual three-phase PMSM; fault-tolerant control; open-phase fault; open-switch fault

1. Introduction

Compared with the conventional three-phase permanent magnet synchronous motors (PMSMs), the multiphase PMSMs have advantages such as high efficiency, increased power density, and improved fault tolerance capabilities [1–4]. As the number of phases increases, reliability can be enhanced by adjusting the remaining phases through FTC. Therefore, multiphase PMSMs are considered promising candidates for high-power applications requiring high reliability, such as more electric aircraft, wind power generation, electric vehicles, ship propulsion systems, and railways [5,6].

The DTPMSMs have been extensively studied based on the well-established principles of three-phase technology [7]. By employing two sets of three-phase stator windings spatially shifted by 30 electrical degrees, DTPMSMs eliminate the sixth harmonic torque ripple by mitigating the fifth and seventh harmonic magnetomotive forces (MMFs) [8]. Moreover, DTPMSMs can feature either isolated or connected neutral points, with configurations including single or dual neutral points. Typically, isolation with two neutral points is preferred for effective zero-sequence current suppression and reduced complexity in current control [9–11]. Consequently, significant research has been conducted on DTPMSMs with two isolated neutral points.

In DTPMSM electrical drive systems, FTC methods are a primary area of research interest. Before reviewing existing FTC strategies, it is essential to understand common types of faults in electric drives, encompassing inverter and machine faults leading to open-circuit (inverter switch [12], phase [13], or line [14]) and short-circuit (phase [15], inverter switch [16], interturn [17]) faults. By isolating hardware circuits electrically, these faults can be transformed into open-phase faults (OPFs) [18,19]. Therefore, FTC strategies of multiphase motors are mainly focused on OPFs. However, OSFs are also a common fault of power devices, and fewer studies are conducted on OSFs compared with OPFs. OSFs



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involve the failure of a single controllable power semiconductor device in a converter leg, with its anti-parallel diode still functional. The simplest strategy for OSFs involves isolating the faulty bridge arm to operate as a single OPF. In this way, the FTC strategy of a single OPF can be used for OSFs. However, this method underutilizes healthy switches, limiting system performance. Alternatively, adding redundant bridge arms or switches enhances fault tolerance but increases hardware costs. Therefore, using existing healthy switches for FTC without additional hardware devices is crucial. For instance, the combination of threephase six-switch (TPSS) and three-phase four-switch (TPFS) topologies is employed for FTC under insulated gate bipolar transistor (IGBT) OSFs in voltage-source inverters [20,21]. Additionally, a new sensorless field-oriented control (FOC) method, utilizing freewheeling currents to manage the IGBT and position sensor failures, further enhances fault tolerance capabilities [22]. However, the likelihood of the simultaneous occurrence of two faults is minimal. In [23], DC currents matched in amplitude with the corresponding fundamental phase currents are injected into open-switch phases to mitigate nonlinearities caused by faults. Nevertheless, large harmonics in experimental current waveforms and their effect on torque ripple reduction are not demonstrated. Postfault operation of a five-phase drive is analyzed in [12], focusing on scenarios where freewheeling diodes in faulty phases remain conducting. However, specific FTC strategies are not explored. Recently, adaptive FTC schemes have been reported in [24–26]. The main concept is to avoid the conflict goal between the torque subspace and the harmonic subspace controllers. However, the performance under multiple faults will be reduced to some extent. In [27,28], an FTC strategy based on virtual current is proposed for OSFs in DTPMSM drives. The current judgement based on virtual current is easy to implement. However, the virtual current is calculated ideally, and there may be amplitude or phase errors between the actual current and the calculated virtual current. Although it is possible to achieve 100% fault tolerance, it cannot be guaranteed to enter FTC at an accurate time. If the actual current is adopted for FTC, the postfault performance will be affected due to the threshold selection of the current judgement. Hence, the FTC strategy based on actual current without current judgement is more reasonable.

To address the issues mentioned above, an optimized FTC strategy for OSFs in DTPMSMs is proposed. The main contributions of this paper are as follows.

- (1) The proposed method is based on actual current by using the Fourier series without relying on threshold-based current judgement. Therefore, this approach can enhance fault tolerance and improve the universality and robustness.
- (2) The unified control scheme is based on a full-dimensional model and remains consistent across normal, faulty, and fault-tolerant operations. This eliminates the need for redundant hardware reconfiguration, simplifying implementation, while ensuring smooth transitions in both current and torque between faulty and FTC operations.
- (3) The proportional-integral quasi-resonant controllers with time delay compensation and phase correction are proposed to effectively eliminate harmonics and significantly improve system stability.

The remainder of this article is organized as follows. Section 2 briefly introduces the mathematical vector space decomposition (VSD) model under normal operation. Section 3 analyzes the faulty operation and proposes the optimized FTC strategies of DTPMSMs under OSFs comprehensively. Section 4 presents the experimental platform, steady-state, and dynamic experimental results. Finally, Section 5 concludes the article.

2. Mathematical Model of DTPMSM Under Normal Operation

The schematic of a typical DTPMSM driver is illustrated in Figure 1. The DTPMSM consists of two sets of three-phase windings, denoted as phase ABC and phase DEF, respectively. The neutral points of the two sets of windings are isolated to eliminate the zero-sequence currents, and there is a phase shift of $\pi/6$ between the phase ABC and phase DEF windings. The inverter comprises two sets of three-phase two-level inverters in parallel, corresponding to the two sets of three-phase windings.

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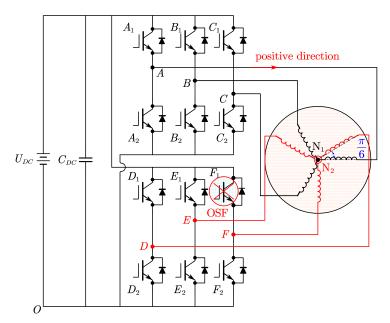


Figure 1. Schematic of a typical DTPMSM drive.

DTPMSM is a high-order, nonlinear, and strongly coupled multivariable system. According to the vector space decoupling theory, the VSD model maps the variables of DTPMSM to three mutually orthogonal subspaces, namely, $\alpha\beta$ subspace, xy subspace, and o_1o_2 subspace [8]. The transformation matrix $T_{\alpha\beta}$ is as follows.

$$[f_a \quad f_b \quad f_x \quad f_y \quad f_{o_1} \quad f_{o_2}] = T_{\alpha\beta}[f_A \quad f_B \quad f_C \quad f_D \quad f_E \quad f_F]$$
 (1)

$$T_{\alpha\beta} = \frac{1}{3} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} & 0\\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} & \frac{1}{2} & \frac{1}{2} & -1\\ 1 & -\frac{1}{2} & -\frac{1}{2} & -\frac{\sqrt{3}}{2} & \frac{\sqrt{3}}{2} & 0\\ 0 & -\frac{\sqrt{3}}{2} & \frac{\sqrt{3}}{2} & \frac{1}{2} & \frac{1}{2} & -1\\ 1 & 1 & 1 & 0 & 0 & 0\\ 0 & 0 & 0 & 1 & 1 & 1 \end{bmatrix}$$
 (2)

where f denotes the motor variables, including voltage, current, or flux. The fundamental component and $(12k \pm 1)$ th (k = 1, 2, 3, ...) harmonic components are mapped to $\alpha\beta$ subspace. Only the components in $\alpha\beta$ subspace participate in the electromechanical energy conversion. The $(6k \pm 1)$ th (k = 1, 3, 5, ...) harmonic components are mapped to xy subspace. The $(6k \pm 3)$ th (k = 1, 3, 5, ...) harmonic components are mapped to the o_1o_2 subspace and negligible with the configuration of isolated neutral points [29].

By applying transformation (3), the variables in the $\alpha\beta$ subspace are converted to the synchronous rotating dq frame. By applying transformation (4), the variables in the xy subspace are converted to a new frame, designated as dxqy frame. Then, the fifth and seventh current harmonic components can be transformed into the sixth harmonic components for elimination.

$$T_{dq} = \begin{bmatrix} \cos \theta_e & \sin \theta_e \\ -\sin \theta_e & \cos \theta_e \end{bmatrix}$$
 (3)

$$T_{dxqy} = \begin{bmatrix} -\cos\theta_e & \sin\theta_e \\ \sin\theta_e & \cos\theta_e \end{bmatrix} \tag{4}$$

where θ_e is the electrical angle.

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According to (1)–(4), the voltage and torque equations can be obtained as

$$\begin{cases}
\begin{bmatrix} u_d \\ u_q \end{bmatrix} = \begin{bmatrix} R_s & 0 \\ 0 & R_s \end{bmatrix} \begin{bmatrix} i_d \\ i_q \end{bmatrix} + \begin{bmatrix} L_d & 0 \\ 0 & L_q \end{bmatrix} \cdot \frac{d}{dt} \begin{bmatrix} i_d \\ i_q \end{bmatrix} + \begin{bmatrix} -\omega_e L_q i_q \\ \omega_e L_d i_d + \omega_e \psi_f \end{bmatrix} \\
\begin{bmatrix} u_{dx} \\ u_{qy} \end{bmatrix} = \begin{bmatrix} R_s & 0 \\ 0 & R_s \end{bmatrix} \begin{bmatrix} i_{dx} \\ i_{qy} \end{bmatrix} + \begin{bmatrix} L_{ls} & 0 \\ 0 & L_{ls} \end{bmatrix} \cdot \frac{d}{dt} \begin{bmatrix} i_{dx} \\ i_{qy} \end{bmatrix}
\end{cases}$$
(5)

$$T_e = 3n_p \left[\psi_f i_q + (L_d - L_q) i_d i_q \right] \tag{6}$$

where R_s is the stator resistance. L_d , L_q , and L_{ls} are the d-axis inductance, q-axis inductance, and stator leakage inductance, respectively. ω_e is the electrical angular velocity. Ψ_f is the rotor flux linkage. n_p is the number of pole pairs. u_d , u_q , u_{dx} , and u_{qy} are the voltages of different subspaces.

3. Operation Analysis and Optimized FTC Strategy for Single OSF

3.1. Single OSF

A single OSF is a common fault in power devices. In this case, the switch is open while the freewheeling continues to operate normally. To clarify the impacts of OSFs on the inverter operation, the open-switch fault in the upper leg switch of phase F is taken as an example. In the VSD model, the normal operation of the remaining five phases will be affected by the single OSF. The schematic diagram is shown in Figure 1 when OSFs occur. Figure 2 illustrates two types of current paths when the polarity of the phase current changes under OSFs. It is evident that the current paths remain the same as in normal operation, flowing through the upper freewheeling diode and the lower IGBT when $i_F < 0$. When $i_F > 0$, there is only one path for the phase current, flowing through the lower freewheeling diode. Figure 3 depicts the phase current waveforms when OSFs occur during a positive current period. The phase current area that is approximately zero and negative is evenly divided. When the phase current is approximately equal to zero, OSFs are equivalent to OPFs, thereby affecting the normal SVPWM strategy of the DEF three-phase winding. Therefore, OSFs can be regarded as a combination of OPFs and normal operation.

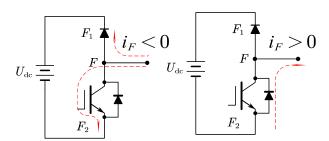


Figure 2. Paths of phase current waveforms under OSFs.

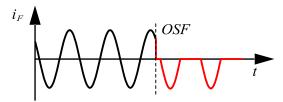


Figure 3. The results of phase F current waveform when OSF of the upper switch occurs at $i_F > 0$.

According to the Fourier series [30], the currents under OSFs can be represented as the superposition of multiple sinusoidal quantities. Since the higher-order harmonic

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components are relatively small, only the first four harmonic components are considered. Then, the $\alpha\beta$ -axis currents and xy-axis currents can be expressed as

$$i_{mf} \approx I_{m0} + \sum_{n=1}^{4} I_{mn} \cos(n\theta_e + \varphi_{mn})$$
 (7)

where m denotes the α , β , x, and y. i_{mf} is the fault currents of the m-axis. I_{m0} is the DC component of the m-axis current. φ_{mn} is the initial angle of the nth harmonic component of the m-axis current.

Substituting (7) into (3), the currents in the dq frame under OSFs can be expressed as

$$\begin{cases} i_{df} \approx I_{d0} + \sum_{n=1}^{3} I_{dn} \cos(n\theta_e + \varphi_{dn}) \\ i_{qf} \approx I_{q0} + \sum_{n=1}^{3} I_{qn} \cos(n\theta_e + \varphi_{qn}) \end{cases}$$
(8)

where i_{df} and i_{qf} are the fault currents of the dq-axis. I_{d0} and I_{q0} are the DC components of the dq-axis current. φ_{dn} and φ_{qn} are the initial angles of the nth harmonic component of the dq-axis current.

The torque ripple at the quadruple frequency is small; hence, the fourth harmonic torque ripple can be ignored. Therefore, the torque under a single OSF can be simplified as a combination of DC, the second and third harmonic torque ripples, which can be calculated as

$$T_{e0} \approx 3n_{p}\psi_{f}I_{q0} + 3n_{p}(L_{d} - L_{q})I_{d0}I_{q0}$$

$$T_{e1} \approx 3n_{p}I_{q1}\cos(\theta_{e} + \varphi_{q1}) + 3n_{p}(L_{d} - L_{q})\left[I_{q0}I_{d1}\cos(\theta_{e} + \varphi_{d1}) + I_{d0}I_{q1}\cos(\theta_{e} + \varphi_{q1})\right]$$

$$T_{e2} \approx 3n_{p}I_{q2}\cos(2\theta_{e} + \varphi_{q2}) + 3n_{p}(L_{d} - L_{q})\left[I_{q0}I_{d2}\cos(2\theta_{e} + \varphi_{d2}) + I_{d0}I_{q2}\cos(2\theta_{e} + \varphi_{q2}) + I_{d1}I_{q1}\cos(\theta_{e} + \varphi_{d1})\cos(\theta_{e} + \varphi_{d1})\cos(\theta_{e} + \varphi_{d1})\cos(\theta_{e} + \varphi_{d1})\cos(\theta_{e} + \varphi_{d1})\right]$$

$$T_{e3} \approx 3n_{p}I_{q3}\cos(3\theta_{e} + \varphi_{q3}) + 3n_{p}(L_{d} - L_{q})\left[I_{q0}I_{d3}\cos(3\theta_{e} + \varphi_{d3}) + I_{d0}I_{q3}\cos(3\theta_{e} + \varphi_{q3}) + I_{d1}I_{q2}\cos(\theta_{e} + \varphi_{d1})\cos(2\theta_{e} + \varphi_{d2})\right]$$

$$\left[I_{q1}I_{d2}\cos(\theta_{e} + \varphi_{q1})\cos(2\theta_{e} + \varphi_{d2}) + I_{d1}I_{q2}\cos(\theta_{e} + \varphi_{d1})\cos(2\theta_{e} + \varphi_{d2})\right]$$

where T_{e0} is the DC component of torque. T_{e1} , T_{e2} , and T_{e3} are the fundamental, the second, and the third harmonic torque ripples, respectively.

It is evident that there are both odd-numbered and even-numbered harmonic torque ripples in the torque under OSFs. The reason is that the current under OSFs comprises two components: zero and negative currents. Consequently, the $\alpha\beta$ -axis current contains DC components as well as even-numbered harmonic components, in addition to odd-numbered harmonic components. Clearly, the torque performance will deteriorate, and obvious torque ripples will occur under OSFs. It is essential to implement the FTC strategy under OSFs to improve its performance.

3.2. Proposed FTC Strategy for Single OSF

It is known that the copper loss and torque ripples can become excessive and cause damage to the motor when OSFs occur. Therefore, the objective of the FTC strategy for a single OSF in this paper is to reduce the torque ripples and copper loss. The copper loss of DTPMSM is calculated as

$$P_{cu_normal} = 3R_s \left(i_{\alpha}^2 + i_{\beta}^2 + i_{x}^2 + i_{y}^2 + i_{o1}^2 + i_{o2}^2 \right) \approx 3R_s \left(i_{\alpha}^2 + i_{\beta}^2 \right)$$
 (10)

where P_{cu_normal} is the copper loss under normal operation. i_{α} , i_{β} , i_{x} , i_{y} , i_{01} , and i_{02} are the current of different subspaces.

Under OSF conditions, the operation can be viewed as a blend of normal and OPF operations. Specifically, when the current of phase F is negative, the copper loss is the same as (10). When the current of phase F is zero, constraints for the *y*-axis and β -axis fault currents can be derived from the inverse matrix of (2).

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$$i_{\gamma f} = -i_{\beta f} \tag{11}$$

where $i_{\beta f}$ and i_{yf} are the fault currents of the β -axis and y-axis, respectively.

From (6), to maintain constant output torque, the current references of the dq-axis under OPFs must align with those under normal operation, meaning the copper losses in the $\alpha\beta$ subspace under the OPFs are identical to those under normal operation. Thus, the copper loss under a single OSF is then calculated as

$$P_{cu_OPF} = 3R_s \left(i_{\alpha}^2 + i_{\beta}^2 \right) + 3R_s \left(i_{xf}^2 + i_{\beta f}^2 \right) = P_{cu_normal} + 3R_s \left(i_{xf}^2 + i_{\beta f}^2 \right)$$
 (12)

where P_{cu_OPF} is the copper loss under OPF operation.

Thus, the copper loss under a single OSF is given by

$$P_{cu_OSF} = \frac{1}{2} P_{cu_normal} + \frac{1}{2} P_{cu_OPF} = P_{cu_normal} + 1.5 R_s \left(i_{xf}^2 + i_{\beta f}^2 \right)$$
 (13)

where P_{cu_OSF} is the copper loss under OSF operation.

From (2), it is evident that the x-axis current is independent of phase F. Since the x-axis current gives no contribution to torque production, the current reference of the x-axis is set to zero to minimize the copper loss, as follows.

$$P_{cu_FTC} = P_{cu_normal} + 1.5R_s i_{\beta f}^2 \tag{14}$$

where P_{cu_FTC} is the copper loss under FTC operation.

Apparently, the conventional FTC strategy of DTPMSM under OSFs can also be categorized into two parts: normal operation and OPF-based FTC. The current reference of the *x*-axis is set to zero, while the current reference of the *y*-axis under OSFs is determined as

$$i_y^* = \begin{cases} -i_{\beta}^*, & i_{Ff} \ge 0\\ 0, & i_{Ff} < 0 \end{cases}$$
 (15)

where i_{Ff} is the phase current of F under OSFs.

It is evident that the conventional FTC relies heavily on judging the magnitude of the fault current. However, the fault current cannot be exactly zero during OSFs, and it will be a value that is infinitely close to zero in practice. Therefore, (15) cannot set the threshold for judgement to exactly zero but rather to a value that is infinitely close to zero. Another challenge lies in setting an appropriate judgement threshold, which varies across different motors and operational conditions. Inappropriate threshold settings can lead to failure in entering FTC or engagement at incorrect times, both of which can impede performance improvement following a fault. While a virtual current with an accurate threshold has been proposed, it still cannot guarantee that the transition to FTC occurs at the optimal moment.

To enter FTC at the optimal moment and avoid misjudgment caused by sinusoidal current zero-crossing or sudden changes in load and speed, a novel FTC method based on actual current without relying on current judgement is proposed. Additionally, the *y*-axis current reference expression is derived using the Fourier series.

$$i_y^* = I_q^* \left[\frac{1}{2} \sin\left(\theta_e - \frac{\pi}{2}\right) - \sum_{n=1}^{\infty} \frac{2}{\pi (4n^2 - 1)} \cos\left[2n\left(\theta_e - \frac{\pi}{2}\right)\right] + 0.3183 \right]$$
 (16)

As shown in (16), the current reference of the *y*-axis is mainly composed of the DC component, the fundamental component, and the even harmonic component. The amplitude of each component is illustrated in Figure 4. From Figure 4 and (16), it can be observed that the amplitude of the DC component and the fundamental frequency component remain constant, while the amplitude of the even harmonic component decreases significantly as n increases. For simplicity of calculation and to avoid significant errors, n = 2 is selected.

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Subsequently, the current reference for the *y*-axis during the upper IGBT OSF of phase F can be determined as

$$i_{yupper}^{*} = I_{q}^{*} \left[\frac{1}{2} \sin \left(\theta_{e} - \frac{\pi}{2} \right) - \frac{2}{3\pi} \cos \left[2 \left(\theta_{e} - \frac{\pi}{2} \right) \right] - \frac{2}{15\pi} \cos \left[4 \left(\theta_{e} - \frac{\pi}{2} \right) \right] + 0.3183 \right]$$
 (17)

Similarly, the current reference of the *y*-axis under the lower switch OSF of phase F is

$$i_{ylower}^* = I_q^* \left[\frac{1}{2} \sin\left(\theta_e - \frac{\pi}{2}\right) + \frac{2}{3\pi} \cos\left[2\left(\theta_e - \frac{\pi}{2}\right)\right] + \frac{2}{15\pi} \cos\left[4\left(\theta_e - \frac{\pi}{2}\right)\right] - 0.3183 \right]$$
 (18)

Figure 5 illustrates the comparison of y-axis current references calculated by (15) and (17) when n = 2. It shows that the current reference calculated by (17) closely matches (15) in the positive half cycle. Although there are fluctuations in the zero-current region, they are relatively minor, making their impact on performance negligible.

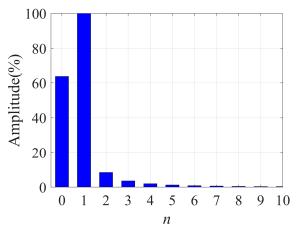


Figure 4. Proportion of different components of *y*-axis current when n = 10.

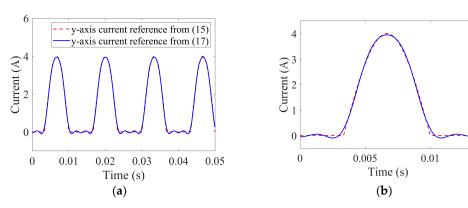


Figure 5. Comparison of *y*-axis current reference calculated by (15) and (17). (a) Overall view. (b) Zoomed-in view of one cycle.

3.3. Proposed Control Scheme

For the *xy*-axis currents, the traditional control method involves current closed-loop control using two PI controllers, where the current references are set to zero, denoted as $i^*_{xy} = 0$. However, it can be derived from (5) that the harmonic impedance in the *xy* subspace is very small, consisting of only stator resistance and leakage inductance. This can result in a significant proportion of fifth and seventh harmonic currents even with low harmonic voltages. Therefore, closed-loop control using two proportional–integral quasi-resonant (PIR) controllers is implemented to suppress the fifth and seventh current harmonics. Considering that the phase margin of the PIR controller decreases with increasing speed,

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phase correction is adopted to improve stability. Firstly, the transfer function of the quasiresonant controller with phase correction denoted as PCQPR, is expressed as

$$G_{PCQPR} = K_{pr} + K_r \cdot \frac{\omega_c [s\cos(\varphi) - \omega_n \sin(\varphi)]}{s^2 + 2\omega_c s + \omega_n^2}$$
(19)

where K_{pr} and K_r are the proportional and resonant coefficients, respectively. ω_c is the cut-off frequency. φ is the phase correction angle for the QPR controller. ω_n is the resonant frequency, which varies with speed.

To avoid affecting the current regulation performance of the existing vector control system, the proportional gain of the PCQPR controller is set to zero. The transfer function of the PCPIR controller, as shown in Figure 5, is then given by

$$G_{PCPIR}(s) = K_p + \frac{K_i}{s} + K_r \cdot \frac{\omega_c[s\cos(\varphi) - \omega_n\sin(\varphi)]}{s^2 + 2\omega_c s + \omega_n^2}$$
(20)

where K_p and K_i are the proportional and integral coefficients of the PI controller, respectively. Besides, for inverters using an SVPWM modulation strategy, the time delay is considered as $1.5~T_s\omega_e$. To compensate for the time delay, the angle θ_{e1} in Figure 5 is corrected by adding $1.5~T_s\omega_e$ [31]. Then, the open-loop transfer function of the current loop in the xy subspace can be derived as

$$G_{open}(s) = G_{PCPIR}(s) \cdot \frac{1}{R_s + sL_{ls}}$$
(21)

The introduced phase correction improves the system's phase margin without affecting the gain at the resonance frequency. Therefore, the parameter design for the PCPIR controller follows the same design rules for the PIR controller. Since the method for designing PI controllers is well-established, only the parameter design of ω_c and K_r for the resonant controller is provided as follows.

The cutoff frequency primarily influences the bandwidth of the controller. A smaller ω_c enhances the frequency-selective characteristics of the controller, but AC drive systems are sensitive to current frequency fluctuations caused by load torque disturbances, compromising system stability. The bandwidth of the resonant controller can be calculated by

$$f_b = \frac{\omega_c}{\pi} \tag{22}$$

where f_b is the bandwidth.

Based on the fluctuation range of the motor speed, the cutoff frequency ω_c can be calculated. The resonant coefficient K_r primarily affects the gain at the resonant frequency. As K_r increases, the steady-state error is reduced, while the system's phase margin decreases. Based on these design principles and experimental conditions, the parameters can be obtained.

It can be found from (5) that the voltage equations of the dx-axis and qy-axis are independent, with no coupling terms. The currents of the dx-axis and qy-axis are also independent of each other and orthogonal in the dx-qy coordinate system. Thus, the two PCPIR controllers can be designed and regulated independently. Additionally, since the parameters of the x-axis and y-axis are identical, the parameters for the two PCPIR controllers are the same. The parameters used for the PCPIR controllers in the experiment are presented in Table 1.

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Table 1	Parameters	of the	PCPIR	controller

Parameters	Value
K_p	4.263
$K_i^{'}$	10.454
K_r	121.8
ω_c	5

According to (20), (21), and Tables 1 and 2, the open-loop Bode diagram of the current loop with traditional PIR and proposed PCPIR is illustrated in Figure 6. It can be observed that the magnitudes of the current open loop with both controllers are nearly identical, but the phase margin is significantly improved with the proposed PCPIR. The phase margin is approximately 90°, indicating enhanced stability performance with the proposed PCPIR.

Table 2. Motor parameters.

Parameters	Value		
Pole number	3		
<i>d</i> -axis inductance	9.36 mH		
<i>q</i> -axis inductance	20.76 mH		
Leakage inductance	1.32 mH		
Stator resistance	0.68Ω		
PM flux-linkage	0.316 Wb		
Rated phase current	4 A		
Rated speed	1500 r/min		
Rated power	2.5 kW		

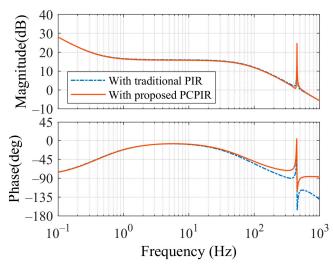


Figure 6. The open-loop Bode diagram of the current loop with traditional PIR and proposed PCPIR.

Subsequently, the complete proposed control block diagram of DTPMSM for both normal operation and FTC is presented in Figure 7. In the *xy* subspace, the *x*-axis current is consistently set to zero under different operation modes to minimize losses. The flag 'FLT' switches the current references of the *y*-axis automatically according to the fault diagnosis results. The FLT values of 0 and 1 represent normal operation and FTC under OSFs, respectively. There is no current judgement, and this allows for accurate and stable entry into FTC. The entire proposed control scheme under faulty operation and FTC is the same as the normal operation instead of the *y*-axis current reference, enabling unified control, simple implementation, and seamless switching of current and torque between faulty operation and FTC.

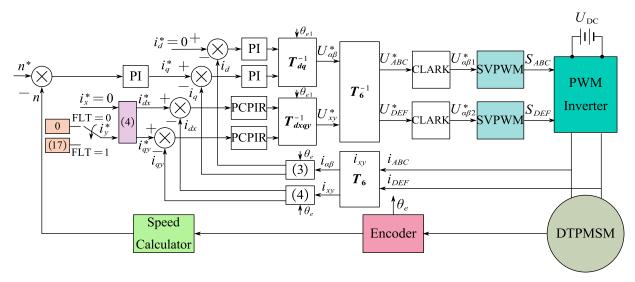


Figure 7. The proposed control scheme for both normal operation and FTC.

4. Experimental Verification

To validate the effectiveness of the proposed method, a dual three-phase experimental platform has been constructed, as depicted in Figure 8. The DTPMSM is powered by two parallel three-phase two-level voltage inverters. PWM signals are generated by a control algorithm implemented on the DSP28335 processor from Texas Instruments, Dallas, TX, USA. The current loop execution frequency is configured to 10 kHz, matching the PWM frequency. The dead time is set to 500 ns. Two independent SVPWM modulation strategies are used to generate PWM signals. The three-phase asynchronous motor is controlled by the inverter to vary the load torque. The speed and torque sensor outputs pulse signals. Figure 9 presents waveforms of the gate drive and voltage between the collector and emitter of the IGBT under normal operation. The parameters of DTPMSM are detailed in Table 2.

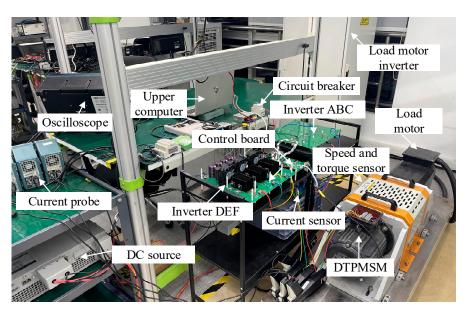


Figure 8. DTPMSM experimental platform.

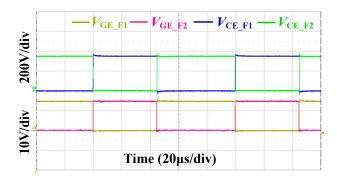


Figure 9. Waveforms of gate drive and voltage between collector and emitter.

4.1. Normal Operation

Due to experimental constraints, the simultaneous measurement of all six phase currents is not feasible; hence, phase currents of both sets of three-phase windings are shown separately in Figure 10a,b. The current waveforms for both sets of windings under the same control strategy are nearly identical, indicating their symmetry. Figure 10c displays the waveforms of the dq-axis currents, *xy*-axis currents, and torque at 1500 r/min under rated load, obtained from the upper computer. As observed in Figure 10c, there are minimal fluctuations in the dq-axis currents, *xy*-axis currents, and torque, indicating effective suppression of the fifth and seventh harmonic components in phase currents by PCPIR controllers in the *xy* subspace. The motor exhibits good performance under rated operating conditions.

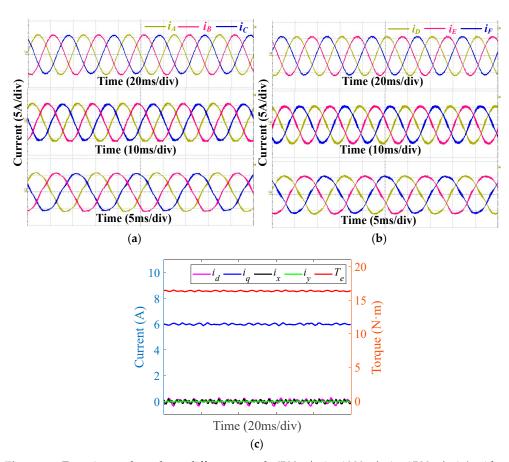


Figure 10. Experimental results at different speeds (500 r/min, 1000 r/min, 1500 r/min) with rated load. (a) Phase currents of ABC. (b) Phase currents of DEF. (c) *dq*-axis currents, *xy*-axis currents, and torque under rated operating conditions.

4.2. Single OSF and FTC

To simulate OSFs, the drive signal of the phase F upper IGBT in the program is forced to the low level. By comparing (6), (9), (10), (13), and (14), it can be found that both torque ripple and copper loss increase significantly under a single OSF and FTC operations. Therefore, to protect the experimental platform, derated experiments are conducted under these operations. Specifically, the speed is set to 1000 r/min, and the load is limited to 7.5 N⋅m. Figure 11 presents experimental results under the upper IGBT OSF of phase F without FTC. In Figure 11a,b, phase current waveforms illustrate that during the conduction time of the upper IGBT, the fault current of phase F is forced to a value that is infinitely close to 0. In this condition, the *y*-axis current and β -axis currents exhibit the same amplitude with opposite phases. In $\alpha\beta$ subspace, the amplitude of the α -axis current surpasses that of the β -axis current, resulting in noticeable second harmonic ripples in the dq-axis currents shown in Figure 11c,d. Conversely, during the conduction time of the lower IGBT, the fault current resembles normal operation, generating negative currents. Figure 11e and f present the torque waveforms and FFT results, illustrating that the upper IGBT OSF of phase F introduces the fundamental frequency torque ripple alongside the second and third harmonic torque ripples due to the absence of one switch.

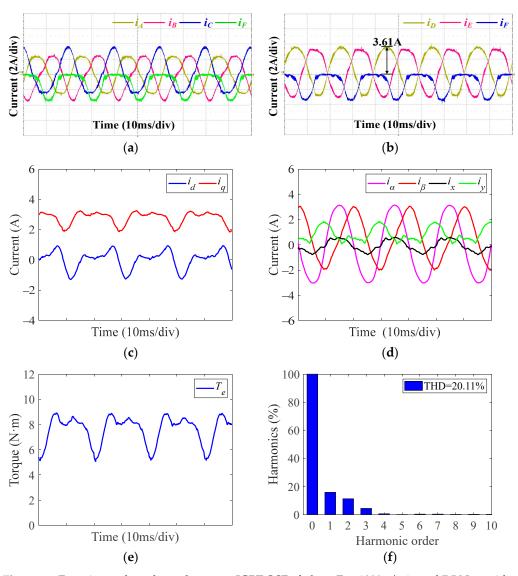


Figure 11. Experimental results under upper IGBT OSF of phase F at 1000 r/min and 7.5 N·m without FTC. (a) Phase currents of ABCF. (b) Phase currents of DEF. (c) dq-axis currents. (d) $\alpha\beta$ -axis and xy-axis currents. (e) Torque. (f) FFT results of torque.

Figures 12 and 13 present the experimental results under the upper switch OSF of phase F with the traditional FTC strategy. The various current judgement thresholds are 0 A, -0.2 A, and -0.5 A, respectively. The fault-tolerant performance is evaluated by analyzing the changes in the phase current, different subspace current, and torque. Figures 12a and 13a,b illustrate the experimental results when the judgement threshold is set to 0 A. A comparison with Figure 11 shows that the experimental results reveal a minimal difference between operation under the upper switch OSF without FTC and with the traditional FTC when the judgement threshold is 0 A. As a result, FTC cannot be effectively applied during the conduction period of the upper IGBT. Figures 12b and 13c,d present the experimental results when the judgement threshold is -0.2 A. For this threshold, obvious oscillations are observed in the phase F current and the xy-axis current during the conduction period of the upper IGBT, even with the application of FTC. The reason is that the phase F current fluctuates around -0.2 A during the conduction time of the upper switch, leading to discontinuities in FTC operation. As a result, although the intermittent application of FTC reduces the torque ripple, the torque ripple remains significant. Figures 12c and 13e,f show the experimental results when the judgement threshold is set to −0.5 A. Apparently, the motor operates under FTC. However, with a judgement threshold of -0.5 A, FTC unnecessarily engages during portions of the lower IGBT's conduction time when the current is smaller than -0.5 A.

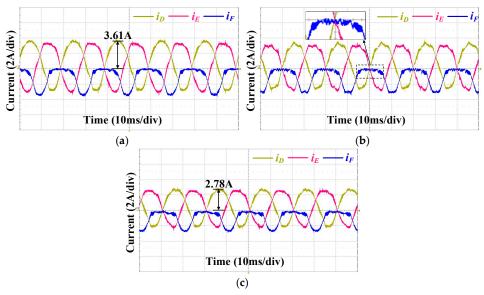


Figure 12. Experimental current waveforms under upper IGBT OSF of phase at 1000 r/min and $7.5 \text{ N} \cdot \text{m}$ with traditional FTC strategy using various judgement thresholds. (a) Judgement threshold is 0 A. (b) Judgement threshold is -0.2 A. (c) Judgement threshold is -0.5 A.

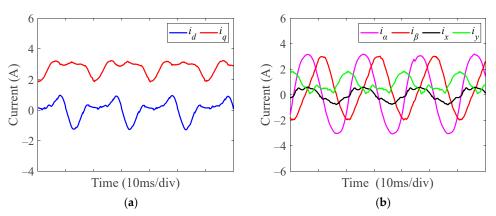


Figure 13. Cont.

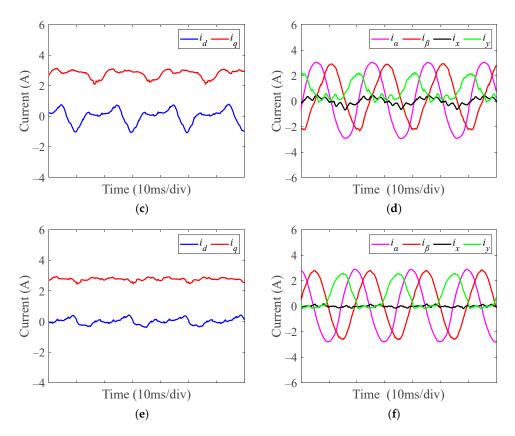


Figure 13. Experimental current waveforms of dq-axis, $\alpha\beta$ -axis, and xy-axis under upper IGBT OSF of phase F at 1000 r/min and 7.5 N·m with traditional FTC strategy using various judgement thresholds. (a,b) Judgement threshold is 0 A. (c,d) Judgement threshold is -0.2 A. (e,f) Judgement threshold is -0.5 A.

For a better understanding of the impact of different judgement thresholds on traditional FTC, the torque results under OSFs without FTC and torque with FTC under different judgement thresholds are compared in Figure 14. From Figure 14a, it can be seen that when the judgement threshold is 0 A, the torque with FTC is the same as the torque under OSFs without FTC. As seen in Figure 14b, the THD of torque decreases with lower judgement thresholds. It can be concluded that only lower judgement thresholds reduce torque ripples, highlighting the critical role of threshold selection in traditional FTC. The postfault performance of the conventional FTC significantly relies on threshold selection. Clearly, if the threshold selection is inappropriate, traditional current-based FTC cannot guarantee to enter FTC after a fault, nor can it ensure performance, thus lacking universality and robustness.

Figure 15 presents experimental results under the upper switch OSF of phase F with the proposed FTC strategy based on (17). In Figure 15d, it can be observed that the x-axis current is effectively controlled to be close to zero, thereby reducing copper losses. Figure 15c,e,f shows that the current harmonic components in the dq-axis currents are greatly reduced compared with Figure 11c, resulting in reduced torque ripples from 20.11% to 5.93%. Compared with Figure 14b, the torque ripple of the proposed method is much smaller than that of the traditional FTC at threshold values of 0 A and -0.2 A, but still slightly larger than that at a threshold value of -0.5 A. This is because the proposed method only approximates the first four harmonic components, resulting in relatively small errors. However, this avoids the selection of the current judgement threshold and ensures the robustness of fault tolerance. This performance improvement with the proposed FTC is evident when compared with Figures 11 and 14, highlighting its effectiveness.

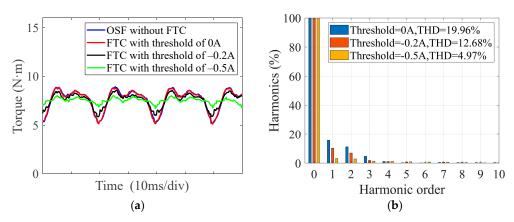


Figure 14. Experimental torque results under upper IGBT OSF of phase at 1000 r/min and $7.5 \text{ N} \cdot \text{m}$ with traditional FTC using various judgement thresholds. (a) Torque waveforms. (b) FFT results of torque.

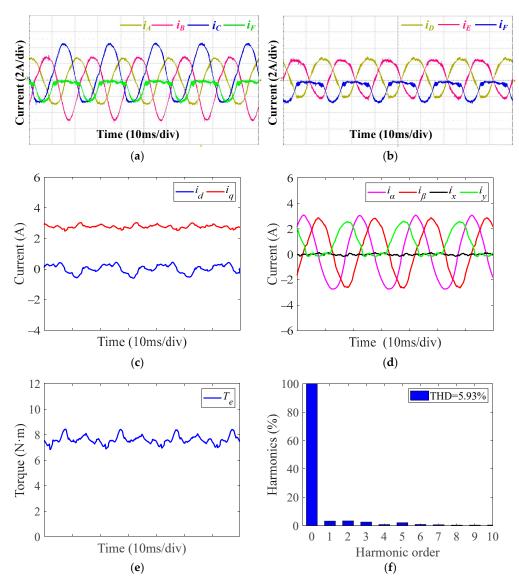


Figure 15. Experimental results under upper IGBT OSF of phase F at 1000 r/min and 7.5 N·m with proposed FTC. (a) Phase currents of ABCF. (b) Phase currents of DEF. (c) dq-axis currents. (d) $\alpha\beta$ -axis and xy-axis currents. (e) Torque. (f) FFT results of torque.

4.3. Dynamic Performance

Dynamic performance experimental verification is also essential alongside steady-state experimental verification. Figure 16 illustrates the dynamic performance of the DTPMSM transitioning from OSFs to traditional FTC using various current judgement thresholds, including phase currents and torque. It can be observed that no torque spikes occur when transitioning from OSFs to FTC operation. This is due to the continuity of the current from the negative half-cycle (normal operation) to the approximately zero half-cycle (traditional FTC). However, the torque performance of FTC varies depending on the selected current judgement threshold. Figure 17 depicts the experimental results during transitions from normal operation to OSFs and then to FTC. OSFs occur during the conduction time of the upper switch, so the phase F current is forced to be a value close to zero immediately, and this condition is similar to the OPF of phase F. At this moment, the torque decreases, as shown in Figure 17c. Then, the motor operates in the conduction time of the lower switch. During lower switch conduction, the operation is similar to normal operation, restoring torque to its reference level. From the next electrical period, the torque ripple starts to increase, and there is no sudden change in torque with the change operation mode, ensuring smooth torque transitions. Compared with Figure 16, the proposed method eliminates the need for a current judgement threshold, ensuring that the performance is not affected by threshold selection. This proposed approach guarantees a smooth and stable transition into FTC after an OSF fault.

Further dynamic performances of the proposed method under OSFs with varying rotational speed and load torque are given in Figure 18. First, the rotational speed is stepchanged from 100 to 1500 r/min and from 1500 to 100 r/min. The load torque is 7.5 N·m. The experimental results under OSFs are shown in Figure 18a. It can be seen that, with the change in rotational speed, the motor can still operate stably without overshoot and oscillation. In Figure 18b, the load torque is changed manually from 3.5 to 15 N·m and from 15 to 3.5 N·m, so the load change time is different. This will not affect the validation of the results. The rotational speed is 1000 r/min. Figure 18b illustrates that the motor can still operate stably without overshoot and oscillation. These results confirm that the proposed strategy achieves robust dynamic performance across different operational conditions.

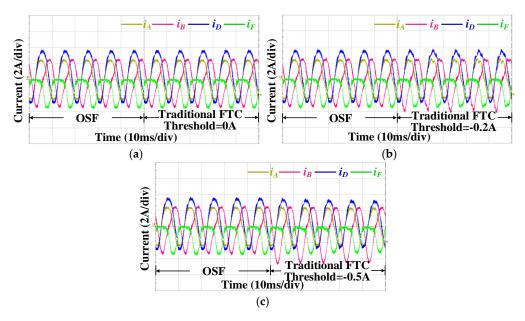


Figure 16. Cont.

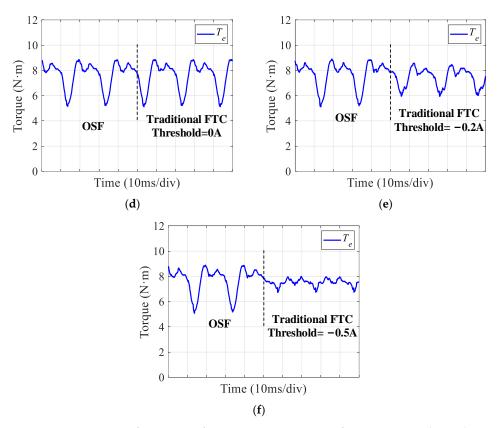


Figure 16. Dynamic performances of DTPMSM transitioning from OSF to traditional FTC using various judgement thresholds. (a) Phase currents with a judgement threshold of 0 A. (b) Phase currents with a judgement threshold of -0.2 A. (c) Phase currents with a judgement threshold of -0.5 A. (d) Torque with a judgement threshold of 0 A. (e) Torque with a judgement threshold of -0.2 A. (f) Torque with a judgement threshold of -0.5 A.

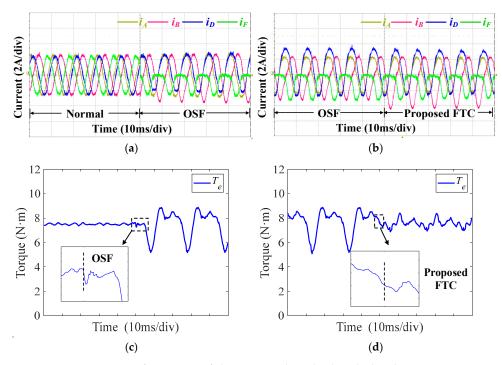


Figure 17. Dynamic performances of the proposed method with the change in operation mode under OSFs. (a) Phase currents from normal operation to OSFs. (b) Phase currents from OSF to FTC. (c) Torque from normal operation to OSFs. (d) Torque from OSF to FTC.

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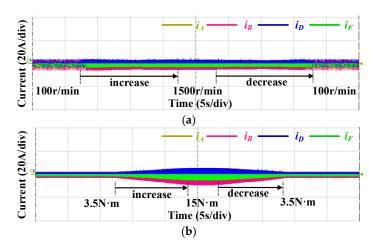


Figure 18. Dynamic performances of the proposed method under OSFs. (a) Rotor speed step-changing. (b) Load torque changing.

5. Conclusions

This article proposed an optimized FTC strategy for a single OSF of DTPMSM drives. The operations under OSFs are analyzed, and the reason for increased torque ripples are investigated. The results show that the torque ripples are inevitable due to the absence of one switch in the bridge arm. Based on the theoretical analysis, the FTC method is proposed for DTPMSM under OSFs with the same output torque. The transformation matrix and control scheme of the proposed FTC is the same as that in normal operation; thus, it has unified control and simple implementation. Smooth transitions of current and torque can be realized between faulty operation and FTC. Compared with traditional FTC methods, the proposed strategy demonstrates superior performance and robust application without the requirement for current judgement. Theoretical analysis and steady-state experiments show a significant reduction in torque ripples under OSFs with the proposed FTC strategy. Dynamic experiments further validate the proposed strategy, highlighting its ability to achieve stable performance under different operations.

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