



Article

High Performance Single and Double Loop Digital and Hybrid PID-Type Control for DC/AC Voltage Source Inverters

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Abstract: The concept of hybrid control has been introduced, in which the analog implementation of the control algorithm is combined with a digital algorithm for determining the PWM duty cycle. Single-loop PD and PID control systems are compared to a double-loop architecture with additional capacitor current sensing for both digital and hybrid controller realizations. The performance is measured as the THD value under resistor-capacitor rectifier load. It has been shown that a properly constructed continuous-time model of a digital controller with a PWM power converter behaves like the actual discrete-time system, which allows for a simple controller analysis and design. The role of a PWM type for capacitor current feedback is emphasized. The simulation models of a real inverter are presented, which are used to tune the controllers and to evaluate the control performance for both rectifier and abruptly changing resistive load. The obtained solutions achieve the THD values comparable to the VSI without load. The results are contrasted with the control methods based on resonant filters.

Keywords: UPS inverter; carrier frequency; PWM types; PID control; resonant controllers; hybrid analog-digital control; capacitor current feedback; THD performance evaluation; modeling; simulation



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1. Introduction

DC/AC Voltage Source Inverters (VSI) are commonly used as a basic component of Uninterruptible Power Supply (UPS) units to convert the DC energy contained in batteries into the appropriate AC voltage when the mains power fails. Due to the ever increasing availability of renewable energy sources, single-phase UPS inverters have also found wide application in supplying local electric power networks.

The sinusoidal output voltage of inverters consisting of conservative passive components and semiconductor components acting as switches is obtained by a Pulse Width Modulated (PWM) signal.

The performance of the inverter is usually measured as the Total Harmonic Distortion (THD) value of the output voltage under the standard non-linear rectifier resistor-capacitor (RC) load. Another measure of performance is the distortion of the output voltage caused by a sudden decrease or increase of the resistive load. In the article, we use both performance indicators regardless of the source of the distortion.

Since the performance of simple inverters without feedback control is usually not satisfactory, many control schemes and algorithms have been proposed in the literature. Since the 1990s, articles have been published that consider combining almost all known control algorithms that appeared then in control theory, including both continuous and discrete, with various inverter models.

Recent developments include digital control strategies such as repetitive control [1–5], dead-beat control [6,7], and discrete-time sliding mode control [8,9].

Due to the sinusoidal reference, a popular concept is to use resonant controllers that have excellent reference tracking ability under a constant resistive load. Unfortunately,

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single-loop control based on a Resonant (R), Proportionally Resonant (PR) [10–13], or Repetitive Controller (RC) [2], have poor disturbance rejection properties.

Therefore, a double-loop architecture was proposed, where the outer loop should follow the reference sine wave and the inner loop should be responsible for disturbance attenuation. For this purpose, the outer loop was equipped with a Resonant (R), Proportional Resonant (PR), or Repetitive Controller (RC), and the inner loop for controlling the filter capacitor current was usually based on a proportional controller [2,14–16].

Even more complex multi-loop structures with DC bus voltage decoupling and load current compensation are shown and discussed in [17]. Another sophisticated multi-input solution is the Passivity-Based Control (PBC) [18–20] which uses three input variables: Output voltage, and the inductor and output currents [7].

Since the task of the VSI control is to stabilize the amplitude of the output voltage, not its phase, reasonable solutions focus on disturbance rejection rather than reference tracking. This is possible both in the double-loop structure and in a simple single loop that does not require additional sensors. This makes it possible to use a simpler Proportional-Integral (PI) controller in the outer loop in a double-loop structure, or a Proportional-Integral-Differential (PID) in the single-loop structure, instead of a resonant R controller or proportionally-resonant PR which cause bad dynamics [14,17,21–23]. To maintain the amplitude of the output voltage, this may only require modifying the amplitude of the reference signal. One approach in this category is the Coefficient Diagram Method (CDM), which leads to single loop with dynamical controllers of higher order [7,24,25].

It should be noted that it is practically impossible to compare the results presented in the literature. They are written at various levels of abstraction, ranging from a verbal description of the expected features of presented systems and ending with specific quantitative results. The power part is modeled very differently, ranging from treating the PWM signal as continuous, leading to a continuous-time description, to sophisticated discrete-time models [26,27]. Some papers, e.g., [2,17], completely neglect digital data processing and treat inverters as continuous-time systems. Others, such as [12,15], only use the sampling rate to discretize continuous-time controllers that were designed, neglecting the effect of sampling and PWM.

To evaluate different algorithms, comparisons should be made for inverters with the same parameters. This requirement is fulfilled in [7,28–30] where the results refer to the same physical system. Unfortunately, it is unclear to what extent the conclusions are valid for other systems. For more general results, a theoretical model for control design is required along with a simulation model to validate the results. This gives flexibility, which makes it possible to test systems with different parameters. Such methodology, based on the Quasi-Continuous-Time (QCT) model of the PWM-controlled system, developed in [31] for a single-loop digital PID control, has been extended in this article to include hybrid analog-digital control and double-loop structures.

The article is inspired by the research devoted to double-loop control structures with additional capacitance current control [14,17,21–23], and on the influence of the sampling rate on single-loop digital PID inverter control [31]. The positive effect of improving performance with an increasing sampling rate is mainly due to the shortening of the one-step delay introduced by the digital controller. This delay can be eliminated by replacing the digital controller by a delay-less analog algorithm. Such a controller can be embedded in a digital control system forming a hybrid control system in which an analog setpoint is obtained as the output of DAC converting the reference value taken from a pre-computed table, and the analog controller output is converted to the digital one used to determine the duty ratio for a digital PWM forming system.

The article focuses on the comparison of a single-loop Proportional-Differential (PD) or PID-type control with a double-loop structure consisting of an inner P capacitor current control loop and an outer P or PI output voltage control loop. Both analog and digital realizations of the control algorithms are taken into account and the influence of both the PWM type and sampling frequency is studied. All control structures are optimally

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tuned to the RC rectifier load case by simulations carried out using the QCT approach and validated on the PWM model. For comparison with other results, the article is based on the parameters of an experimental VSI designed for the carrier frequency $f_s = 25.6$ kHz, which was presented in [7,29,30]. Although it was designed for the carrier frequency $f_s = 25.6$ kHz, it will be used in the article for two additional frequencies, 12.8 and 51.6 kHz.

2. Double-Loop Control Architecture, PWM Strategies, and QCT Inverter Model

2.1. Double-Loop Control Architecture

Consider the systems depicted in Figure 1 and assume that the Equivalent Serial Resistance (ESR) of the capacitor is negligible.

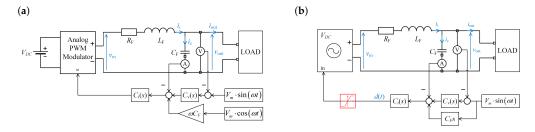


Figure 1. (a) Schematic diagram of a double-loop analog control system with the capacitor current feedback and an analog PWM modulator, (b) continuous-time model with one reference input according to [14,17,21-23]. R_F is the ESR of the choke.

A distinctive feature of a double-loop structure, proposed in [14,17,21–23] and depicted in Figure 1, consisting of an inner capacitor current control loop and an outer voltage control loop, is that the current i_c is proportional to the first derivative of v_{out} , i.e.,

$$i_c(t) = C_F \frac{d}{dt} v_{out}(t). \tag{1}$$

As a result, instead of differentiating $v_{out}(t)$ to be used in control algorithms, which can not be done exactly, the signal proportional to the exact derivative of $v_{out}(t)$ can be sensed. Two combinations of controllers were proposed:

$$C_v(s) = k_v, C_i(s) = k_i, (2)$$

$$C_v(s) = k_v \frac{s+c}{s}, \qquad C_i(s) = k_i. \tag{3}$$

The first one, called further 2L P+P, is equivalent with a single-loop control system fitted with an ideal PD controller, while the second, called 2L PI+P, is equivalent with a single-loop control system fitted with an ideal PID controller. Their transfer functions are:

$$C(s) = (k_v + sC_F)k_i \text{ and } C(s) = (k_v \frac{s+c}{s} + sC_F)k_i.$$
 (4)

It should be emphasized that neither the substitute diagram of Figure 1b nor the controllers with a purely differentiating term are physically realizable, and as a result, double-loop systems cannot be simply replaced by single-loop ones. This also suggests that the double-loop structures can be superior to the single-loop ones, but at the cost of an additional sensor. Therefore the main aim of the article is to compare the performance of these structures.

The systems presented in the aformentioned literature were purely analog with the natural PWM created by comparing a saw-tooth carrier with the analog output from the current controller. Any analysis was done assuming the PWM signal as a continuous-time one. It can easily be shown that such a system would be stable at any, even very large,

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values of k_i and k_v , leading to ideal control with a control error approaching zero. As for the actual PWM system, this is of course not true.

Nowadays, in the digital age, analog systems are considered obsolete since all analog solutions can be cheaply replaced by the digital ones. However, in the area of high-speed control systems, this is not entirely true since computations take time that leads to delays affecting the performance. Therefore, we also consider hybrid control, taking the advantages of both analog and digital techniques with an analog delay-less controller core and digital set-point and PWM generation.

To summarize, our task is to consider and compare double-loop and single-loop structures in both purely digital as well hybrid realization. This is illustrated in Figures 2 and 3 along with the notation used to distinguish the particular structure.

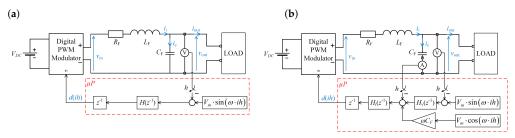


Figure 2. Schematic diagrams of digital control systems: (a) 1Ld-digital single loop, (b) 2Ld-digital double loop. Switches symbolize sampling performed by Analog-to-Digital-Converters (ADC).

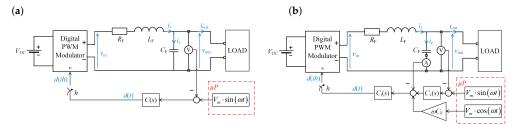


Figure 3. Schematic diagrams of hybrid control systems: (a) single loop, 1Lh (b) double-loop, 2Lh. It is assumed that the continuous sine wave is produced from the discrete sinusoidal reference passed by a Digital-to-Analog-Converter (DAC) and an analog smoothing filter.

2.2. Preliminaries: Open Loop System

The LC filter acts as a controlled plant, the output of which is to follow the amplitude of the reference sine wave $V_m \sin \omega t$ despite the variable load depicted in Figure 4.

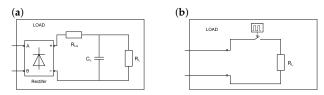


Figure 4. (a) Schematic diagram of the rectifier RC load, (b) abruptly changing resistive load. According to [7,28,29,31], the parameters for th rectifier load in (a) are $R_{Ls} = 1 \Omega$, $C_L = 430 \mu$ F, $R_L = 100 \Omega$. For the abruptly changing resistive load in (b) there is $R_L = 500 \Omega$. R_L is periodically turned on when the reference sine wave $V_m \sin(\omega t) = 0$, and off when it reaches V_m or $V_m = 0$.

The dynamics of the inverter can be represented by transfer functions K(s) related to particular modes of operation. As shown in [31], for the idle period there is:

$$K(s) = \frac{1}{L_F C_F s^2 + R_F C_F s + 1}$$
 (5)

for the resistive load:

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$$K(s) = \frac{1}{L_F C_F s^2 + \left[\frac{L_F}{R_I} + R_F C_F\right] s + 1 + \frac{R_F}{R_I}}$$
(6)

and for the conducting rectifier mode:

$$K(s) = \frac{b_1 s + b_0}{a_3 s^3 + a_2 s^2 + a_1 s + a_0},\tag{7}$$

with $a_3 = R_{Ls}R_LC_LL_FC_F$, $a_2 = R_{Ls}C_F(L_F + R_FR_LC_L) + R_LL_F(C_F + C_L)$, $a_1 = R_{Ls}(R_FC_F + R_LC_L) + R_LR_F(C_F + C_L) + L_F$, $a_0 = R_{Ls} + R_L + R_F$, $b_1 = R_{Ls}R_LC_L$, $b_0 = R_{Ls} + R_L$.

For

$$R_F = 1 \Omega$$
, $L_F = 1 \text{ mH}$, $C_F = 50 \mu\text{F}$ (8)

as in [7,29], the characteristics of these transfer functions are presented in Figure 5.

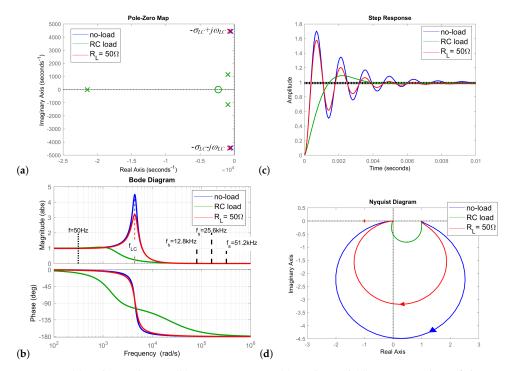


Figure 5. (a) Poles and zero, (b) step responses, (c) Bode, and (d) Nyquist plots of the open-loop system in two modes. Blue lines—idle, green lines—activ load. In the no-load mode there is $\sigma_{LC}=500$, $\omega_{LC}=4444$. The resonant frequencies f_{LC} are 702.8 Hz for $R_L=\infty$, and 701.4 Hz for $R_L=50$. Note small differences between characteristics of the no-load and the resistive load system, and a large discrepancy between the no-load and RC load system.

2.3. PWM Strategies

The inverters are controlled by the PWM signal produced by the appropriate switching of MOSFET or IGBT transistors in the the H-bridge legs which produces a three-level PWM signal taking the values V_{DC} , $-V_{DC}$ and 0 whose entire length over the carrier period h equals to d(ih)h. Duty ratio d(t) plays the role of the control variable. There are many PWM strategies that use different switching techniques [32] implemented in microcontrollers as so-called PWM with regular sampling, as opposed to natural PWM produced using analog comparators. Figure 6 shows two symmetrical double-edge PWM along with the single-edge PWM and PAM. Symmetrical double-edge PWM signals are denoted by PWM $_{\Lambda}$ and PWM $_{V}$ according to the isosceles shape of the triangular carrier signal. PWM $_{\Lambda}$ consists of two pulses of width hd(ih)/2: One at the beginning of the sampling period and one at the end. PWM $_{V}$ consists of one pulse of width hd(ih) symmetrical around the centre of the

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period. As opposed to them, a non-symmetrical single-edge PWM $_S$ signal is produced by comparison of the constant value with a sawtooth signal. Pulse Amplitude Modulation (PAM) is a standard modulation strategy used in the classical theory of sampled-data control systems. Its amplitude equals to $V_{DC}d(ih)$. As a result, the area of all pulses equals to $V_{DC}hd(ih)$. The PWM modulators are built into modern signal processors and digital controllers. Pulse Amplitude Modulation (PAM) has a purely theoretical value in the VSI context.

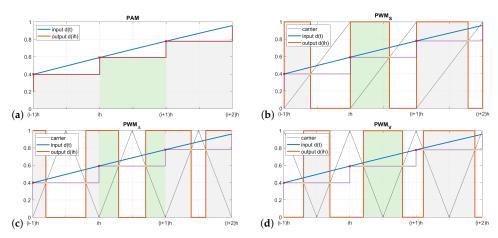


Figure 6. Illustration of various modulation methods: (a)-PAM, (b)-PWM_S, (c)-PWM_{Λ}, (d)-PWM_V. Red crosses denote samples of the input signal, yellow dots-switching instants on the intersection of the modulating function with appropriate sampled value.

2.4. QCT Model of the PWM Controlled Inverter Output

Consider two models of the open-loop system with the rectifier RC load depicted in Figure 7, where:

$$M = \frac{V_m}{V_{DC}}. (9)$$

is called the modulation index. In the article $V_m = 20$ V and $V_{DC} = 40$ V are assumed as default values.

The first model contains a PWM modulator, the second assumes a linear controllable voltage source with a reference signal delayed by τ . The appropriate value of τ can be determined as a mean delay introduced by the modulator.

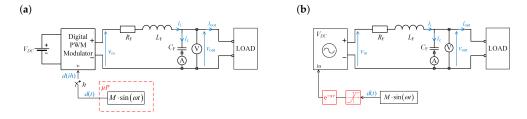


Figure 7. (a) Open loop PWM controlled system model, and (b) QCT model of the system in (a).

Assume that the system is unloaded and denote:

$$v_{out}(t) = \sum_{k=0}^{\infty} A_k \sin(k\omega t + \varphi_k)$$
 (10)

the output signal $v_{out}(t)$, and:

$$\gamma = \frac{\tau}{h} = \frac{\varphi_1^{ref} - \varphi_1}{\omega h} \tag{11}$$

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the relative delay, where φ_1^{ref} is the phase shift introduced by the LC filter, and φ_1 is the phase shift of the first harmonic after passing the sinusoidal reference through the modulator and filter. The results for three values of the sampling rate f_s and three values of the modulation index M are collected in Table 1.

Table 1. Values of the relative delay γ as functions of M and f	s.
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M	f_s (kHz)	PAM	PWM_S	PWM_{Λ}	PWM_V
	12.8	0.5000	0.0849	0.5000	0.5000
0.2	25.6	0.5000	0.0849	0.5000	0.5000
	51.2	0.5000	0.0849	0.5000	0.5000
	12.8	0.5000	0.2122	0.5000	0.5000
0.5	25.6	0.5000	0.2122	0.5000	0.5000
	51.2	0.5000	0.2122	0.5000	0.5000
	12.8	0.5000	0.3395	0.5000	0.5000
0.8	25.6	0.5000	0.3395	0.5000	0.5000
	51.2	0.5000	0.3395	0.5000	0.5000

It is noticeable that PAM, PWM_V, and PWM_{Λ} are characterized by $\tau = h/2$, while PWM_S lives its own life. However, although the system with PWM_S is nonlinear, the delay does not exceed h/2. Linearity is one reason to prefer symmetrical PWM.

2.5. Distortion Functions and Total Harmonic Distortion-THD

Denoting the distortion function $\psi(t)$ as:

$$\psi(t) = \frac{v_{out}(t) - A_1 \sin(\omega t + \varphi_1)}{A_1} = \frac{1}{A_1} \sum_{k=2}^{\infty} A_k \sin(k\omega t + \varphi_k), \tag{12}$$

the quality of the output voltage can be expressed by the value of THD_H defined as:

$$THD_{H} = \frac{1}{A_{1}} \sqrt{\sum_{k=2}^{H} A_{k}^{2}}$$
 (13)

for H high enough, i.e., covering an interesting range of harmonics. Similarly, a distortion function $\chi(t)$ can be defined for a control signal determined by the duty ratio d(t). Let:

$$d(t) = \sum_{k=0}^{\infty} B_k \sin(k\omega t + \phi_k). \tag{14}$$

Then the distortion function of d(t) is:

$$\chi(t) = \frac{d(t) - B_1 \sin(\omega t + \phi_1)}{B_1} = \frac{1}{B_1} \sum_{k=2}^{\infty} B_k \sin(k\omega t + \phi_k).$$
 (15)

2.6. Distortion and THD Function in Open-Loop VSI without Load

A comparison of the distortion functions for an unloaded VSI with open loop, characterizing the deviation of the actual output from its first harmonic, obtained for various PWM types and various carrier frequencies f_s , is presented in Figure 8. The residual values of the carrier frequency are represented there as time functions. Surprisingly, they can have quite large values, especially at 12.8 kHz. The THD values are collected in Table 2 for all modulation methods. Note that they depend on both the modulation factor M and f_s . They play the role of a lower bound for a feedback-controlled VSI.

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Since the THD values for PWM_{Λ} are smaller than those for PWM_S, this is the second reason that PWM_{Λ} is used in the article in contrast to [31] which is based on PWM_S.

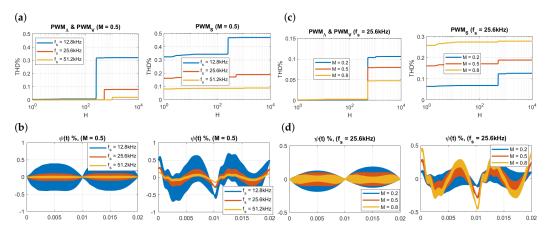


Figure 8. Dependence of $\psi(t)$ and THD_H from f_s and M in the no-load open loop PWM_V and PWM_S controlled system. (**a**,**b**): $\psi(t)$; M = 0.5, $f_s = 12.8$, 25.6 and 51.2 kHz, (**c**,**d**): $\psi(t)$; $f_s = 25.6$ kHz, M = 0.2, 0.5, 0.8.

The THD values for PAM depend only on f_s and are more than two orders smaller than those of PWM_{Λ}. Unfortunately, since PAM is technically irrelevant to the VSI, this finding is only of theoretical value.

Table 2. The	alues of THD_{∞} for a no-load open-loop PWM-controlled system as a function of M
and f_s .	

PWM_Λ and PWM_V					PWM_S		
f_s	f _s M			M			
(kHz)	0.2	0.5	0.8	0.2	0.5	0.8	
12.8	0.4263	0.3201	0.1913	0.4479	0.4693	0.5814	
25.6	0.1063	0.0798	0.0477	0.1266	0.1892	0.2786	
51.2	0.0266	0.0199	0.0119	0.0435	0.0881	0.1378	

2.7. Capacitor Current and QCT Method

While the output voltage $v_{out}(t)$ seems to be a smooth function of time, the capacitor current $i_c(t)$ is not. Since the capacitor current $i_c(t)$ is proportional to the first derivative of the output volatage $v_{out}(t)$, and $v_{out}(t)$ is contaminated with a ripple presented in Figure 8, then the ripple gets amplified in $i_c(t)$. Figure 9 presents i_c obtained assuming three types of PWM together with the output from the QCT model and the values sampled at sampling instants. It should be noted that despite the widely scattered inter-sample values, the samples obtained using PWM_{Λ} and PWM_V coincide with the results of the QCT model. With the exception of the PWM_S , there is a close proximity of all models at sampling points in spite of large inter-sample variability of PWM-controlled systems. This also justifies the QCT model for non-smooth outputs provided that the appropriate modulation type is selected. Therefore, in the article, PWM_{Λ} is chosen.

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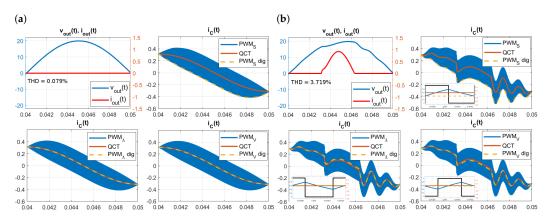


Figure 9. Capacitor current $i_c(t)$ in the open-loop inverter of Figure 7a using different modulation types compared with QCT output of the model of Figure 7b and the sampled values. The plots in (a) are for the no-load system and in (b) for the inverter with a RC rectifier load. Observe that for PWM_{Λ} and PWM_V the sampled values and the QCT values overlap, and for PWM_S they differ. Details are shown for individual sampling periods enlarged in the figures in (b).

3. Control Systems

3.1. Remarks on the Loop and Controller Gains

Note that in the schematic diagrams depicted in Figure 3 and 4, the control systems work with original variable v_{out} expressed in volts. This is of course not true in the physical VSI system where these variables are scaled by the measurement path to $v'_{out}(t)$ such that:

$$v'_{out}(t) = k_{xa}v_{out}(t), (16)$$

where the non-dimensional k_{xa} is the gain of the measurement path chosen so that e.g., $v'_{out}(t)$ fits the allowable range of further components. The variable $v'_{out}(t)$ is either sampled in a digital control system, or it is a physical variable expressed in volts in analog systems. In the hybrid control system, the physical output of the controller is sampled by an ADC. In either case, sampling means conversion form volts to machine units, which can be expressed as $[x(ih)] = k_{ad}x(ih)$, where x(ih) are samples of a variable x(t) standing either for $v_{out}(t)$ or the output u(t) of the analog controller expressed in volts, and [x(ih)] is its digital representation expressed in machine units. As a result, the dimension of k_{ad} is 1/V, and in the digital control system the actual output of the VSI is represented by its digital counterpart according to the formula:

$$[v_{out}(ih)] = k_D v_{out}(ih), \text{ where } k_D = k_{xa} k_{ad}, (1/V).$$
(17)

Conversion of the digital controller output [u(ih)] into the duty cycle involves another gain k_{PWM} such that $d(ih) = k_{PWM}[u(ih)]$. As described in [31], k_{PWM} depends on f_s . Moreover, it depends on the PWM type, and for PWM $_{\Lambda}$ or PWM $_{V}$ is twice as large as for PWM $_{S}$. In the hybrid control system, the machine representation of the controller output u(ih) is $[u(ih)] = k_D u(ih)$. And finally, the amplitude of the PWM signal is V_{DC} . As a result, the entire loop gain k_c whose value determines the properties of the closed loop can be expressed as:

$$k_c = V_{DC} k_{PWM} k_D k_C', \tag{18}$$

where k'_c is the controller gain.

In further considerations, we use k_c or its equivalent as the loop gain. Then the gain of the physically existing controller is:

$$k_c' = \frac{k_c}{V_{DC}k_{PWM}k_D}. (19)$$

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Similar considerations can be made for double loop system also with regard to $i_c(t)$, such that $i'_c(t) = k_{xa}i_c(t)$ with $i'_c(t)$ expressed in (V), but they do not influence the loop gain represented by k_i in the same way as k_c in the single loop.

3.2. Digital Control Systems

The digital single-loop control systems with a PID controller have been the subject of extensive analysis in [31]. Here we extend the class of controllers to the PD ones. The appropriate discrete-time transfer functions are:

1Ld (PD):
$$H(z) = k_c(b_0 + b_1 z^{-1}),$$
 (20)

1Ld (PID):
$$H(z) = k_c \frac{b_0 + b_1 z^{-1} + b_2 z^{-2}}{1 - z^{-1}}$$
. (21)

The QCT controllers are obtained by the following transformation:

$$C(s) = H(z)|_{z^{-1} = \frac{1 - s\frac{h}{2}}{1 + s\frac{h}{2}}}.$$
(22)

As a result the QCT controllers in Figure 10a corresponding with those in (13)–(14) are:

1Ld (PD):
$$C(s) = k_c^* \frac{s+c}{\frac{h}{2}s+1} = k_c \frac{s+c}{s+\frac{2}{h}},$$
 (23)

1Ld (PID):
$$C(s) = k_c^* \frac{(s+c_1)(s+c_2)}{s(\frac{h}{2}s+1)} = k_c \frac{(s+c_1)(s+c_2)}{s(s+\frac{2}{h})}.$$
 (24)

Discrete-time controllers applied in the double-loop structure are as follows:

2Ld (P+P):
$$H_v(z) = k_v$$
, $H_i(z) = k_i$, (25)

2Ld (PI+P):
$$H_v(z) = k_v \frac{b_0 + b_1 z^{-1}}{1 - z^{-1}}, \quad H_i(z) = k_i.$$
 (26)

The corresponding QCT controllers in Figure 10b are:

2Ld (P+P):
$$C_v(s) = k_v$$
, $C_i(s) = k_i$, (27)

2Ld (PI+P):
$$C_v(s) = k_v \frac{s+c}{s}, \quad C_i(s) = k_i.$$
 (28)

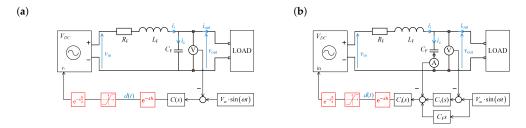


Figure 10. Schematic diagrams of QCT models for digital control systems depicted in Figure 3a: (a) 1Ld-single loop and (b) 2Ld-double-loop with the capacitor current feedback.

3.3. Hybrid Control Systems

The subject of this section is the analysis of control systems depicted in Figure 3. Consider schematic diagrams of QCT models of hybrid control systems depicted in Figure 11.

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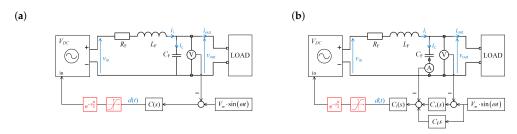


Figure 11. Schematic diagrams of QCT models for hybrid control systems depicted in Figure 2: (a) 1Lh-single loop and (b) 2Lh-double-loop with the capacitor current feedback.

Analog controllers of the single-loop structure are:

1Lh (PD):
$$C(s) = k_c^* \frac{s+c}{\mu s+1} = k_c \frac{s+c}{s+\frac{1}{\mu}},$$
 (29)

1Lh (PID):
$$C(s) = k_c^* \frac{(s+c_1)(s+c_2)}{s(\mu s+1)} = k_c \frac{(s+c_1)(s+c_2)}{s(s+\frac{1}{\mu})}$$
 (30)

with $k_c = k_c^{\star}/\mu$, while in the double-loop control system there is:

2Lh (P+P):
$$C_v(s) = k_v$$
, $C_i(s) = k_i$, (31)

2Lh (PI+P):
$$C_v(s) = k_v \frac{s+c}{s}, \quad C_i(s) = k_i.$$
 (32)

The denominator of the transfer function in Equation (29) and (30) acts as a filter enabling the physical impementation of the PID controller. The time constant $\mu > 0$ should be small, but at the same time large enough not to amplify noises or ripples contained in the sensed voltage $v_{out}(t)$. We assume $\mu = h/2$ as an option compatible with a QCT model in Equation (24) of the discrete-time PID regulator in Equation (21). Then the fundamental advantage of hybrid control over digital is the lack of one-step delay in the control path in Figure 11, as it is in Figure 10.

3.4. Optimal Controller Tuning

As far as tuning of the control systems is concerned, it described in detail in [31] for single-loop discrete-time controllers. With small modifications, it can also be used for hybrid controllers. More modifications are necessary for differently parameterized double-loop systems, where for the hybrid control there is:

$$C(s) = (k_v + sC_F)k_i = k_iC_F(s + \frac{k_v}{C_F}) = k_c^*(s + c),$$
(33)

$$C(s) = (k_v \frac{s+c}{s} + sC_F)k_i = \frac{k_i C_F}{s} (s^2 + \frac{k_v}{C_F} s + \frac{k_v k_\sigma \sigma_0}{C_F}),$$
 (34)

$$= k_c^* \frac{(s+c_1)(s+c_2)}{s}. \tag{35}$$

The easiest way is to express THD as a function of parameters k_v or k_v , k_σ and to find its minimum assuming the value of k_i providing a certain value ΔA of the gain margin for the no-load system. Another method consists in determining $c_1 = \hat{c}_2$ using methods of [31]. Exemplary surfaces of the THD values as functions of parameters are shown in Figure 12. Note the flatness of THD near the extremum. From this it follows that no particular precision of analog PID components is required.

It should also be noted that the open-loop gain, which determines the dynamics of the system, is the product of the controller gain and V_{CD} . The gain values presented in Table 3 and Figure 13 and 14 should be interpreted as open-loop gains, not just controller gains.

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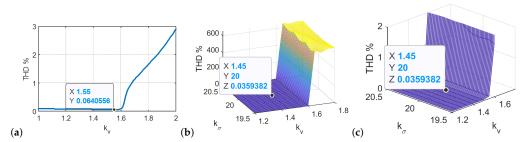


Figure 12. Exemplary surfaces of THD as functions of parameters (a) THD(k_v) for 2Lh (P+P) and (b) THD(k_v , k_σ) for 2Lh (PI+P), (c) zoomed in view of (b).

Table 3. Values of the optimal gains k_c , k_i , k_v for different systems, $(f_s = var)$.

f _s (kHz)	1L (PD)	2L (P+P)	1L (PID)	2L (PI+P)
digital 12.8 25.6 51.2	$k_c = 8.5$ $k_c = 31.7$ $k_c = 124.6$	$k_i = 8.4, k_v = 0.20$ $k_i = 15.5, k_v = 0.50$ $k_i = 30.4, k_v = 1.01$	$k_c = 9.1$ $k_c = 35.9$ $k_c = 146.9$	$k_i = 8.1, k_v = 0.22$ $k_i = 17.2, k_v = 0.45$ $k_i = 37.2, k_v = 0.80$
hybrid 12.8 25.6 51.2	$k_c = 19.1$ $k_c = 73.8$ $k_c = 294.9$	$k_i = 23.3, k_v = 0.78$ $k_i = 46.5, k_v = 1.55$ $k_i = 93.1, k_v = 3.08$	$k_c = 90.1$	$k_i = 23.3, k_v = 0.72$ $k_i = 46.5, k_v = 1.45$ $k_i = 93.1, k_v = 2.95$

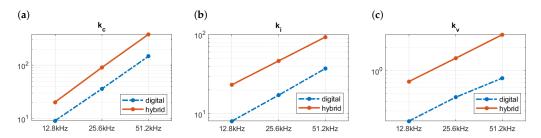


Figure 13. Dependence of (a) the gain k_c for PID single-loop controllers, (b) k_i and (c) k_v of PI+P double-loop structures as functions of carrier frequency f_s in both digital and hybrid implementation.

The difference between the THD values obtained using various values of k_c as functions of the gain margin ΔA at different carrier frequencies f_s are displayed in Figure 14. These characteristics can be used to determine the effect of controller detuning.

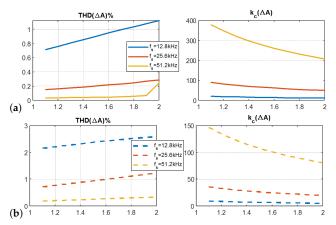


Figure 14. Dependence of THD and the gain k_c from the gain margin ΔA for different values carrier frequency f_s : (a) 1Lh PID and (b) 1Ld PID.

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4. Simulation Results

4.1. Results for the Basic Carrier Frequency $f_s = 25.6 \text{ kHz}$

A comparison of the distortion functions, characterizing the deviation of the actual output from its first harmonic, obtained in various control structures with different types of controllers, is presented in Figure 15. It is to be noted that the 2L structures perform better than the 1L ones, and that the results of (PI+P)/(PID) are better than of (P+P)/(PD). Therefore, in the article we will focus on the 1L (PID) and 2L (PI+P) control structures. Nevertheless, the full set of results in terms of THD for (P+P)/(PD) structures is collected in Table 4.

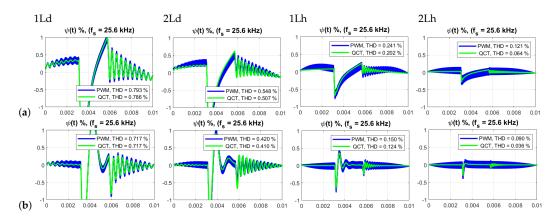


Figure 15. Distortion function $\psi(t)$ and THD values of $v_{out}(t)$ in 1L (PD)/(PID) and 2L (P+P)/(PI+P) control systems optimized for rectifier RC load working at carrier frequencies $f_s = 25.6$ kHz; (a) (PD)/(P+P) controller and (b) (PID)/(PI+P) controller.

Blurring is seen in the original PWM output around the QCT one, making the difference between their THD values shown in Figure 16, where the THD jump in f_s is due to the residual presence of f_s in v_{out} .

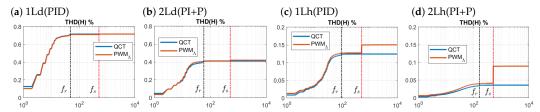


Figure 16. THD $_H$ of $v_{out}(t)$ as a function of H in optimally tuned control systems with $\psi(t)$ displayed in Figure 15b. In (**a**–**d**) control structures are arranged as in Figure 15b. Here f_r denotes the closed-loop resonant frequency in the no-load mode. According to Table 6, it equals approximately to 2.4 kHz for (**a**), 2.9 kHz for (**b**), 4.8 kHz for (**c**) and 8.0 kHz for (**d**).

The dependence of THD_H on H has such property that to find controller parameters minimizing THD, it is enough to use the THD of the QCT system, taking H so that the frequencies up to the closed-loop resonant frequency f_r are covered. To find the final THD value for a PWM-controlled system, the number H of harmonics taken for the calculation should be great enough to cover the carrier frequency f_s .

Another interesting feature is the shape of the duty cycle d(ih), which in digital systems is produced by a digital controller, and in hybrid systems can be considered as samples of a d(t) signal produced by an analog controller. Exemplary plots of both d(t) and $\chi(t)$ along with d(ih) and $\chi(ih)$ are displayed in Figure 17.

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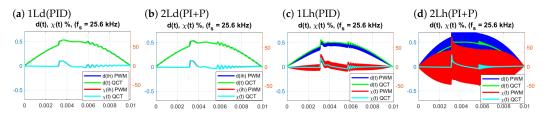


Figure 17. Functions d(t) and $\chi(t)$ and their sampled values in optimally tuned control systems ordered in (**a**–**d**) as in Figure 16 at f_s = 25.6 kHz under a rectifier load. Note that the blurry shape of the continuous-time controller output d(t) is due to large intersample deviations from the values at sampling instants d(ih) depicted in light blue.

4.2. Results for Carrier Frequencies $f_s = 12.8, 25.6$ and 51.2 kHz

The comparison of PD/P+P with (PID)/(PI+P) controllers shown in Figure 15 reveals the superiority of controllers with integral action. Therefore only plots for better structures (PID)/(PI+P) are presented in Figures 18 and 19 and further considerations.

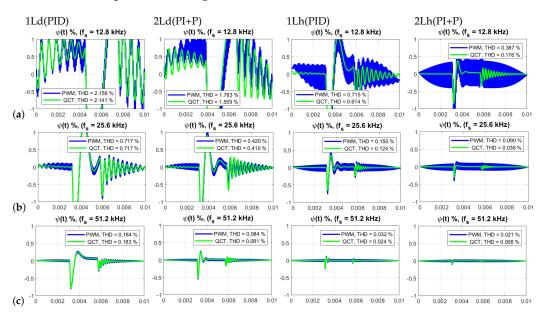


Figure 18. Distortion function $\psi(t)$ and THD values of $v_{out}(t)$ in 1L (PID) and 2L (PI+P) control systems optimized for the RC rectifier load operating at different carrier frequencies f_s : (a) 12.8 kHz, (b) 25.6 kHz, and (c) 51.2 kHz. All plots are displayed in the same scale. Plots in (a) extending beyond the display window are also displayed in the appropriate scale in Figure 25.

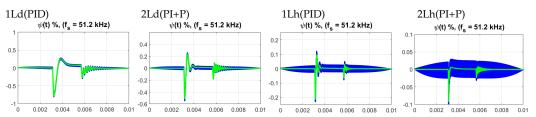


Figure 19. Zoomed-in views of the distortion function $\psi(t)$ showed in Figure 18c for various structures at carrier frequency $f_s = 51.2$ kHz.

The THD values in the right part of Table 4 are shown graphically in Figure 20. Notice that THD of 1Ld (PID) at 25.6 kHz approximately equals to that of 1Lh (PID) at 12.8 kHz. The same applies to 2Ld (PI+P) at 25.6 kHz and 2Lh (PI+P) at 12.8 kHz. These dependencies also apply to the frequencies of 25.6 and 51.2 kHz in the sense that the use of a more complex structure 2L is equivalent to doubling the carrier frequency. This property is valid for both hybrid and digital implementations. Figure 20c suggests that this property is also valid for intermediate frequencies.

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Table 4. Summary of THD values for the PWM modulator and QCT method, written as PWM/QCT, for the RC rectifier load. The difference between both components is due mainly to the ripple caused by the carrier signal.

f_s (kHz)	1L (PD)	2L (P+P)	1L (PID)	2L (PI+P)
digital:				
12.8	2.011/1.980	1.753/1.469	2.156/2.141	1.782/1.559
25.6	0.793/0.786	0.548/0.506	0.717/0.717	0.419/0.409
51.2	0.239/0.238	0.150/0.144	0.184/0.183	0.083/0.081
hybrid:				
12.8	0.848/0.672	0.534/0.242	0.715/0.614	0.387/0.176
25.6	0.241/0.202	0.121/0.064	0.150/0.124	0.089/0.036
51.2	0.063/0.054	0.028/0.016	0.032/0.024	0.021/0.007

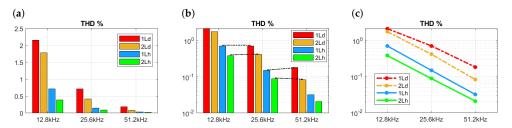


Figure 20. THD values for different structures with controllers PI/PID and carrier frequencies f_s . (a) THD in linear scale, (b,c) in logaritmic scale.

4.3. Abruptly Changing Resistive Load

As far as the effect of abruptly changed resistive load according to Figure 4b is concerned, Figure 21 shows that the 2Ld structure does not significantly improve the response. However, it shows the excellent control results for hybrid systems. Interestingly, the maximum value of $\psi(t)$ is similar as in a digital system, but it extinguishes much faster, so that the THD value is smaller.

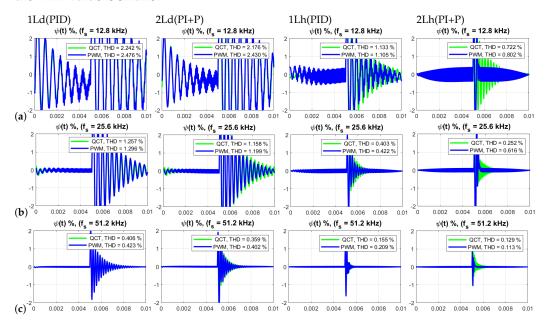


Figure 21. Distortion function $\psi(t)$ and THD values of $v_{out}(t)$ in 1L (PID) and 2L (PI+P) control systems optimized for the rectifier resistive load operating at different carrier frequencies f_s : (a) 12.8 kHz, (b) 25.6 kHz, and (c) 51.2 kHz. Plots in (a) extending beyond the display window are displayed in the appropriate scale in Figure 25.

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5. Other Features

In this section, comparisons of the considered structures will be made using various characteristics. Further interpretations and explanations can be gained from the root loci and frequency characteristics.

5.1. Closed-Loop Roots

The root loci of single-loop PID hybrid and digital control systems for different values f_s are shown in Figure 22, with the dominant roots:

$$s_{1,2} = -\sigma_0 \pm j\omega_0 = -\sigma_0(1 \pm j\theta); \quad \omega_0 = 2\pi f_0$$
 (36)

pointed by arrows. Two aspects are to be noted. First, that ω_0 thus f_0 increases as the sampling rate f_s increases. Second, that at the same f_s , f_0 of the hybrid system is greater than that of the digital system.

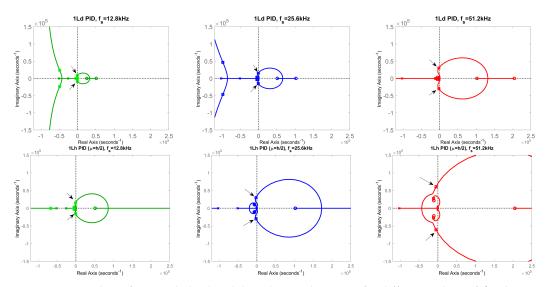


Figure 22. Root loci of 1L PID hybrid and digital control systems for different values of f_s . The **top** row is for 1Lh (PID), the (**bottom**) row for 1Ld (PID). From (**left**) to (**right**), f_s takes ascending values of 12.8 kHz, 25.6 kHz, and 51.2 kHz. The dominant roots are indicated by arrows.

In Figure 23a, comparison is made between double-loop and single-loop systems working at the same $f_s = 25.6$ kHz. Similarly, as previously, the f_0 of hybrid control systems is greater than of digital ones. However, when comparing f_0 of more advanced double-loop systems with simpler single-loop systems then the benefit in terms of higher frequency f_0 is much greater for a hybrid than for digital implementations. This is clearly seen in Table 5 where the dominant roots are listed for all structures (with integral action) and for all frequencies considered. These roots are also shown in Figure 24 illustrating the relationships between them graphically.

The absolute value of the real part of the root, σ , called the degree of stability, determines the rate of extinction of transients envelope. The higher σ is, the faster the transients approach zero. Specifically, the time t_r to shrink the envelope r times equals to:

$$t_r = -\frac{1}{\sigma} \ln r. \tag{37}$$

 θ , called the degree of oscillability, relates two consecutive amplitudes, A_{i+1} and A_i , by the relation:

$$\frac{A_{i+1}}{A_i} = e^{-\frac{2\pi}{\theta}} \tag{38}$$

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and determines the number of oscillations during transients. High θ values mean visually more dense filling of the space between transients asymptote by oscillations to be observed on plots of $\psi(t)$ presented in Figures 15, 18 and 21. These parameters are collected in Table 5 and it is clearly seen in the previous plots of $\psi(t)$ that they improve as f_s increases.

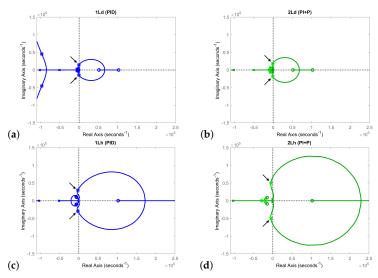


Figure 23. Comparison of (a) 2Lh (PI+P) with (b) 1Lh (PID) and (c) 2Ld (PI+P) with (d) 1Ld (PID) at $f_s = 25.6$ kHz. Comparison of hybrid and digital systems is also possible. The dominant roots are shown by arrows.

Table 5. Dominant roots in various control structures and their parameters: f_0 , σ , and θ .

f_s (kHz)	1Ld (PID)	2Ld (PI+P)	1Lh (PID)	2Lh (PI+P)
		f_0 (Hz)		
12.8	1470	1714	2447	3929
25.6	2396	2930	4770	8018
51.2	4798	5938	9650	16,533
	-	$-\sigma_0(1\pm j\theta_0)$ and $-\sigma_0$	1	
12.8	$-\sigma_1 = -270$	$-\sigma_1 = -343$	n.a	n.a.
	$-397(1 \pm j23)$	$-547(1 \pm j20)$	$-691(1 \pm j22)$	$-2373(1 \pm 10)$
25.6	$-752(1 \pm j20)$	$-1065(1 \pm j17)$	$-1958(1 \pm j15)$	$-4626(1 \pm j11)$
51.2	$-1678(1 \pm j18)$	$-2963(1 \pm j12)$	$-4903(1 \pm j12)$	$-8578 (1 \pm 12)$

Dominant real roots are the distinguishing feature of the 1Ld (PID) and 2Ld (PI+P) systems. As a result, the transients in the no-load mode consist of two components: Damped oscillations and a decaying exponent. This can be seen in Figure 25 as a certain asymmetry of the oscillations. Another unpleasant property is the relatively high THD value, not much better than in the uncontrolled system, and the phase lag with respect to the reference signal, see Figure 26a. The reason of the latter is the relatively small gain value that makes it necessary to increase the reference amplitude to obtain the required output voltage $v_{out}(t)$ at 50 Hz. Increasing the gain does not influence the phase. As a result, the system does not follow the phase but is able to maintain the required amplitude. Figure 26 shows that there are no such problems at higher carrier frequencies. Summarizing, it can be concluded that 12.8 kHz is too low a frequency for the digitally controlled VSI under consideration.

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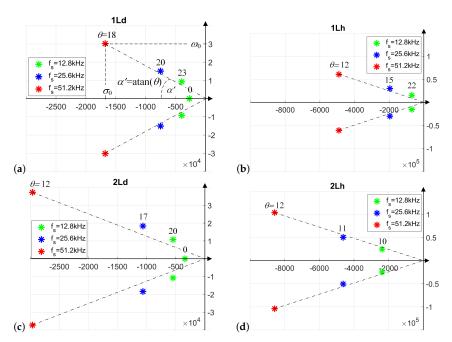


Figure 24. Dominant roots of the closed-loop systems. (**a**,**b**) single-loop systems, (**c**,(**d**) double-loop systems. Axis scales other than in Figures 22 and 23 are used to show the details. Therefore, angle $\alpha = \arctan \theta$ differs from α' . In particular, $\alpha = 86.8^{\circ}$, $A_{i+1}/A_i = 0.70$ for $\theta = 18$, and $\alpha = 85.2^{\circ}$, $A_{i+1}/A_i = 0.59$ for $\theta = 12$.

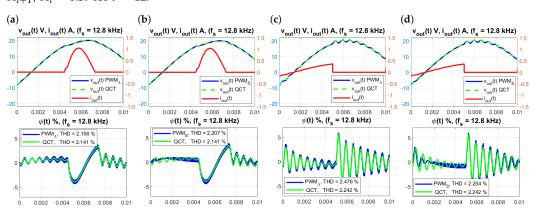


Figure 25. Plots of $v_{out}(t)$, $i_{out}(t)$ and $\psi(t)$ for the rectifier RC load and abruptly changing resistive load in a 1Ld(PID) system at 12.8 kHz using PWM_Λ and PWM_S modulation. (a) PWM_Λ, rectifier load; (b) PWM_S, rectifier load; (c) PWM_Λ, resistive load; (d) PWM_S, resistive load. The presence of slow real root in the no-load mode demonstrates visually in the last part of the $\psi(t)$ plots in (a,b), and in the first part of the $\psi(t)$ plots for a system with a resistive load in (c,d), as asymmetry with respect to the time axis. Observe that there is greater agreement between PWM_Λ and QCT than between PWM_S and QCT.

5.2. Closed-Loop Frequency Plots

Further interpretations can be obtained from the frequency plots of the complementary sensitivity function $T(\omega)$ and the sensitivity function $S(\omega)$ defined generally as:

$$S(\omega) = \left| \frac{1}{1 + C(s)K(s)} \right|_{s = j\omega}, \quad T(\omega) = \left| \frac{C(s)K(s)}{1 + C(s)K(s)} \right|_{s = j\omega}. \tag{39}$$

Figure 26 shows a comparison of these functions for single-loop hybrid and digital systems at all considered carrier frequencies f_s . The values of resonant frequencies f_r , collected in Table 6, are equal to the values f_0 which determine the pair of dominant roots defined in Equation (29) of the closed-loop system. It is to be noted that a single-loop hybrid

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system with $\mu \simeq 0$ is equivalent to a double-loop hybrid system. In Figure 27, 1L and 2L structures are compared separately at all considered carrier frequencies. It is interesting to note that, analogously to THD, an upgrade from 1Ld (PID) to 1Lh (PID) is equivalent to doubling f_s in 1Ld (PID). This is, however, not the case for 2L structures where an upgrade from 2Ld (PID) to 2Lh (PID) leads to a value of f_r greater than the one obtained when doubling f_s in 2Ld (PID).

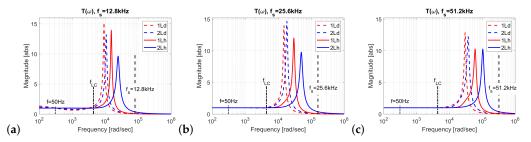


Figure 26. Frequency plots $T(\omega)$ of the closed-loop systems. Collections of $T(\omega)$ for all structures considered at various carrier frequencies f_s : (a) 12.8 kHz; (b) 25.6 kHz; (c) 51.2 kHz. The values of f_r are summarized in Table 6. $f_{LC} \approx 700$ Hz.

Table 6. Resonant f	requencies	f_r for	different structure	s and	l carrier i	frequencies	f_s .

f_s (kHz)	1Ld (PID)	2Ld (PI+P)	1Lh (PID)	2Lh (PI+P)
		f_r (Hz)		
12.8	1479	1714	2446	3930
25.6	2395	2927	4770	8013
51.2	4795	5922	9650	16,520

5.3. Impact of PWM Type on Control

PWM $_{\Lambda}$ was selected as the standard PWM type in this article due to the property shown in Figure 9 that the samples of actual $i_c(t)$ coincide with $i_c(t)$ resulting from the QCT model. It is interesting to check the impact of the PWM type on control structures considered in the article. The results are shown in Figure 28. As expected, PWM $_{\Lambda}$ -controlled systems are closer to the QCT model. Surprisingly, except for the 2Lh structure, the PWM type is irrelevant to the control performance.

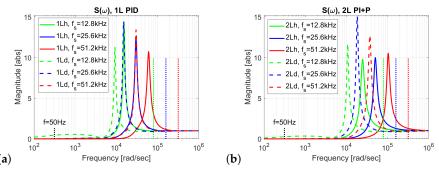


Figure 27. $S(\omega)$ and comparison of (a) 1L structures and (b) 2L structures at various f_s .

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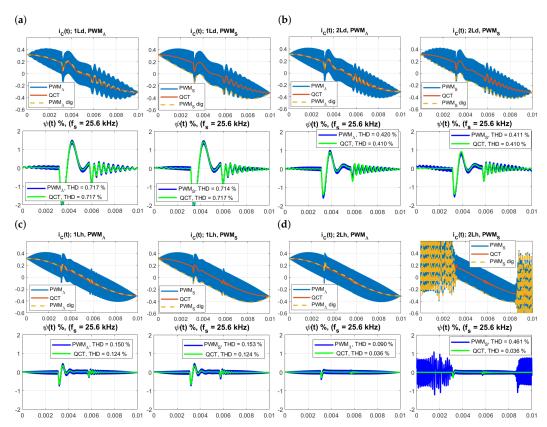


Figure 28. Influence of PWM type on control. The capacitor current $i_c(t)$ and distortion function $\psi(t)$ are displayed for the structures (**a**) 1Ld (PID), (**b**) 2Ld (PI+P), (**c**) 1Lh (PID), and (**d**) 2Lh (PI+P) operating at 25.6 kHz.

6. Comparison with Other Control Methods

The aim of the section is to compare our solutions with Resonant (R), Proportional-Resonant (PR), and Integral-Resonant (IR) single-loop control structures known in the literature and mentioned in the introduction. Open Loop (OL), Proportional (P), and Proportional-Integral (PI) controllers are also a part of this comparison. They are briefly discussed for digital implementations only.

Observe that for the closed-loop system to reproduce the reference magnitude V_m at $f_{out} = 50$ Hz, the V_m value of the reference should be replaced by $V_m/T(\omega_{out})$, where $T(\omega)$ is defined in (39). If desired, the phase can also be adjusted by applying an appropriate phase shifter to the reference signal.

6.1. Open-Loop System with a Rectifier Load

The Open-Loop system and its QCT representation is presented in Figure 7. Note that the signal $M\sin(\omega t)$ can be generated by a resonant controller with appropriately chosen initial conditions. To maintain amplitude A_1 of the fundamental harmonic of $V_{out}(t)$ independent of V_{DC} , any change of V_{DC} can be easily compensated in OL by adjusting $M = V_m/V_{DC}$. Plots of $v_{out}(t)$, $i_{out}(t)$, $\psi(t)$, d(ih), and $\chi(ih)$ as well as the start-up process are presented in Figure 29.

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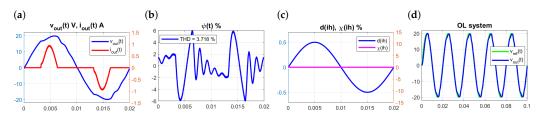


Figure 29. Control in the OL system with rectifier RC load. (a–c) steady-state variables, (d) start-up process. Note that the start-up process is very fast

6.2. Proportional controller P

The continuous-time transfer function $C_P(s)$ and the discrete-time one are:

$$C_P(s) = k_v, \quad H_P(z) = k_v.$$
 (40)

The optimal value $k_p = 0.6$ of the gain was found as the one minimizing THD in Figure 30d using the QCT method. Then V_m has to be multiplied by (1+0.6)/0.6 = 2.67.

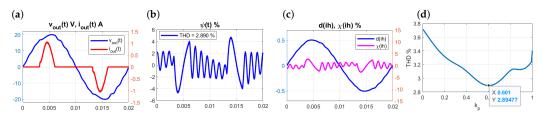


Figure 30. Control with the digital P controller. (**a–c**) steady-state variables, (**d**) THD as a function of k_p . Note a small improvement of THD from 3.72% in OL to 2.9% with the P controller

6.3. Resonant Controller R

The continuous-time transfer function $C_R(s)$ of the R controller and its digital implementation $H_R(z)$ are:

$$C_R(s) = k_r \frac{s}{s^2 + \omega^2}, \quad H_R(z) = k_r \frac{3.9057 \cdot 10^{-5} - 3.9057 \cdot 10^{-5} z^{-1}}{1 - 1.9997 z^{-1} + 0.9998 z^{-2}}$$
 (41)

where $\omega_{out} = 2\pi f_{out}$. Since for any k_r , $T(\omega_{out}) = 1$ at $f_{out} = 50$ Hz then V_m has not been modified when using the R controller.

In Figure 31 the characteristics $T(\omega)$, $S(\omega)$ and root locus are shown for the unloaded system along with roots at relevant values of k_r . Note that the imaginary part of these roots is almost the same and equals to $j\omega_{LC}$ at which $T(\omega)$ and $S(\omega)$ have resonant maxima. Therefore the transients in the no-load mode in Figure 32 have the same frequency f_{LC} .

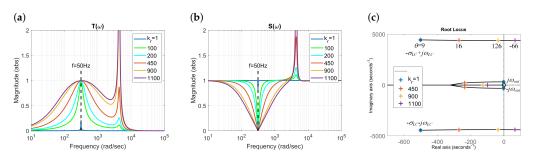


Figure 31. (a) $T(\omega)$, (b) $S(\omega)$ and (c) root locus of R controlled system in the no-load mode for various k_r . As k_r increases, oscillability at f_{LC} increases. The system loses stability at $k_r = 964.4$.

The steady-state of regulation process is presented in Figure 32, and the start-up process in Figure 33. From $T(\omega)$ displayed in Figure 31, it follows that in the unloaded system the sinusoidal reference can be recreated exactly regardless of k_r . On the other hand, the shape of $S(\omega)$, far from 0 except for $\omega = \omega_{out}$, with a resonant peak at ω_{LC} increasing

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with increasing k_r , explains why the THD in the system with rectifier load increases with k_r increasing.

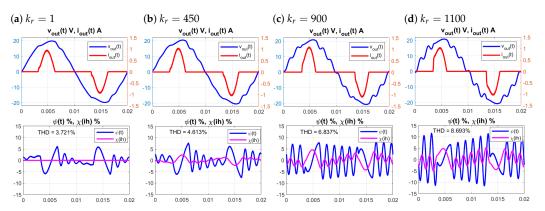


Figure 32. R control at different values of gain k_r . (a) $\Delta A = 964.4$, $k_r = 1$, no-load mode is stable; (b) $\Delta A = 2.143$, $k_r = 450$, no-load mode is stable; (c) $\Delta A = 1.072$, $k_r = 900$, no-load mode is stable but close the stability boarder; (d) $\Delta A = 0.877 < 1$, $k_r = 1100$, no-load mode is unstable.

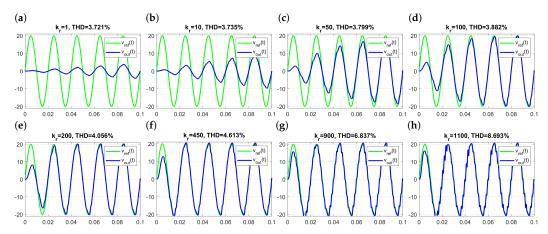


Figure 33. Start-up process of the R control. (**a**–**h**) Transients of $v_{out}(t)$ ordered for increasing values of k_r . Note that it is slower than in OL in Figure 29, even for large k_r .

Figures 32 and 33 show that there is a trade-off between the THD value and the start-up time, where shortening the time results in an increase in THD. As a result, the minimum THD value approaches the OL value as $k_r \to 0$. Whenever $k_r > 0$, sooner or later the system automatically adjusts to V_{DC} . In the limit situation, $k_r = 0$, the system becomes OL with the control signal generated by the R-controller with a zero input and appropriately selected initial values. Then, to conform to actual V_{DC} , it needs to be sensed and compensated as described in the previous subsection.

6.4. PI, PR, and IR Controllers

Let us consider three controllers:

$$C_{PI}(s) = k_p + \frac{k_i}{s}, \quad C_{PR}(s) = k_p + k_r \frac{s}{s^2 + \omega^2}, \quad C_{IR}(s) = \frac{k_i}{s} + k_r \frac{s}{s^2 + \omega^2}.$$
 (42)

Their digital implementations can be built on the basis of earlier solutions. The performance surfaces as a function of their parameters are shown in Figure 34.

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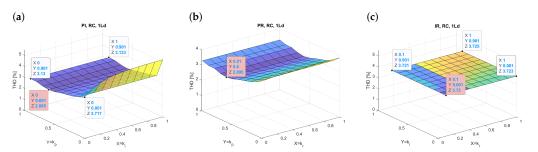


Figure 34. Surfaces of THD values as functions of controller gains for (a) PI, (b) PR, and (c) IR control systems. Parameters leading to the lowest THD values are marked with a reddish background.

It can be interpreted that the optimal PI controller approaches P with THD = 2.90, so the optimal PR controller and IR controller also approach the R controller with THD = 3.72.

6.5. Summary

THD for the controls considered here is at best as good as that of the P controller when PR or PI is used, and for R and IR at best as in an open loop system. To avoid long start-up process, it is necessary to increase the R-controller gain, leading to a performance worse than in the open-loop system. As a result, it is hard to find a good reason to choose the former, especially if V_{DC} =const or no compensation for its variations is necessary.

Summing up, this group of controllers is dramatically inferior to the PID-type structures analyzed in the article.

7. Conclusions

Two architectures were compared, single loop and double loop for VSI voltage control systems using hybrid (analog/digital) and purely digital controller implementations. In the double-loop variant, the the output voltage is controlled by an external loop equipped with a P or PI controller. The inner loop with a P regulator is based on the filter capacitor current. The effects of discrete-time data processing and PWM signal modulation were analyzed using the Quasi-Continuous-Time (QCT) approach that approximates such systems with continuous-time models. It has been shown that the type of PWM modulation is important for applicability of the QCT method in a double-loop control architecture where symmetric double-slope modulation is required.

While building the model and tuning the controller, the variable dynamic structure of the inverter with rectifier load was taken into account. This was achieved by using a physical VSI model in the form of electrical circuit for all calculations.

Control loop delay due to PWM signal and the digital control delay are the main factors that reduce the control performance. Delays can be reduced by increasing the sampling rate and replacing digital control algorithms with analog.

Another factor diminishing the performance is the implementation of signal differentiation either by differences in digital solutions, or filtered differentiation in analog ones. This factor is counteracted in the double-loop structure by sensing the capacitor current that is exactly proportional to the derivative of the output signal. Its disadvantage is the need for an additional sensor. When comparing more advanced double loop systems with simpler single-loop systems, the benefits in terms of THD reduction are much greater for hybrid than digital implementations. Another interesting property is that compared to a 1L structure, the use of a more complex 2L structure is equivalent to doubling the carrier frequency in the 1L structure. This property is valid for both hybrid and digital implementations.

In order to obtain good control performance, a high sampling rate should be used, and the digital control should be replaced with hybrid in which the analog controller core is integrated with a microprocessor that generates a sinusoidal reference and produces a PWM signal. An additional benefit is then the release of the processor from the control algorithm calculations, leaving more time for inverter monitoring, e.g., for detecting overloads and short circuits [33,34], and other system interface functions or for increasing the sampling

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frequency. Moreover, this can be done almost at no cost as an extension of the signal conditioning circuit. Unfortunately, upgrading to a 2L design requires investment in an additional sensor.

As an alternative to the analog controller, a high-speed digital regulator consisting of A/D converters, hardware-accelerated programmable compensators and digital modulators implemented using FPGA or as a dedicated CMOS integrated circuit capable of operating in the range of hundreds kHz [35–37], can be used.

Our solutions control both the first harmonic amplitude and the distortion caused by the rectifier and abruptly changing resistive load. They are superior in all respects over P, R, PR, IR and PI controllers.

As the carrier frequency increases, the THD of the controlled VSI with rectifier load can theoretically approach the lower bound obtained for the system without load. Our model does not take into account switching delays, ESR of the capacitor, sensor dynamics and noises becoming important in such extreme situations. This creates space for experimentation and, if necessary, for further model development. The article provides guidelines and methodology how to do that.

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