



Article Influence of Negative Sequence Injection Strategies on Faulted Phase Selector Performance

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Abstract: Renewable power is expected to increase drastically in the coming years due to the energy transition. A large part of the newly installed generators will be connected to the power system through inverters and electronic converters, whose behaviour differs from the generators connected synchronously to the network. One of the main differences is the current contribution during symmetrical and asymmetrical faults which can affect protection systems. New grid codes establish requirements for fast current injection, but the converter's maximum current limitations during faults make it difficult to establish control strategies for such current contribution. This paper studies the performance of faulted phase selector algorithm of a commercial relay under the current contribution from renewables before unbalanced faults. Two positive and negative sequence current injection strategies in compliance with new Spanish grid code requirements are proposed and tested under fault conditions in HiL (Hardware in the Loop). Test results show that the selected injection strategy affects the fault phase identification algorithm. Furthermore, the negative sequence injection requirements established in the new grid code improve the relay performance when line-to-line faults are applied, but they are not enough to identify all fault types.

Keywords: negative sequence injection; decoupled control structure; injection strategies; asymmetrical faults; fault phase selector algorithm; HiL; photovoltaic generator and type-IV wind turbine

1. Introduction

In recent years, the renewable generation penetration has been increasing to reduce the dependence on fossil fuels and carbon emissions, displacing conventional synchronous generation. Many of these renewable generators (mainly photovoltaic solar and wind power) are connected to the electric grid through power electronics converters whose behaviour differs from the conventional generator in normal operation and during disturbances.

In the case of faults in the electric power system, synchronous generators may inject fault current around 3–6 p.u. of nominal rating [1]. By contrast, the fault current typically provided by converters is limited to a range of 1.1–1.3 p.u. [2,3]. In addition to such lower fault current contribution [4,5], the fault characteristic differs from synchronous generators. Fault characteristic of renewable generators based on power electronics depends on the converter size, control strategy and the fault-ride-through requirements established by the network grid code [5–7]. The difference in the fault characteristic affects the protection system behaviour [8], endangering system stability and affecting the fault detection and protection selectivity [9,10].

During faults in the electric power system, renewable generators based on power electronics must help overcome the voltage dip by supplying reactive current as established in the network grid codes [11–13]. In some grid codes, the reactive current requirements only



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Copyright: © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). consider positive sequence current injection under balanced (symmetrical) and unbalanced (asymmetrical) faults [14]. Thus, in asymmetrical faults, the renewable generators provide a balanced reactive current that increases the voltage in all phases producing overvoltage in the healthy phases, affecting the safety of the system [1,15]. Moreover, the lack of negative sequence current also affects the behaviour of protection, causing maloperation [16–18].

New grid codes establish positive and negative sequence injection requirements to address these drawbacks. The European directive [19] allows network operators in coordination with transmission system operators (TSO) to define the requirements of fast fault current injection before asymmetrical faults. For example, the Spanish transmission system operator (Red Eléctrica de España, REE) specifies in the 12.2 Operating Procedure [20] the requirements for negative sequence current injection, the same as the German [21,22] or Greek [23]. Generally, all these grid codes state that the increment of reactive current in the positive and negative sequences should be proportional to the depth of the voltage dip.

Several studies have been performed regarding negative sequence contributions from converted-based renewable generators and their impact on the performance of protection relays [24–31]. Negative sequence current injection is analysed in [24]. This paper proposes and analyses several injection strategies from a voltage support point of view but does not analyse the influence of these strategies on the behaviour of protection relay algorithms. Most of the indicated studies ([25–28]) only consider simulation in different software, and some of them are only focused on directional algorithm misoperation ([28–31]) and do not consider phase fault identification (FID).

From these studies, it is worth highlighting [25,27], which investigate the influence of inverter-based resource control on several protection functions of the relay by comparing negative sequence impedance. The protection functions analysed are Negative-Sequence Overcurrent (50Q), Negative-Sequence Time Overcurrent (51Q), Negative-Sequence Directional Overcurrent (67Q) and Phase Fault Identification (FID). They show the malfunction of 51Q and 67Q through simulation examples run in transient electromagnetic programs (EMTP). These studies consider coupled and decoupled sequence control strategies but do not explain the positive and negative sequence injection priorities used to obtain these results. Although [27] also explains that FID may be influenced by the negative sequence current phase angle, the study only focuses on the negative and zero-sequence current angle relationship. However, it does not analyse the angular relationship between negative and the pure positive sequence current usually used in protection relays. Furthermore, it does not explain the negative sequence injection priorities used to carry out the study. In [25], Hardware in the Loop (HiL) is used, but only 67Q behaviour is tested before a single line-to-ground fault, and the main results are obtained by simulation.

In [26], the need to inject a negative sequence current is pointed out along with the influence of the control of the generators on distance protection installed in transmission systems. In this document, directionality is also considered. The authors of [31] report on the impact of negative-sequence current injection from solar plants connected to the transmission grid in the operation of negative sequence directional elements of the protective relays by recorded data, but only single-line-to-ground and line-to-line faults are analysed. Nevertheless, in [26,31], the control of the power converters is unknown, and no conclusions about the influence of such control can be obtained.

The objective of the study is to check whether compliance with these new grid code requirements will help the protection systems select the faulted phase correctly, or it will be necessary to develop new algorithms to achieve it. For that aim, this paper proposes two strategies to establish the priority of positive and negative sequence injection under unbalanced faults considering the maximum current limit of the power converter. These strategies, which fulfil the new Spanish grid code requirements, are implemented in a decoupled converter control. Once the strategies are implemented, their effect on FID algorithms typically used in commercial protection relays installed in the distribution grid has been analysed and tested in a lab environment. These FID algorithms are based on the angular relation between negative and pure positive sequence current.

A HiL configuration with a Real-Time Digital Simulator (RTDS) is used to carry out this task. In HiL, the RTDS reproduces events that can appear in an electric power system modelled in RSCAD software, generating currents and voltages seen by the relay during a fault and recording the relay behaviour. In this study, two strategies compliant with the Spanish grid code are proposed and implemented in the decoupled sequence control used by Photovoltaic generator (PV) and Type-IV wind turbines RSCAD models.

To describe the developed work, the paper is structured as follows. Section 2 describes the faulted phase selection algorithm implemented in the commercial relay to test during this study. Section 3 summarises the grid code requirements regarding low voltage ride-through and fast fault current injection. These requirements are based on synchronous generator behaviour during unbalanced faults; therefore, this section shows an example of such behaviour. Section 4 proposes fast fault current injection strategies to fulfil grid code requirements. After that, Section 5 describes the general control block diagram implemented in the power electronic interfaces model used by PV and Type-IV wind turbines to fulfil grid code requirements. The laboratory test bench used to evaluate the new strategies in relay behaviour is described in Section 6. Finally, the main results and conclusions are included in Sections 7 and 8.

2. Faulted Phase Selection Algorithm

The main function of faulted phase selectors is to identify the type of fault (singleline-to-ground, line-to-line-fault, line-to-line-to-ground fault or three-phase fault) and the phases involved. Once this identification is performed, the results are provided to the different protection algorithms whose operation depends on this information, for example, the release of impedance loops of a distance function or directional elements.

The faulted phase selection logic of the overcurrent commercial protection relay has been tested in this study. The operation principle of this logic, which is based on sequencecurrent theory, is illustrated in Figure 1. This figure shows the angular relationship between the negative sequence current (*I*2) and the pure positive sequence current (*I*1_*f*) used by the commercial relay to identify the faulted loop in case of asymmetrical faults. Figure 1a) is used to identify single-phase- or two-phase-to-ground faults, and Figure 1b) to identify two-phase faults. The faulted phase logic identifies the faulted phases by determining which sector the angle relationship falls. For example, the fault is classified as AB if the phase angle between *I*2 and *I*1_*f* is 60° in a line-to-line fault. By contrast, if the phase angle falls at 180° , the faulted phase is identified as BC.



Figure 1. Angular relationship between the negative current (*I2*) and the pure positive sequence current (*I1_f*): (a) Single-phase- and two-phase-to-ground fault diagram; (b) two-phase fault diagram [32].

The right operation of the faulted phase selection logic described in Figure 1 depends on the angular relationship between *I*2 and *I*1_*f*, which is based on the fault current contribution behaviour of synchronous generators. In this study, this logic is tested when fault current comes from renewable generators connected to the electric grid by means of power electronic devices and considering different injection strategies.

3. European Grid Code Requirements Review: Spanish Example

All generators must meet the requirements established by the TSO of the network to which they are connected. These requirements depend on the maximum capacity and/or the voltage at the connection point. In [33], power-generating modules are classified into Type A, Type B, Type C and Type D according to different capacity and voltage ranges. Table 1 shows this classification.

Power-Generating Module	Voltage at the Connection Point (V_{PCC})	Maximum Capacity (P _{max})
Туре А	V _{PCC} < 110 kV	$0.8 \text{ kW} \le P_{\text{max}} \le 100 \text{ kW}$
Туре В	V _{PCC} < 110 kV	$100 \text{ kW} < P_{\text{max}} \le 5 \text{ MW}$
Type C	V _{PCC} < 110 kV	$5 \text{ MW} < P_{\text{max}} \le 50 \text{ MW}$
Type D	$V_{PCC} \ge 110 \text{ kV}$	$P_{max} > 50 MW$

Table 1. Power-generating modules classification in the Spanish grid Code [33].

This paper is focused on distributed generation connected in a medium voltage distribution network. Hence, Type D generating modules, which consider voltages above 110 kV, are dismissed. Furthermore, Type A generating modules are also dismissed because they do not provide a low voltage-ride through control. Therefore, only requirements for Type B and Type C are considered in the following sections.

When there is a fault in the electric grid, the PV and Type-IV wind turbine generation must remain connected to the grid for a minimum time to ensure that power system operation is secure and reliable. This operational behaviour is known as Low-Voltage Ride-Through (LVRT) [34]. Figure 2 shows the LVRT profile according to the Spanish grid code that must be satisfied by Type B and Type C generation modules. It shows the relationship between the voltage dip depth (ΔU in p.u.) measured at the connection point and the minimum time that the electrical power module shall remain connected to the grid.



Figure 2. Low-voltage ride-through profile applied to type B and type C electric power-generating module [20].

Additionally, before a voltage dip, the reactive current injection must increase depending on the voltage depth. The new grid code published in July 2020 [33] specifies positive and negative sequence current injection requirements for Type B and Type C generators when an asymmetrical fault is applied. The reactive current injection must comply with the profiles shown in the Figure 3.



Figure 3. (a) Injection/consumption of additional positive reactive current ($\Delta I_{1,r}$) depending on positive voltage drop; (b) total reactive current Injection/Consumption limitation (I_r); (c) injection/consumption of additional negative reactive current ($\Delta I_{2,r}$) depending on negative voltage drop [20].

Where ΔU_1 represents the voltage drop/rise in the positive sequence, ΔU_2 represents the voltage drop/rise in the negative sequence, and K_1 and K_2 are proportional constants in the positive and negative sequences. These constant values must be adjustable in the range of 2–6, although the default value is 3.5 p.u., unless specifically indicated by the DSO in coordination with the TSO. In the following sections, these proportional constants will be called *K*-factor.

From Figure 3, it is concluded that under unbalanced faults, the control of the powergenerating module injects positive and negative sequence reactive current proportional to the voltage deep by a factor *K*. Furthermore, the figure shows the maximum current provided by the control of the power converter.

The Figure 4 shows the contribution of a synchronous generator to a two-phase AB fault. During the fault, for a 30% depth voltage dip (ΔU_1 and ΔU_2), the generator supplies reactive currents in positive ($I_{1,r}$) and negative ($I_{2,r}$) sequences proportional to the voltage dip ($K_1 = 2.8$ and $K_2 = 2$), as indicated in the grid code. This synchronous generator injects a phase current that reaches 2.2 p.u. in phase A and 1.8 p.u. in phase B. This behaviour is not possible in a power electronics-based generator due to the current limitations imposed by the converter. Therefore, it is necessary to analyse how this type of generator can supply positive and negative sequence currents during unbalanced faults without exceeding the power converter limits. With this objective, the next section proposes different active and reactive current injection strategies under unbalanced faults. After that, the general control diagram implemented in the power electronic interfaces model used by PV and Type-IV wind turbines is described.



Figure 4. Contribution of a synchronous generator to a two-phase AB fault: (**a**) positive (U_1) and negative (U_2) sequence voltage; (**b**) phase current injection; (**c**) active ($I_{1,a}$) and reactive ($I_{1,r}$) current in positive sequence; (**d**) active ($I_{2,a}$) and reactive ($I_{2,r}$) current in negative sequence.

4. Current Fault Injection before Unsymmetrical Faults by Power Electronic-Based Generator

The traditional protection schemes are designed based on the current contribution of conventional synchronous generators under fault conditions, but the behaviour of power electronic-based generators differs during faults. As explained in Section 3, conventional synchronous generators can provide currents several times greater than nominal current during unbalanced faults. In contrast, fault current in power electronic-based generators is limited to avoid exceeding their designed current ratings. Therefore, it is not always possible to provide the reactive current in positive and negative sequence and the active current in the positive sequence from the direct application of Figure 3 without exceeding the limits of the power converter, and it is necessary to establish a system of injection priorities so as not to exceed the current ratings of the converters.

From an analysis of grid code requirements described in Section 3 [33], two injection strategies (strategy A and B) have been proposed. These strategies establish the priority of positive and negative sequence current injection before unbalanced faults, considering the limits of the power converter and applying the same *K*-factor for positive (K_1) and negative sequence (K_2) based on synchronous generator behaviour. Furthermore, the strategies do not consider the negative sequence active current injection during a voltage dip to resemble the performance of the negative sequence network of a synchronous generator, where the impedance is predominantly reactive.

Figure 5 shows the flowchart from strategy A. If an unbalanced fault is applied, this strategy prioritizes the active and reactive current injection in the positive sequence over the negative sequence reactive current injection. In the first step, active and reactive current in the positive sequence is calculated based on Figure 3a and compared with the maximum phase current limit ($I_{max,phase}$). If such limit is not reached, the negative sequence reactive

current is calculated according to Figure 3c, considering the same *K*-factor ($K_1 = K_2$). Still, if the value obtained exceeds the maximum phase current limit, K_2 is reduced until the limit is fulfilled. Moreover, it must be considered that this strategy does not define a minimum negative sequence reactive current.



Figure 5. Strategy A flowchart to fulfil fast fault current injection requirement.

In strategy B, described in Figure 6, the power converter control prioritises the positive and negative sequence reactive current injection over the positive sequence active current injection during unbalanced faults. The same *K*-factors between 2 and 6 are initially selected to calculate the reactive current injection ($K_1 = K_2$) in the first step. Still, they will be reduced at the same proportion during the fault to fulfil the maximum reactive current injection limitation (I_{1_LimU}), which is lower than the maximum phase current limit ($I_{max,phase}$). The active current injection in the positive sequence is calculated considering the margin between the maximum phase current limit and the maximum reactive current injection.



Figure 6. Strategy B flowchart to fulfil fast fault current injection requirement.

5. Control Diagram of Power Electronic Interfaces Devices to Be Used during the Study

The strategies proposed in the previous section are modelled in the outer control loop of a PV and a Type-IV wind turbine power converter, whose structure is shown in Figure 7. The control technique used by the power converter is based on the decoupled control structure in a d-q reference framework that permits managing active and reactive power independently [35–37]. Figure 7 shows a schematic diagram of the implemented control.

As shown in Figure 7, under normal conditions, the active current loop controls the DC bus voltage of the power converter and the active power delivered by the generator. Moreover, the reactive current control manages the voltage at the converter connection point or fixes the reactive power provided to the grid.

During fault conditions, the reactive current control must provide voltage support based on the grid code requirements, considering the maximum current supplied by the converter to avoid undesirable damage to the equipment.



Figure 7. Schematic control diagram.

To carry out these tasks, as a first step, the three-phase voltages (V_{abc}) and current (I_{abc}) waveforms are measured at the point of common coupling (PCC). After that, these time-domain waveforms are decomposed in stationary frame α - β using the power variant Clarke transform [38,39]. After the decomposition, voltage and current values are split into a positive and negative sequence. From the decomposition and using Park transformation [39,40], d-q parameters in both sequences are obtained ($V_{dq(+)}$, $V_{dq(-)}$, $I_{dq(+)}$ and $I_{dq(-)}$). The control uses these d-q values in steady-state conditions and under fault conditions.

In normal conditions, currents and voltages in negative sequence are close to zero. Therefore, the negative sequence control loop is deactivated, and only the positive sequence control operates. By contrast, in unbalanced faults, according to grid code requirements described in Section 3, the power converter must supply positive and negative sequence current, and as a result, positive and negative control loops are activated.

Based on the control diagram described and considering the strategies previously detailed, the scheme shown in Figure 8 is introduced in the control diagram (Figure 7) during $I_{d,fault}$ and $I_{q,fault}$ calculation in positive and negative sequences under fault conditions:



Figure 8. Control methodology to inject positive and negative sequence current.

To analyse the influence of these strategies on faulted phase selection algorithm operation described in Section 4, several tests were carried out in the laboratory test bench described in the next section.

6. Laboratory Test Bench

The two injection strategies proposed are tested in the benchmark grid model shown in Figure 9. In this benchmark grid, the external grid is represented by a Thevenin equivalent, and it is connected to the distribution level by a 55/12 kV transformer. The MV side grounding of the transformer is set to consider a grounded and an ungrounded distribution grid topology in the analysis. An 11 MW renewable generator is connected at the point indicated as "RES" in the figures. During tests, two technologies are considered: PV and a Type-IV wind turbine. Overhead lines connect renewable technologies to the distribution grid, and the lightning symbol represents the fault inception point. The following fault types have been applied at this point:

- Single-line-to-ground faults (SLG): AG, BG and CG.
- Line-to-line faults (LL): AB, BC and CA.
- Line-to-line-to-ground faults (LLG): ABG, BCG and CAG.



Figure 9. Benchmark distribution grid used to analyse relay performance.

The figure shows the fault point, the current and voltage transformers' location and the fault current contribution from the renewable source, which in the figure is denoted as $I_{short-circuit}$. Breaker CB2 is closed during the simulations to analyse the faulted phase selector in the protection relay.

The analysis of commercial protection systems is performed by Hardware in the Loop (HiL) configuration with an RTDS simulator. Figure 10 shows the scheme used during the study.

RTDS is a real-time digital simulator that reproduces events that can appear in the electric grid, generates currents and voltages seen by the relay during a fault, and records the relay behaviour under this scenario.

The benchmark distribution grid is modelled in RSCAD software to perform the study and simulate it by the RTDS, which generates the current and voltage values to be measured by relay. These values are in the range ± 10 V, so the amplifier amplifies them to represent the actual secondary values measured by the relay. The amplifier has six current and voltage channels with a maximum output voltage and current values of $300 V_{rms}$ and $70 A_{rms}$. Once the current and voltage values are amplified, they are used to supply the voltage and current analogue inputs of the protection relay. The relay produces a trip signal when a fault current is detected and sends this signal to the RTDS to continue the simulation.



Figure 10. Hardware in the loop configuration.

7. Results and Discussion

Table 2 summarises the results obtained in the tests described in the previous section when the renewable source is a PV generator. These results show that both strategies improve faulted phase selection in LL faults compared to current controls, in which only a positive sequence current is supplied during unbalanced faults. Consequently, the faults are identified as three-phase faults [41]. Nevertheless, they still cannot correctly identify LLG faults.

Table 2. Study cases to evaluate negative current injection in current protection relays located in distribution grids ($K_1 = K_2$).

Fault Type	PV (Strategy A)		PV (Strategy B)	
	Ungrounded	Grounded	Ungrounded	Grounded
AG		\checkmark		×
BG	N.A.	\checkmark	N.A.	×
CG		\checkmark		×
AB	\checkmark	~	\checkmark	\checkmark
BC	\checkmark	~	\checkmark	\checkmark
CA	\checkmark	~	\checkmark	\checkmark
ABG	×	×	×	×
BCG	×	×	×	×
CAG	×	×	×	×

Regarding SLG faults, two situations can be distinguished in the table. In ungrounded systems, these faults cannot be detected using phase overcurrent protection. This fact is indicated in the table as NA (not applicable). In the grounded distribution system, the faulted phase selector results before SLG faults depend on the current injection strategy, identifying it correctly with Strategy A but not with Strategy B.

In LL faults, the phase selection is subjected to the initial *K-factor* with Strategy A; for example, it fails if *K-factor* is 3.5, but the faulted phase is correctly identified if this parameter is set to 2.5. However, Strategy B uses a calculated *K-factor* that considers the current limit of the converter for the other fault types, but the behaviour is still wrong for this fault type.

To perform a detailed analysis of the obtained results, the negative sequence current phasors (*I2*) with respect to the pure positive sequence current (*I1_f*) obtained during an AG fault are represented in the angular diagram that describes the operation of the faulted phase selection logic (Figure 11). From this figure, it is concluded that the PV control, for

both strategies, shifts the negative sequence phasor and makes that *I2* falls within the angle sector defined for ABG faults with strategy B (green colour). In strategy A (red colour), the phase shift is not big enough to make that *I2* falls in the ABG sector.



Figure 11. Graphical representation of the angle sectors used to identify single-line and double-line-to-ground faults and *I*2 phasor when strategy A and Strategy B with a *K*-factor = 2.5.

Furthermore, to observe the influence of the initial *K-factor* value in test results, the AB fault in the grounded system is analysed when the initial *K-factor* is set to 2.5 or 3.5. The results obtained from this study are gathered in Figures 12–15. These figures show the phase voltages (*VA*, *VB* and *VC*) and phase currents (*IA*, *IB* and *IC*) measured by the protection relay. These figures also show the digital signals included in the oscillography, which are indicated in Table 3. When one of these digital signals is activated, it is indicated by an orange rectangle whose length depends on the time that signal remains active. For example, in Figure 12, the fault detector (FD) is activated after a few milliseconds after t = 0 s and is deactivated a few milliseconds before t = 0.7 s indicating that the protection relay detects a fault during such time.



Figure 12. Faulted phase selector when AB fault is applied and *K*-factor = 2.5 using Strategy A.

In these conditions, when strategy A is used, faulted phase selector operation depends on such initial *K-factor*, as shown in Figures 12 and 13. If the *K-factor* is set to 2.5 (Figure 12), the relay can identify the AB fault correctly (digital signal AB is activated), but the faulted phase selection algorithm identifies a three-phase fault (3PH_F) when 3.5 is used (Figure 13). By contrast, Strategy B provides the same faulted phase identification for both initial *K-factor* values (AB fault). Therefore, Strategy A is more sensitive to the initial *K-factor* than Strategy B because faulted phase selector operation changes depending on such value.



Thus, strategy B is the most stable because the result does not depend on the initial settings, but *K*-*factor* during the fault is out of the range defined in the grid codes.

Figure 13. Faulted phase selector when AB fault is applied and *K*-factor = 3.5 using Strategy A.



Figure 14. Faulted phase selector when AB fault is applied and *K*-factor = 2.5 using Strategy B.

Moreover, the maximum current limit of the power converter can make the *K*-factor fall below the minimum indicated in the grid code when Strategy B is used. When an unbalanced fault is applied in this strategy, the reactive current increments in positive and negative sequences are calculated depending on the voltage dip depth and the initial *K*-factor value selected. Nevertheless, these reactive current values may exceed the imposed limit of 0.9 p.u. In these conditions, initial K_1 and K_2 factors are reduced in the same proportion to satisfy the required limit for reactive current values. This effect is shown in Figure 16 for a PV generator before an AB fault in the undergrounded system. In the example, the initial *K*-factor value is 2.5. When the fault is applied, the reactive current changes depending on the dip depth shown in Figure 16a, but the calculated current is above the maximum limit, activating the limitation flag (Figure 16c). This flag indicates that



the *K*-*factor* must be reduced to avoid converter damage and satisfy the reactive current injection requirement.

Figure 15. Faulted phase selector when AB fault is applied and *K*-factor = 3.5 using Strategy B.

Digital Signals	Definition
FD	Fault detection
GR	Ground fault
2PH_F	Two-phase fault
MULTIPH_F	Several phases are involved during the fault
AG	Phase A to ground fault
BG	Phase B to ground fault
CG	Phase C to ground fault
AB	Line-to-line fault between phases A and B
BC	Line-to-line fault between phases B and C
CA	Line-to-line fault between phases C and A
ABG	Line-to-line-to-ground fault between phases A and B
BCG	Line-to-line-to-ground fault between phases B and C
CAG	Line-to-line-to-ground fault between phases C and A
3PH_F	Three-phase fault

 Table 3. Digital signals definition.

This strategy has been tested in the benchmark grid described in Section 6, obtaining the results shown in Table 4, which summarises the *K-factor* average value obtained in the simulations. In all these simulations, the reactive current before the event is 0.129 p.u., the initial *K-factor* value is 2.5, and the fault has been produced at 50% of the line shown in Figure 9. In each case, the depth of the voltage dip depends on the fault type; for example, in the AB fault simulated in the grounded configuration, the voltage dip depth is 0.42 p.u., and for the AG fault, 0.2 p.u. The highest values of the *K-factor* are obtained for SLG faults, but they are still below the 2.0 limit defined in the grid code requirements, even for low values of voltage dip depth. This scenario is not considered in the network code, but it is in the technical standards for the compliance of generators (NTS-generators) [42] that are used to verify the compliance of the generation unit. It specifies that if the factors are less than indicated, it will be considered valid if the positive and negative sequence components are limited to the same proportion or the positive sequence component will be higher than 40% of the apparent nominal current. In this case, the first condition is met. It should be noted that a different *K-factor* is obtained when fault current comes from PV or Type-IV



wind turbine. It is due to the different impedances between the renewable resources and the fault locations in both generator models.

Figure 16. *K-factor* when AB fault is applied and PV plant is connected: (**a**) voltage dip depth in positive (*Fault deep U1*) and negative (*Fault deep U2*) sequences; (**b**) reactive current in each phase (*Ir_Phase A, Ir_Phase B* and *Ir_Phase C*) calculated without consider the maximum current limit of the power converter; (**c**) limitation flag; (**d**) *K-factor* values; (**e**) reactive current in positive (*I1r*) and negative (*I2r*) sequences; (**f**) Active current in positive sequence; (**g**) phase current; (**h**) reactive current injected in each phase considering the maximum reactive current injection limitation (*Max_Ir*).

Table 4. K-factor values when strategy B is applied for negative current injection.

Fault Type	PV		Type-IV Wi	Type-IV Wind Turbine	
	Ungrounded	Grounded	Ungrounded	Grounded	
AG		1.460		1.937	
BG	N.A.	1.350	N.A.	1.966	
CG		1.420		1.947	
AB	0.920	1.055	1.299	1.361	
BC	0.885	1.038	1.307	1.361	
CA	0.975	1.111	1.306	1.375	
ABG	0.934	1.034	1.315	1.333	
BCG	0.884	1.000	1.314	1.346	
CAG	0.983	1.073	1.308	1.355	

8. Conclusions

This paper analyses the performance of the faulted phase selection algorithm of a commercial overcurrent relay before short circuit current contribution according to new

Spanish grid code requirements, which indicate that in case of unbalanced faults, negative sequence current input must be performed.

Two different injection strategies that prioritise positive and negative sequence current injection have been defined, considering the grid code requirements and the limitations imposed by power converters from renewable generators. To check the influence of new current injection requirements in commercial relays, these strategies were implemented and tested by HiL. In these tests, a real-time digital simulator reproduces events in a benchmark distribution grid, generates currents and voltages measured by the commercial relay during a fault, and records the relay behaviour under these scenarios.

As expected, test results show that the new grid code requirements improve the behaviour of the faulted phase selection compared to those obtained with a current contribution without negative sequence. However, such behaviour depends on the current injection strategy and is still insufficient to identify faulted phase in every unbalanced fault type correctly. In fact, line-to-line-to-ground faults are not correctly identified in any test performed. Consequently, new fault phase selection algorithms are still needed to guarantee the correct faulted phase identification and ensure the reliability of the protection installed and, thus, the stability of the electric power system.

Another conclusion is related to the effect of the current injection defined in the grid code. On the one hand, the fact that different results with the two strategies described in this paper are obtained indicates the need for a further definition of the requirements noted in the grid codes, such as, for example, a minimum level of negative sequence current during the unbalanced fault. On the other hand, comparing the proposed injection strategies, phase selection with Strategy B, which prioritises the positive and negative current injection, is less sensitive to the initial *K-factor* value selected because the faulted phase selection algorithm provides the same faulted phase when *K-factor* is varied. Furthermore, it is seen that the *K-factor* obtained by the algorithms depends on the injection strategy, the fault type, the voltage dip depth and the maximum current injection from the power converter during faults. The tests show that the initial *K-factor* is reduced during unbalanced faults, even outside the defined range.

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Nomenclature

FID	Phase fault identification
LVRT	Low-voltage ride-through
PV	Photovoltaic generator
I2	Negative sequence current
I1_f	Pure positive sequence current
U_1	Voltage in positive sequence

U_2	Voltage in negative sequence
ΔU_1	Voltage drop/rise in the positive sequence
ΔU_2	Voltage /drop/rise drop in the negative sequence
K_1	Proportional constant in positive sequence
<i>K</i> ₂	Proportional constant in positive negative sequence
<i>I</i> _{1,<i>r</i>}	Reactive current in positive sequence
<i>I</i> _{1,<i>a</i>}	Active current in positive sequence
<i>I</i> _{2,<i>r</i>}	Reactive current in negative sequence
$I_{2,a}$	Active current in negative sequence
I _{max,phase}	Maximum phase current limit
I _{1_LimU}	Maximum reactive current injection limitation
$\Delta I_{1,r}$	Reactive current variation in positive sequence
$\Delta I_{2,r}$	Reactive current variation in negative sequence
PCC	Point of common coupling
$V_{dq(+)}$	Positive sequence voltage in the dq-frame
$V_{dq(-)}$	Negative sequence voltage in the dq-frame
$I_{dq(+)}$	Positive sequence current in the dq-frame
$I_{dq(-)}$	Negative sequence current in the dq-frame
Idfault (+)	Positive sequence current under fault conditions in d-axis
Idfault (—)	Negative sequence current under fault conditions in d-axis
Iqfault (+)	Positive sequence current under fault conditions in q-axis
Iqfault (—)	Negative sequence current under fault conditions in q-axis

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