



Review

Islanding Detection in Rural Distribution Systems

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Abstract: This paper summarizes the literature on detection of islanding resulting from distributed generating capabilities in a power distribution system, with emphasis on the rural distribution systems. It is important to understand the legacy technology and equipment in the rural distribution electrical environment due to the growth of power electronics and the potential for adding the new generations of intelligent sensors. The survey identified four areas needing further research: 1. Robustness in the presence of distribution grid disturbances; 2. the future role of artificial intelligence in the islanding application; 3. more realistic standard tests for the emerging electrical environment; 4. smarter sensors. In addition, this paper presents a synchro-phasor-based islanding detection approach based on a wireless sensor network developed by the University of Texas at Austin. Initial test results in a control hardware-in-the-loop (CHIL) simulation environment suggest the effectiveness of the developed method.

Keywords: distribution system; islanding; sensors; testing

1. Introduction

Like the preponderance of distribution systems, rural networks have historically been radial networks. A key difference is the distance between meters, and the likelihood that a large farm or ranch will have a larger electrical load than the load of a traditional residential consumer. The increasingly critical role played by electricity coupled with the large geographic vulnerability of the distribution system requires system operators to have better situational awareness and better control on their systems. Additionally, the decreasing cost of distributed energy resources (DERs) provides more incentive for the consumers to install DERs and provide electric power locally.

Earlier research has suggested that the operation state of equipment on a distribution feeder can be predicted based on the system model and data captured from the substation and smart meters [1]. While this approach is appealing, it is slow due to meter sampling intervals. The reduction in size, weight, and cost of processors and memory make it possible for the smart line-mounted sensors and advanced applications to improve the reliability, power quality, and system observability of general distribution network with large penetration DERs. This survey was conducted to support the development of such sensor systems, particularly those with the ability to identify unintentional islanding.

If a DER supplies electricity to a portion of the grid that is isolated from the utility grid, the isolated grid is islanded [2]. Islanding can cause adverse impacts on utilities and customers. Consequently, standards have been developed to specify the time by which an island situation must be cleared. For unintentional islanding, IEEE Std 1547–2018 requires that the DER should detect the island, cease to energize the area electric power system, and trip within 2 seconds of the formation of an island [3]. Thus, islanding detection methods are required for distribution system with DERs.

Energies **2020**, 13, 5503 2 of 12

While there are challenges in the system due to islanding, intentional islanding can be designed into the system for economic or resiliency reasons. A local grid, which can operate in grid-connected mode or island mode as well as incorporating the technology for a smooth mode transition, can be defined as a microgrid. The DER inverter system [4] in the microgrids usually works in current source control mode to provide power to the grid under normal operations. After being islanded, the inverter system must switch to the voltage control mode to provide voltage and/or frequency support to the local load. In addition to the mode transitioning from current source control to grid-forming control, the protection settings or other control settings such as frequency droop coefficient for DERs may also need to be adjusted right after the islanding happens. To effectively achieve the adaptive settings, accurate islanding detection function is required.

There are several adverse impacts associated with unintentional islanding [5]. First, unintentional islanding brings power quality issues to the customers. The abnormal voltage magnitude and power frequency due to the power mismatch may cause inconvenience to customers and may even damage their equipment. In addition, an islanded grid can also cause other adverse impacts while restoring the normal power delivery service to neighboring customers. Since DERs keep supplying power to the islanded grid, lines remaining energized can put utility crews and public safety in great danger if islanding is not detected. This will be more likely to happen in a distribution system with high penetration of DERs. Furthermore, DERs and other power equipment in the island could be damaged if the island is reconnected to the main grid without proper synchronization. Therefore, DERs are required to be disconnected during unintentional islanding [6] for human safety and distribution system reliability.

The motivation for this survey is to provide information that can support sensors used by system operators to identify unintentional islanding. The information is also technically relevant in microgrids and in more dense distribution systems.

This paper reviews the impacts of islanding and methods to detect islanding. The other contributions of this paper are listed below:

- 1) The test approach for islanding detection algorithms is also summarized.
- 2) This survey provides a summary of shortcomings and advantages of existing islanding detection methods.
- 3) This survey suggests some potential research directions including robustness in the presence of distribution grid disturbances, the future role of artificial intelligence in the islanding application, more realistic standard tests for the emerging electrical environment, and smarter sensors.
- 4) Based on the findings in the survey, the team has implemented a synchro-phasor based islanding detection approach based on a wireless sensor network developed by the University of Texas at Austin. This paper presents the new islanding detection approach and preliminary testing results in CHIL simulation environment.

2. Existing Islanding Detection Methods

Generally, islanding detection methods are classified as passive methods [7–14], active methods [15–28], and remote (communication-based) methods [29–31]. The classification is illustrated in Figure 1. The remote method uses power line signaling and transfers trip decision to DERs. [29].

Energies **2020**, 13, 5503 3 of 12

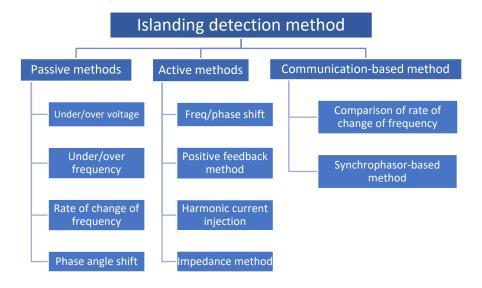


Figure 1. Islanding detection solutions classification.

2.1. Passive Methods

A conventional quality metric for islanding detection algorithms is the size of the non-detection zone (NDZ). Simply put, the NDZ is the portion of a distribution circuit in which islanding would not be detected if it occurred. In [7], an analytical approach was derived to visualize the NDZ for various passive islanding detection methods including under/over voltage, under/over frequency, and phase change. The test circuit, as shown in Figure 2, is the standard testing circuit in IEEE 1547.1-2005 [8], IEEE 929–2000 [6], and UL1741. The RLC load is used to perform theoretical analysis to estimate the NDZ of various methods.

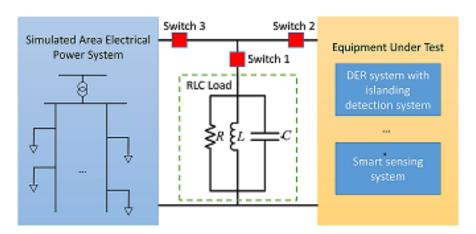


Figure 2. Unintentional islanding test configuration.

Passive methods use local voltage/frequency information to detect islanding. Any passive method [7,9] will fail the unintentional islanding test if the distribution generation (DG) power and load demand are 100% matched. This is because the islanding event would not cause obvious voltage and frequency variations, so the passive method would not be able to detect the event. Moreover, the island [7,9] can be undetectable with up to 30% power mismatch. In addition to the level issue, there is also a temporal behavior. When the power mismatch is larger than about 30%, the frequency or voltage may take some time to deviate to the threshold and the time may be larger than the required value, i.e., 2 seconds as specified in IEEE 1547 [3]. Thus, the NDZ of the passive methods may be larger than the derived NDZ, as presented in [7].

Signal processing techniques are used to increase the utility of passive methods, for example, wavelet analysis technique. The wavelet transform and *S*-transform are used to extract features of the

Energies 2020, 13, 5503 4 of 12

negative sequence voltage component [10]. The voltage signals are obtained from a hybrid system with photovoltaic (PV), wind, and fuel cell. In [11], the wavelet transform is used to extract detail coefficients at different levels of decomposition. Then, the detail coefficients are used to build a singular value matrix and calculate wavelet singular entropy for each phase. The simulation study shows this approach can detect islanding within 10 milliseconds.

Pattern recognition has also been used to detect islanding [12]. A random forest classification method is developed using features such as voltage, frequency, and total harmonic distortion. Evaluation studies based on the IEEE 34 bus system show that the provided method has zero NDZ. Lin et al. [14] report a wide-area measurement-based detection method for the bulk power system. It uses data captured by the frequency disturbance recorder and then examines frequency difference and change of angle difference. Simulation studies suggest that the method is immune to generation trips, load shedding, and system oscillations.

2.2. Active Methods

The active methods inject a disturbance into the system and use the responses to the disturbance to detect islanding. Lopes and Sun [15] assess the performance of an active frequency drifting islanding detection method. It uses a load parameter space that determined by the quality factor and resonant frequency of the RLC load. The authors claim that an NDZ is inevitable if considering all the quality factors. Karimi et al. [16] provide an islanding detection method that can prevent false trips for a single DER, which is connected to the main grid by a three-phase voltage sourced converter (VSC). A negative sequence current is injected through the VSC controller. Then, the corresponding negative sequence voltage at the point of common coupling (PCC) is measured. In reference [17], a dq frame technique is exploited as an active islanding detection method. Disturbance injected by the d-axis controller adjusts the voltage amplitude at PCC, while the q-axis controller injects a disturbance to alter the frequency at PCC. Simulation results show that injecting disturbance through the q-axis controller is a viable solution for islanding detection. Both simulations in [16] and [17] are implemented in a PSCAD/EMTDC environment.

Kim et al. [18] detect islanding by injecting a ninth harmonic component into the current in the grid and analyzing the ninth harmonic component of the voltage at PCC. The Goertzel algorithm is used to reduce the computation burden. This approach uses the estimated grid impedance to detect unintentional islanding. The authors report that this method does not have any NDZ. Another injection alternative [19] is to detect islanding by injecting high-frequency voltage signals. The measurement of the grid high-frequency impedance is obtained and used to identify islanding in a few milliseconds.

An automatic phase shift method [20] detects islanding for grid-connected PV inverters. At the kth zero-crossing of the terminal voltage, while the frequency remains at normal line frequency, the output current at the inverter has its starting angle changed based on the frequency of previous voltage cycle. The developers assert that this method alleviated the non-detection problem. Chen and Li [21] analyze the connection of power mismatch and frequency deviation related to islanding. An indication of islanding is determined based on the large voltage deviation caused by active power mismatch.

Another impedance-measurement-based approach [22] is proposed for the PV inverter by injecting an inter-harmonic current and measuring the voltage response. The proposed approach was digitally implemented on the digital signal processor (DSP) of the PV inverter. The utility grid impedance was estimated using the existing sensors and DSP of the PV system. The accuracy of the method was tested in experimentally yielding less than 15% uncertainty for impedance estimation and less than 5% total harmonic distortion.

An impedance variation approach using changes in active power and reactive power is proposed in [23,24]. The power quality perturbations were analyzed to capture the transient voltage and current signals to calculate the grid impedance. Unintentional islanding results in a detectable impedance change.

Energies **2020**, *13*, 5503 5 of 12

Reference [25] presents an attempt to improve islanding detection by using intermittent bilateral reactive power variation. The method adjusts the reactive power reference for the DER. Once unintentional islanding happens, the system frequency deviates from the nominal value and reaches the threshold in a short period of time. The approach is expected to eliminate the NDZ.

For most of these active methods, the NDZ could be effectively removed for single DER cases, but it may not be true for cases involving multiple-DER. Since the injected harmonics or the generated perturbations may be canceled due to multiple sources, the active method may not eliminate NDZ effectively for multiple-DER systems as suggested in [9].

A positive feedback approach for islanding detection [9,26,27] was developed to address the challenge of multiple sources on the same distribution feeder. Positive feed control was achieved using a dq formulation. The control coefficients were also carefully selected based on an actual distributed generation system and grid properties to ensure that the applied positive feedback signal did not cause instability in grid-connected conditions. Experimental tests were used to verify the effectiveness of the proposed approach.

In general, active methods have smaller NDZ than passive approaches, but they perturb the system [28]. However, passive methods sometimes are not able to detect islanding when source and load are balanced [9].

2.3. Communication-Based Methods

The remote methods use communication between substation and DERs to detect the islanding. Such methods could eliminate NDZ and are suitable for a system with multiple DERs, but these methods require additional communication capability to enable a reliable islanding detection. One implementation [30] uses a phasor measurement unit (PMU) and intranet to implement the remote islanding detection method. The voltage angle difference between the utility grid and the DG site is calculated based on the local measured phase angle as well as the received grid phase angle. The angle difference threshold was selected as 120 degrees, which can achieve accurate islanding detection under conditions as small as 1% power mismatch. In this method, global positioning system (GPS), wide area network (WAN) communication over TCP/IP, and PMU are needed to accomplish the unintentional islanding detection.

Another approach [31] is to compare rate-of-change of frequency (COROCOF). The local measured rate-of-change of frequency (ROCOF) signal was compared with a reference signal from utility grid (i.e., a substation). If the local signal deviates from the reference signal and the difference exceeds a pre-specified threshold, an unintentional islanding event is detected. The threshold is based on the measurement accuracy and communication channel accuracy. Audio frequency signaling, radio signaling, or power line communication methods could be adopted to broadcast an event-triggered rate-of-change of frequency reference signal to the whole network. When a high ROCOF was detected locally, but the local device did not receive a reference signal, an unintentional islanding was detected.

2.4. Hybrid Methods

In addition, hybrid methods [5,28,32–34] to detect islanding have been proposed. The hybrid methods combine active and passive methods. One such approach is to use voltage unbalance and total harmonic distortion of current [5]. The large or small variation in the loading of DER after islanding is considered.

An alternative method obtains phase criteria from the phase locked loop of the PV inverters [33]. The method predicts the ranges of loads that may possibly result in islanding undetected. Another approach is to combine the average rate of voltage change and real power shift to detect islanding with different load conditions for systems with multiple DERs [28]. The average rate of voltage change is used to initiate the real power shift.

Another hybrid method [32] combines total harmonic distortion with the principle of positive feeder technique. It discriminates between islanding and load switching. In [34], the approach of using wavelet analysis to obtain time localization of the islanding condition was assessed. In this

Energies **2020**, 13, 5503 6 of 12

approach, high-frequency components are injected by PV inverters, and then local measurements of controller voltage and current are obtained. In [35], a hybrid method is proposed to combine remote islanding detection signal from substation and local rate of change of voltage phase (ROCOVP) to improve the robustness of the islanding detection. The proposed method could eliminate NDZ and also be able to detect islanding in the presence of multiple DERs.

2.5. Comparison of the Methods

Various methods for islanding detection were reviewed in this Section. A comparison of the islanding detection methods is presented in Table 1.

Methods	Pros	Cons
Passive	Incorporate the passive method with	Cannot detect islanding if
	signal process techniques and pattern	source and load are balanced;
	recognition enables fast islanding	cannot detect islanding with
	detection and immunization to system	30+% power mismatch.
	parameter changes	
Active	Usually have smaller NDZ than passive	Need to inject a disturbance,
	methods	which alter the system
Communication-	Can eliminate NDZ; suitable for a system	Requires additional
based	with multiple DERs	communication capability
Hybrid	Flexible to employ various methods	Complexity of the methods may
	together	add burden to computation

Table 1. A comparison between the islanding detection methods.

3. Islanding Detection Testing Approach

Given the fact that various approaches have been proposed to measure all relevant electrical parameters to identify islanding, it is important to test these approaches to applicability in legacy systems. Test approaches range from offline simulation, to control hardware-in-the-loop (HIL) simulation, and to full-scale experiments. In most of the literature, off-line simulation was used. This is likely due to the combination of the fact that such simulation gives significant information and the tools for such simulations are widely available. Simulations are typically augmented by reduced scale experiments. IEEE 1547.1–2005 [8], IEEE 929–2000 [6], and UL1741 provide standard reduced scale test procedures.

Since an RLC load bank is used in the standard test circuit, the standard test scenario may not be able to emulate other load conditions, such as motor loads and power-electronic-interfaced loads. In addition, the test circuit may not be able to cover the systems with multiple DER units. Thus, the islanding detection performance in these cases may still need additional analysis to ensure that the developed islanding detection approaches could handle more complicated scenarios.

4. Future Research Directions

Even though various islanding detection methods have been developed in the past two decades and test techniques have been standardized, the changing electrical environment and technology advances have raised some research opportunities.

First, more efforts are needed to assure the island detection does not yield a false positive, needlessly interrupting service, due to disturbances in the grid. This is particularly important in a distribution grid, which can be electrically noisier than a transmission grid. If a grid disturbance occurs, the local DER should be able to ride-through the transient to maintain reliability.

Second, the benefits of widespread edge computing for islanding detection need to be assessed. For example, the potential benefits of artificial intelligence tools need to be determined. An advertised but unpublished AI-based islanding detection approach has been explored by ABB [36]. It uses the estimated system inertia or system strength to detect unintentional islanding events, has been

Energies **2020**, 13, 5503 7 of 12

developed. This approach needs more extensive computation to run simulation studies to help better select adaptive settings for the algorithm. This approach would be more suitable for a future grid with more intermittent renewable resources and fast changing circuit topologies.

Third, research is needed to support more robust standard tests in the emerging electrical environment with much more power electronics integrated into the legacy grid.

Finally, the persistent development of more capable and smaller data excision and processing equipment as well as communications equipment, are making the development and deployment of distributed intelligent sensors throughout a distribution system possible. An intelligent sensor-based islanding detection method could be developed by leveraging the sensor's powerful computational capability and GPS synchronization capability as well as the communication network. The deployed low-cost intelligent sensors monitor the voltage and current on the line with GPS synchronization. Once a disturbance is recorded, the sensor can broadcast the information on the network. Each sensor will compare the broadcast signals with its local measurement to detect unintentional islanding. In addition, advanced system inertia estimation algorithm could be locally implemented on each intelligent sensor to estimate the real system strength in operational real time. Additional development and test work are still required in this area.

5. Synchro-phasor Based Islanding Detection Method

The University of Texas at Austin team is developing a wireless sensor network for distribution system to monitor and detect abnormal conditions on the circuit [37]. Each line sensor has GPS synchronization and wireless communication capability. The line sensor is single-phase and is powered by the high voltage line directly. The sensor control function has been prototyped on National Instrument single-board RIO 9607 (NI sbRIO-9607) controller. The synchro-phasor measurement function is implemented on the FPGA of the NI sbRIO-9607 controller using Labview software. Standard P class and standard M class synchro-phasor measurement methods [38] are implemented on each sensor controller. P class method or M class method could be pre-configured based on requirement. In this work, P class was adopted to implement the islanding detection function. The synchro-phasor measurements include voltage, current, frequency, phase-angle, rate of change of frequency (ROCOF), and absolute timestamp.

In the sensor network, each intelligent sensor continuously monitors the local grid. Once a system disturbance (voltage, frequency, rate of change of frequency deviation above a threshold) is detected, the islanding detection function is activated. Each sensor will compare the local phasor measurement with its peers in terms of the phase angle difference. If the phase angle difference exceeds a pre-specified threshold and a high ROCOF is detected, an islanding event is detected. This method can further reduce the NDZ in islanding detection, as compared with local measurement-based approach.

The synchro-phasor data packet is buffered in direct memory access (DMA), first-in-first-out (FIFO) on FPGA. The microprocessor accesses the buffered data once certain number of data packets are available in the DMA FIFO. On the microprocessor, the standard TCP/UDP communication method is implemented to establish the data transmission between sensor controller and control center. The wireless LTE network is adopted in this sensor network to enable synchro-phasor data transmission. The measured round-trip time (RTT) of the wireless sensor network (LTE network) is less than 200 ms. In the preliminary sensor testing, the synchro-phasor report rate is selected as 10 reports per second to achieve a trade-off between the wireless LTE network bandwidth and the islanding detection accuracy.

Energies 2020, 13, 5503 8 of 12

6. Preliminary Control Hardware-in-the-Loop Simulation Test Result

To test the developed islanding detection methods, a relevant distribution system is constructed based on an operational distribution circuit from the project utility partner. The actual three-feeder circuit includes around 4000 single-phase nodes, 4600 elements, and 600 loads. It is relatively challenging to simulate the exactly-the-same system in real-time. To simulate the system in real-time and maintain the model fidelity, an aggregated system model for a single-feeder circuit model is developed. The simplified system includes 30 aggregated loads, 32 three-phase line segments, load switches, a capacitor bank, and one substation transformer. This model is implemented in the Matlab/Simulink simulation environment, as shown in Figure 3. The aggregated model accuracy is validated by the actual network model in OpenDSS software. The OpenDSS model is considered as an accurate representation of the feeder circuit. Various cases were compared including normal load flow and short-circuit fault current. In the worst case, the aggregated model has 1.5% error as compared with OpenDSS circuit model. The real-time model is implemented in Opal-RT simulator. The simulation time step is selected as 200 μ s. It is validated that this model can run in real-time on the Opal-RT simulator.

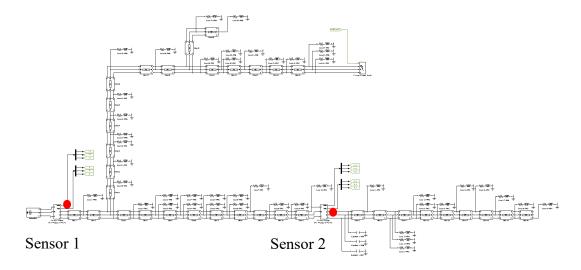


Figure 3. Diagram of the aggregated circuit model in Matlab/Simulink.

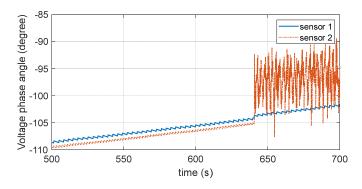
Two wireless sensors are integrated in the control-hardware-in-the-loop simulation environment. The GPS synchronization and wireless LTE communication functions are enabled on both sensor controllers. Both sensors are installed on phase A of the distributed feeder, as shown in Figure 3. Sensor 1 is installed at feeder head. Sensor 2 is installed right before capacitor bank. A switch is placed right before sensor 2. During the test, the initial status of the switch is close to emulate a grid-connected condition. In the simulation test, a signal is sent to the switch to open the line. The switch open creates an islanding event. The synchro-phasor report rate is configured as 10 reports per second. Each synchro-phasor report includes voltage phase angle, current phase angle, voltage magnitude, current magnitude, frequency, and rate of change of frequency. The synchro-phasor data from the sensor network are logged on a central computer for visualization and post analysis. When a sensor phase angle has a large mismatch with other sensors over 10 degree and a high ROCOF is detected, an islanding event is detected.

In the test, the system is initially operated in normal condition. Before the islanding happens, the voltage phase angles of sensor 1 and sensor 2 are very close, as shown in Figure 4a. The mismatch of phase angles is less than 2 degrees. In the distribution circuit, the physical distance of sensors is low. This guarantees that all sensors on the same phase have very close phase angle. Once islanding happens, the phase angle of the sensor in island area significantly deviates from the other sensors in grid-connected area. This provides a clear indicator for islanding event. The ROCOF also provides

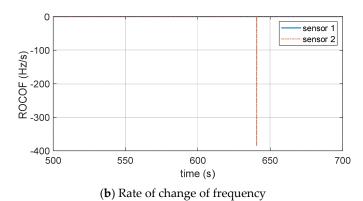
Energies **2020**, 13, 5503 9 of 12

an indicator at the time instant of islanding event, as shown in Figure 4b. The current phase angle also starts oscillating during islanding condition, as shown in Figure 4d.

The preliminary test result suggests that the synchronized phase angle difference can provide a clear indicator for islanding event. This approach can be extended to a circuit with multiple sensors on the same phase. It should be noted that the preliminary test system is a rural distribution network, which does not have a significant amount of DERs. The proposed method has a great potential to be used to detect islanding in a system with extremely high DER penetration. Additional test results for high DER penetration grid will be presented in a future paper.



(a) Voltage phase angle.



60.5 N 60 Sensor 1 sensor 2 sensor 2

(c) Measured frequency

Energies 2020, 13, 5503

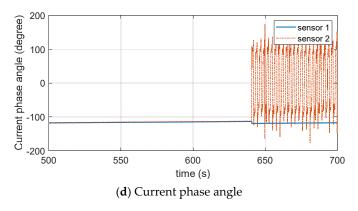


Figure 4. Islanding detection function test result.

7. Conclusion

This paper first surveyed existing islanding detection methods in literature. The major focus of the survey is to understand the key challenges in islanding detection for high DER penetration grid and to identify the major practical solutions. Based on the survey, four potential research directions are identified including improving algorithm robustness, developing AI-based approach, improving test approach for emerging electrical environment, and smarter sensors. The smarter sensor technology is adopted by the team to improve the islanding detection performance.

Synchro-phasor based islanding detection is implemented on the wireless sensor network prototyped at the University of Texas at Austin. Standard P class synchro-phasor measurement method is used in the islanding detection. The GPS and wireless communication functions are implemented for the sensor controller to ensure that the synchro-phasor could be reliably transmitted to the central controller. Preliminary CHIL simulation testing is conducted in Opal-RT real-time digital simulator to validate the performance of the developed algorithm.

Future work will focus on improving the algorithm robustness and validating the performance in field. In addition, additional CHIL tests will be performed to show the performance of the developed synchro-phasor based islanding detection method in high DER penetration grid.

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