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# **Evaluation of Phase Imbalance Compensation** for Mitigating DFIG-Series Capacitor Interaction

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Abstract: The phase imbalance compensation concept is proposed in literature as an alternative way to mitigate classical subsynchronous resonance (SSR) problems in series-compensated transmission lines. However, a fundamental analysis to determine this concept's ability to mitigate resonances between a doubly-fed induction generator (DFIG) and a series compensated transmission line, i.e., DFIG-SSR, is not reported in literature. Therefore, the objective of this paper is to investigate to which extent phase imbalance compensation is able to mitigate DFIG-SSR. For the phase imbalance compensation scheme, an analytical model that captures the relation between the level of series compensation, the degree of asymmetry between the compensated phases, and the resulting shift in resonance frequency is developed and validated using time domain simulations. Then, an optimisation framework is developed to search for an adequate level of compensation asymmetry, capable of mitigating the adverse interactions. The optimisation allows us to show that, even with the best set of parameters, phase imbalance compensation is not suitable for mitigating DFIG-SSR. The analytical model enables us to explain the underlying physical reasons for this and an attempt is made to explain why this concept is theoretically able to mitigate classical resonance issues. Lastly, directions for future research are identified.

Keywords: DFIG; FACTS; MIGRATE; series compensation; SSCI; SSR; stability and control

#### 1. Introduction

The sub-synchronous resonance (SSR) between a doubly fed induction generator (DFIG) and a series capacitor is a form of self-excitation and is defined as DFIG-SSR in Reference [1]. This phenomenon was observed in a practical power system for the first time in 2009, where the interactions between a DFIG-based wind farm and a series compensation transmission line in the power system of Texas resulted in a system disturbance [2]. It is worth noting, however, that the theoretical analysis presented in Reference [3] already illustrated the possibility of such adverse interactions in 2003. The DFIG's impedance can be used to explain the fundamentals of the DFIG-SSR phenomenon. This impedance is formed by the rotor side impedance  $Z_{RS}$  and the grid side impedance  $Z_{GS}$ . The impedances of the induction machine and the rotor side converter (RSC) determine the equivalent impedance  $Z_{RS}$ . On the other hand,  $Z_{GS}$ , is formed by the impedances of the filter and the grid side converter (GSC). Both impedances  $Z_{GS}$  and  $Z_{RS}$  are connected in parallel during normal operation [4,5], where the decoupling between the controls of the RSC and GSC is achieved by means of the dc-link. Since practical DFIGs generally have a large  $Z_{GSC}$ , the impedance of the DFIG,  $Z_{DFIG}$ , can then be approximated by  $Z_{RS}$  [5–7]. A negative slip, which occurs at sub-synchronous frequencies, will lead to a negative resistance of the DFIG, as viewed from the machine's stator terminals. When this negative resistance is greater than the resistance provided by the remaining part of the transmission

system, existing electromagnetic oscillations can be amplified and grow rapidly. This phenomenon is known as DFIG-SSR.

In Reference [1], the authors presented a critical review of the existing mitigation solutions for DFIG-SSR. In this review, the solutions were categorised in control solutions, hardware solutions, and solutions based on system level coordination. This paper builds further on that classification and aims to evaluate the effectiveness of the phase imbalance compensation (i.e., a potential hardware solution) in mitigating DFIG-SSR. Fixed series compensation is used to increase the transfer capability of a transmission line and can be achieved by using the classical series compensation concept or the phase imbalance compensation (PIC) concept. Compared to the classical series compensation concept, the phase imbalance concept implements an asymmetrical compensation throughout the three phases. This paper focuses on whether the latter can be used for mitigating DFIG-SSR.

DFIG-SSR can be mitigated by increasing the damping (e.g., by means of altered DFIG controller parameters), or by weakening the rotating magnetomotive force (MMF) produced by subsynchronous currents in the armature. The PIC concept was initially developed to mitigate classical SSR using the second method and was initially proposed in Reference [8] with a theoretical case study presented in Reference [9]. The use of the phase imbalance concept for mitigating classical SSR is reported in Reference [10] and for damping inter-area oscillations in Reference [11].

Although briefly touched upon in Reference [12], the ability of the phase imbalance concept in mitigating DFIG-SSR is not yet studied in detail. The goal of this paper is to thoroughly investigate to which extent phase imbalance compensation is able to mitigate DFIG-SSR by weakening the rotating MMF produced by sub-synchronous currents in the armature. For this, first an analytical model that captures the relation between the level of series compensation, the degree of asymmetry between the compensated phases, and the resulting shift in resonance frequency is developed and validated. Next, an optimisation framework is developed to identify the most suitable degrees of asymmetry for mitigating DFIG-SSR. Lastly, to compare the performance of different cases, a performance index is also developed.

The rest of the paper is structured as follows. Section 2 gives a brief overview of the existing hardware solutions for mitigation DFIG-SSR. Section 3 reviews the fundamentals of classical series compensation and lays the basis for understanding the phase imbalance compensation concept. The phase imbalance compensation is introduced in Section 4. The derivation and validation of the analytical model as well as the performance of the imbalance compensation concept are also discussed in Section 4. Lastly, conclusions and directions for future work are discussed in Section 5.

#### 2. Overview of Hardware Solutions for Mitigating DFIG-SSR

Solutions for mitigating DFIG-SSR can be based on (i) modifications in existing control algorithms of the DFIG (i.e., control solutions), (ii) installation of new hardware equipment or modification of existing hardware equipment (i.e., hardware solutions), and (iii) coordination between the power system state and existing or new controls (i.e., system level coordination) [1]. Control solutions are implemented in the power electronics interfaced device under consideration (in this case, the DFIG). Hardware solutions can be divided into flexible AC transmission systems (FACTS) and other methods based on voltage source converters (VSC). Solutions that fall in the category of system level coordination require some type of information from the power system, e.g., damping ratio or short circuit capacity level. Such information can be obtained from measurements or forecasts.

FACTS devices are used for a variety of reasons. An up-to-date overview of practical FACTS applications is given in Reference [13]. Compared to the fixed series capacitor, the thyristor controller series capacitor (TCSC) inherently prevents DFIG-SSR [14] and was used in Reference [15]. On the other hand, when the goal is to mitigate DFIG-SSR by means of any other FACTS device, it will have to be equipped with a supplementary damping controller (SDC). Several of such cases are reported in Reference [1]. Installing a FACTS device (except for TCSC) for mitigating DFIG-SSR is only cost-effective when additional power system support, such as voltage control, is required.

On the other hand, dedicated VSC-based solutions for mitigating DFIG-SSR are developed. A robust sub-synchronous damping controller is reported in Reference [16] and is planned to be commissioned in the Guyuan power system in China.

Lastly, when a battery energy storage system (BESS) includes an SDC, superior damping can be obtained. This is illustrated in Reference [17] for the mitigation of classical SSR. As was the case for the FACTS solutions, installing a BESS only for mitigating DFIG-SSR may not be the most optimum solution. On the other hand, when the BESS is also used to provide system services as reported in Reference [18], it could be a good alternative for more intrusive mitigation solutions.

Readers that are interested in the advantages and disadvantages of various solutions for mitigating DFIG-SSR are encouraged to read the comprehensive review given in Reference [1].

## 3. Classical Compensation Scheme

Fixed series compensation can be achieved by means of the classical compensation scheme or the phase imbalance compensation concept. In the classical compensation scheme, the transmission line with series inductance L is compensated symmetrically, i.e., the capacitance C of the series capacitor is the same across all three phases (see Figure 1). In contrast, the added capacitances  $C_A$ ,  $C_C$  and inductances  $L_A$ ,  $L_C$  in the phase imbalance concept are not identical to each other, as will be explained in Section 4. Furthermore, the inductances  $L_A$ ,  $L_C$  are added to the uncompensated line as part of the phase imbalance concept and are not the phase inductances of the uncompensated line.



Figure 1. Overview of classical and phase imbalance compensation schemes.

Due to the symmetrical compensation of the three phases, a uniform reduction in the phase reactance is achieved and, as a result, a balanced equivalent impedance across the three phases is obtained. This is shown in Figure 2. The shift in the impedance angle due to the compensation is also shown. In such cases, a disturbance in the grid will produce currents in the sub-synchronous frequency range. The resulting rotating MMF at this frequency will be symmetrical. As will be shown in Section 4, this is different for the phase imbalance compensation concept, where the unbalanced impedances create an asymmetrical rotating MMF at sub-synchronous frequencies.

Due to the symmetry, the resonance frequency  $\omega_r$  is the same for all three phases and is defined as given in Equation (1).

$$\omega_r = \sqrt{\frac{1}{LC}} \tag{1}$$

 $\omega_r$ : resonance frequency (*rad*/*s*)

*L*: transmission line inductance (*H*)

*C*: capacitance of series capacitor (*F*)

The compensation degree k, as defined by Equation (2), quantifies the ratio of the series compensation's capacitive reactance  $X_C$  and the line's inductive reactance  $X_L$ .

$$k = \frac{X_C}{X_L} \times 100\% \tag{2}$$

*k*: compensation degree

 $X_C$ : capacitive reactance

*X<sub>L</sub>*: inductive reactance

The resulting change in active power transfer as a function of *k* is defined in Equation (3).

$$\frac{dP}{dk} = \frac{1}{1-k} \tag{3}$$

*dP*: change in active power transfer

*dk*: change in *k* 

*k*: compensation degree

At the fundamental frequency  $\omega_0$ ,  $X_L = \omega_0 L$ , and  $X_C = \frac{1}{\omega_0 C}$ , Equation (2) can be rewritten as Equation (4).

$$k\omega_0^2 = \frac{1}{LC} \tag{4}$$

The relation between  $\omega_r$  and k can be derived by substituting Equation (4) in Equation (1), which results in Equation (5).

$$\omega_r = \sqrt{k\omega_0^2} \tag{5}$$

Figure 3 is derived using Formulas (2), (3) and (5) and graphically illustrates the relation between the compensation degree k, the resonance frequency  $f_r$ , and the active power transfer. In practical series compensation applications, k lies between 20% and 80% [19] (In exceptional cases, k can be lower (e.g., the classical series compensation application in Hebei, China [20], has a k-value of 6.67%), which leads to an increased active power transfer of 1.25–5 p.u. and results in resonance frequencies, as seen from a point of interconnection to the transmission network, between approximately 22 and 45 Hz (based on fundamental frequency  $f_0$  of 50 Hz). The actual resonance frequency will be different depending on the reactance of the connected generator. It should be remembered that k is always calculated at  $f_0$ .



**Figure 2.** Vector diagram of the phase impedances at fundamental frequency (**a**) without series compensation and (**b**) with series compensation. For both cases, the phase diagram of the impedance is shown as well (**c**). The vector diagrams of the classical series compensation and the phase imbalance compensation are identical at the fundamental frequency.



**Figure 3.** Influence of the compensation degree *k* on the resonance frequency  $f_r$  ( $\omega_0 = 2\pi f_0 = 2\pi 50$ ) and active power transfer *P*.

#### 4. Phase Imbalance Compensation

#### 4.1. Concept Description

The phase imbalance compensation (PIC) concept is shown in Figure 1. In this concept, a phase wide series compensation is implemented in such a way that the impedance at the fundamental frequency remains the same under both compensation schemes, i.e.,  $Z'(\omega_0) = Z''(\omega_0)$ , where the prime symbol denotes quantities in the classical compensation scheme and the double prime symbol denotes quantities in the phase imbalance concept. In order to achieve this, consider phase *A*. The relation between capacitor  $C_A$  and inductor  $L_A$  is derived as shown in Formula (6), where  $C_A$  and  $L_A$  are additions due to the PIC, *L* is the inductance of the uncompensated line, and *C* is the capacitance of the series capacitor under the classical compensation scheme.

$$Z_A''(\omega_0) = Z_A'(\omega_0)$$
  

$$\omega_0(L+L_A) - \frac{C+C_A}{\omega_0 CC_A} = \omega_0 L - \frac{1}{\omega_0 C}$$
  

$$\omega_0^2 L_A + \frac{1}{C} = \frac{C+C_A}{CC_A}$$
(6)

The same applies for phase *C* and solving Equation (6) for  $\omega_0$  ultimately leads to Equation (7), where *Q* represents the degree of asymmetry. This equation sets the relation between the additional inductance and capacitance in each phase that applies the PIC. For each phase, a single degree of freedom exists.

$$\omega_{0} = \sqrt{\frac{1}{L_{A}C_{A}}} = \sqrt{\frac{1}{L_{C}C_{C}}} Q_{A} = \frac{C_{A}}{C} Q_{C} = \frac{C_{C}}{C}$$

$$(7)$$

As a result of the imbalanced compensation, the resonance frequency of each asymmetrically compensated phase is different. With asymmetrical compensation deployed in phases *A* and *C* (see Figure 1),  $\omega_{r,A}'' \neq \omega_{r,B}'' \neq \omega_{r,C}''$ . The resonance frequency is then defined as Equations (8)–(10) for phase *A*, *B*, and *C*. Since no additional impedances are included in phase *B*, it should be noted that  $\omega_{r,B}'' = \omega_{r,B}'$ .

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$$\omega_{r,A}^{"} = \sqrt{\frac{1}{L_{eq,A}C_{eq,A}}} = \sqrt{\frac{1}{(L+L_A)\frac{CC_A}{C+C_A}}}$$

$$\omega_{r,A}^{"} = \sqrt{\frac{C+C_A}{CC_A(L+L_A)}}$$
(8)

$$\omega_{r,B}^{\prime\prime} = \omega_{r,B}^{\prime} = \sqrt{\frac{1}{LC}} \tag{9}$$

$$\omega_{r,C}'' = \sqrt{\frac{C + C_C}{CC_C(L + L_C)}}$$
(10)

Figure 4 shows the vector and phasor diagrams of the phase impedances at a sub-synchronous frequency  $\omega_{r,B}''$  for the classical as well as PIC concepts. For the classical compensation concept, the phase impedances are identical. At  $\omega_{r,B}'$ , these impedances are equal to the damping (Figure 4a) and result in a balanced phasor (Figure 4c). In the PIC, the phase impedances at  $\omega_{r,B}''$  are different (Figure 4b), which lead to an unbalanced phasor (Figure 4c). As mentioned in Section 3, the vector and phasor diagrams at the fundamental frequency are the same for both compensation concepts.



**Figure 4.** Vector diagram of the phase impedances at sub-synchronous frequency for (**a**) classical series compensation and (**b**) phase imbalance compensation. For both cases, the phasor diagram of the impedance is shown as well (**c**). At sub-synchronous frequencies, the phases that are compensated using the imbalance concept have different impedances compared to the respective phases that use the classical compensation concept.

When a disturbance occurs in the grid, the unbalanced phase impedances will create a non-uniformly distributed *MMF*. This *MMF* is defined as given in Reference (11).

$$MMF_{total} = \sum_{i=A}^{C} MMF_{i}$$
  

$$MMF_{A} = K\hat{I}_{A}\cos(\omega t)\cos\theta$$
  

$$MMF_{B} = K\hat{I}_{B}\cos(\omega t)\cos\theta$$
  

$$MMF_{C} = K\hat{I}_{C}\cos(\omega t)\cos\theta$$
  
(11)

With no series compensation,  $MMF_{\omega'_{r,B}}$  is zero. With the classical compensation, the  $MMF_{\omega'_{r,B}}$  in each phase is identical, as the maximum current  $\hat{l}$  in each phase is the same (due to the same Z' in each phase). On the other hand, for each phase that implements an asymmetrical compensation,  $\hat{l}$  is different depending on Z'', which, in turn, is a function of the degree of asymmetry Q in that respective phase. As such, the phase imbalance concept is able to alter the  $MMF_{\omega'_{r,B}}$  (and electrical torque) at sub-synchronous frequencies, and, therefore, potentially mitigate DFIG-SSR. The larger Z'', the smaller

 $\hat{I}$  and the smaller the  $MMF_{\omega''_{r,B}}$ . With decreasing  $MMF_{\omega''_{r,B}}$ , the resulting subsynchronous oscillations are smaller.

#### 4.2. Developed Analytical Model for Phase Imbalace Compensation

In order to create an understanding of how the PIC alters the dynamic behaviour of the power system, an analytical model describing the relation between the inductance and capacitance of the imbalanced phase and the resonance frequency of that phase, is developed next. The starting point for this derivation is  $\omega_{r,B}'' = \omega_{r,B}'$ . Consider phase *A*. If  $\omega_{r,A}''$  differs from  $\omega_{r,B}''$  by  $\Delta \omega_r$ , then the relation between  $\omega_{r,A}''$  and  $\omega_{r,B}''$  is given by Equation (12).

$$\omega_{r,A}^{\prime\prime} = \omega_{r,B}^{\prime\prime} + \Delta\omega_r = \omega_{r,B}^{\prime} + \Delta\omega_r = \sqrt{\frac{C + C_A}{CC_A(L + L_A)}}$$
(12)

Substituting Equation (7) into Formula (12) yields Equation (13) and, after rearranging, it yields Equation (14).

$$\omega_{r,B}' + \Delta \omega_r = \sqrt{\frac{C + C_A}{CC_A L + \frac{C}{\omega_0^2}}}$$
(13)

$$\omega_{r,B}^{\prime}{}^2 + 2\Delta\omega_r\omega_{r,B}^{\prime} + \Delta\omega_r{}^2 = \frac{C + C_A}{CC_A L + \frac{C}{\omega_0{}^2}}$$
(14)

Solving Equation (14) for  $C_A$  results in Equation (15).

$$C_A = C \left[ \left( \frac{\omega'_{r,B} + \Delta \omega_r}{\omega_0} \right)^2 - 1 \right] \frac{1}{1 - LC \left( \omega'_{r,B} + \Delta \omega_r \right)^2}$$
(15)

Substituting  $LC = \frac{1}{\omega'_{r,B}^2}$  in Equation (15) gives Formula (16).

$$C_A = C \left[ \left( \frac{\omega'_{r,B} + \Delta \omega_r}{\omega_0} \right)^2 - 1 \right] \frac{1}{1 - \left( \frac{\omega'_{r,B} + \Delta \omega_r}{\omega'_{r,B}} \right)^2}$$
(16)

Finally, solving Formula (16) results in Equation (17).

$$C_A(\Delta\omega_r) = C \left[ \frac{\left(\omega_{r,B}' + \Delta\omega_r\right)^2}{\omega_0^2 - \left(\frac{\omega_0(\omega_{r,B}' + \Delta\omega_r)}{\omega_{r,B}'}\right)^2} - \frac{1}{1 - \left(\frac{\omega_{r,B}' + \Delta\omega_r}{\omega_{r,B}'}\right)^2} \right]$$
(17)

The relation between  $C_A$ ,  $\Delta \omega_r$ , and k is derived by substituting Equation (5) in Equation (17), which ultimately results in Formula (18).

$$C_A(k,\Delta\omega_r) = C \left[ \frac{\left(\sqrt{k\omega_0^2} + \Delta\omega_r\right)^2}{\omega_0^2 - \left(\frac{\omega_0\left(\sqrt{k\omega_0^2} + \Delta\omega_r\right)}{\sqrt{k\omega_0^2}}\right)^2} - \frac{1}{1 - \left(\frac{\sqrt{k\omega_0^2} + \Delta\omega_r}{\sqrt{k\omega_0^2}}\right)^2} \right]$$
(18)

The degree of asymmetry *Q* for phase *A* is then defined as given in Equation (19) and represents the analytical model of the PIC concept. The same relation holds for phase *C*.

$$Q(k,\Delta\omega_r) = \frac{\frac{C_A(k,\Delta\omega_r)}{C} = Q(k,\Delta\omega_r)}{\left(\sqrt{k\omega_0^2 + \Delta\omega_r}\right)^2} - \frac{1}{1 - \left(\frac{\sqrt{k\omega_0^2 + \Delta\omega_r}}{\sqrt{k\omega_0^2}}\right)^2}$$
(19)

Figure 5 is derived from Equation (19) and shows different compensation levels and different degrees of asymmetry. The resulting change in resonance frequency is  $\Delta f_r$ . Since any Q requires an additional series capacitor, the resonance frequency  $f_r$  will always increase.  $\Delta f_r$  is always positive, as is shown in Figure 5, and it is, therefore, not possible to reduce the resonance frequency below  $\omega''_{r,B}$  using the series resonance PIC scheme. Furthermore, for any given k,  $\Delta f_r$  is inversely proportional to Q.



**Figure 5.** Influence of *k* and  $\Delta f_r$  on the required degree of asymmetry *Q*.

From Figure 5, it can be observed that an imposed  $\Delta f_r$  in the PIC can be achieved by modifying either *k* or *Q* in the design phase. For fixed *Q*, a modification of *k* (which is a quantity at  $f_0$ ) results in an altered active power transfer limit and altered  $\Delta f_r$ . For fixed *k*, modification of *Q* results only in altered sub-synchronous behaviour (i.e., altered  $\Delta f_r$ ), as  $Z''(\omega_0) = Z'(\omega_0)$  should be respected for each value of *Q*.

#### 4.3. Development of the Power System Model and DFIG Model

To assess the effectiveness of PIC in mitigating DFIG-SSR, the development of the power system and DFIG model is briefly described next. Due to its highly local nature, the investigation of DFIG-SSR requires a detailed model of the DFIG. Modelling of the grid is required in such a way that it excites the oscillation mode, which, ultimately, will result in the interaction between the DFIG and the grid. Therefore, many investigations on DFIG-SSR reported in literature use the topology of the IEEE First Benchmark Model [21]. The model in the current work is modified in two ways. First, the synchronous generator model is replaced by a detailed DFIG model. Second, the equivalent grid is modelled as a voltage source with a series impedance, so that the grid contains a sub-synchronous resonance point at 30 Hz, which corresponds to *k* at 36%. The equivalent grid has a short circuit ratio of 10. The single line diagram of the power system under consideration (including all relevant network data) is given in Figure 6. Several practical studies have used similar models for investigating DFIG-SSR [22–25].



Figure 6. Modified IEEE First Benchmark Model.

Detailed modelling of the DFIG is crucial, as the adverse interactions are influenced by the damping available at the DFIG terminal. A generic detailed EMT wind turbine model following the IEC standard 61400-27-1 [26] was developed in PSCAD as part of the MIGRATE project [27]. Since the controls presented in the IEC standard are meant for root mean square applications, several adaptations and additions were implemented in order to make the models applicable for EMT analysis. The development of the detailed model is reported in Reference [28]. This model contains, among others, a carefully designed filter to address switching harmonics, where the filter design is based on Reference [29]. Furthermore, the semiconductor switches are modelled in detail, including the thyristor's turn on and turn off resistances, giving a more realistic damping behaviour of the DFIG. The mechanical dynamics of the wind turbine generator were modelled using a two mass model. The classical double loop control was used as a control structure of the grid as well as the rotor side converter of the DFIG. The implemented controls are the same as the ones used in the authors' previous work (Figures 8 and 9 in Reference [1]). The DFIG's parameters are given in Appendix A. The behaviour and fault response of the developed model was successfully validated against vendor models. Further discussion on the model development is out of the scope of the current work.

#### 4.4. PIC Evaluation for DFIG-SSR Mitigation

Prior to evaluating the effectiveness of the PIC in mitigating DFIG-SSR, the influence of the PIC on the impedance characteristic of the transmission line in the modified IEEE First Benchmark Model is assessed. One of the contributions of this work is the derivation and validation of Formula (19) through repetitive EMT simulations of an actual system, considering different values for the degrees of asymmetry across one and two phases. To this end, low *Q* values are chosen as these have a more pronounced effect on the impedance characteristic. The results are shown in Figure 7. Let  $Q_A$  and  $Q_C$  be equal to 0.2 and 0.5, respectively. Substituting these values for the degree of asymmetry and *k* equal to 36% in Equation (19) results in  $\Delta f_r$  of 14 and 10 Hz for phase *A* and phase *C*. As is shown in Figure 7a, the exact same results are achieved using EMT simulations, where the impedance characteristic of each phase is determined using the dynamic frequency scanning technique [30]. Figure 7b shows the impedance characteristics of phase *A* for different levels of *Q*. These results are also in line with Formula (19) and Figure 5. Higher *Q* values decrease  $\Delta f_r$ .

When the DFIG is added to the overall system, the equivalent impedance characteristic is changed by following the actual  $f_r$  will be different from 30 Hz. As shown in Figure 8, the connection of the DFIG to the classical series compensated transmission line reduces  $f_r$  to 10 Hz. The converter controller parameters, inductances from the transformers and the DFIG machine, and the inductance and shunt capacitance from the filter at the GSC are additional impedances that alter the impedance characteristic of the DFIG. The additional impedance introduced by these components is one of the reasons for the reduced  $f_r$ .



**Figure 7.** Influence of the degree of asymmetry Q on the resonance frequency, as seen from the point of common. (a) Impedance characteristic of phase A ( $Q_A = 0.2$ ), B and C ( $Q_C = 0.5$ ); (b) Impedance characteristic of phase A for  $Q_A = 0.10, 0.25$  and 0.50.



**Figure 8.** Impedance response of the doubly fed induction generator (DFIG) and transmission system. Using the impedance-based stability analysis methodology, the resonance frequency was found to be 10 Hz.

To identify whether there exists an adequate degree of asymmetry Q capable of mitigating DFIG-SSR, optimisations were performed in PSCAD. Q is defined as given in Equation (7). An optimisation approach is selected, as it can quickly and efficiently search through wide parameter ranges. Two independent optimisations were performed. In the first optimisation, the goal was to find the adequate degree of asymmetry  $Q_A$  when only phase A is compensated using PIC, i.e., single-phase imbalance compensation. The objective function of this optimisation is given by Formula (20), where performance index  $\eta$  is developed to monitor the performance of the optimisation. The performance index  $\eta$  is a measure of the energy associated with the oscillation. The threshold of  $\eta$  below which DFIG-SSR can be considered mitigated was determined using extensive empirical analysis. These analyses consisted of thousands of EMT simulations, the transmission line was compensated using the classical compensation scheme. For each simulation, the performance index  $\eta$  is smaller than 0.07 per unit, as this represents a case with negligible oscillation energy. As a reference, under the classical series compensation scheme,  $\eta$  is equal to approximately 0.215 per unit.

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In the second optimisation, the goal was to find adequate degrees of asymmetry  $Q_A$  and  $Q_C$  when phase *A* and phase *C* are compensated using PIC, i.e., two-phase imbalance compensation. The objective function is given by Formula (21). In both optimisations, DFIG-SSR, when using phase imbalance compensation, is triggered by a switching action, which causes a permanent radial connection of the DFIG to the unbalanced compensation.

minimise 
$$\eta = \int_{t=36}^{t=38} (P_{ref}(t) - P_{measured}(t)_{Q_A})^2 dt$$
  
subject to  $0.01 < Q_A < 25$ 
(20)

minimise 
$$\eta = \int_{t=36}^{t=38} (P_{ref}(t) - P_{measured}(t)_{Q_A,Q_C})^2 dt$$
  
subject to 0.01 <  $Q_A, Q_C$  < 25 (21)

To illustrate a case where DFIG-SSR is successfully mitigated when the transmission line is equipped with classical series compensation, Figure 9 shows the response of the DFIG to a fault before and after modification of the rotor side converter controller parameters. After 1.5 s, a switching action caused the DFIG to be radially connected to the series capacitor (classical compensation), which resulted in DFIG-SSR in the case of unmodified parameters. After modification of the RSC controller parameters, DFIG-SSR was mitigated.



**Figure 9.** Mitigation of doubly fed induction generator-sub-synchronous resonance (DFIG-SSR) after a rotor side converter and controller parameter modification.

The optimisation results are given in Figure 10a for the single-phase imbalance and in Figure 10b for the two phase imbalance compensation. Approximately 3000 iterations for the single-phase imbalance compensation and 4000 iterations for the two-phase imbalance compensation were taken as sufficient to judge the optimisation's convergence, where each iteration includes an EMT simulation. The Genetic Algorithm [31] was used as an optimisation algorithm, where the settings (e.g., initial population, mutation rate, pairing method, etc.) were chosen as recommended in Reference [32]. The influence of the algorithm's settings on the optimisation results is out of the scope of the current work and is left as a topic for further research.

25

20

15

0





**Figure 10.** Optimization of the degree of asymmetry Q to mitigate doubly fed induction generator-sub-synchronous resonance (DFIG-SSR). Imbalance compensation applied for (**a**) one phase and (**b**) two phases.

In neither of the cases,  $\eta$  converged to a value smaller than 0.07 per unit and, consequently, no adequate degree of asymmetry capable of mitigating DFIG-SSR was identified. On the contrary, for the investigated DFIG, the PIC concept consistently performed either the same as or worse than the classical series compensation scheme. This is concluded by observing the evolution of  $\eta$  as the degree of asymmetrical change. It shows that  $\eta$  decreases with increasing values of Q, but never reaches a value below 0.215 per unit, which is the value of  $\eta$  for the classical series compensation scheme. For the sake of consistency, the optimisations were performed multiple times without affecting the findings.

One way to assess in more detail the influence of Q on mitigating DFIG-SSR (and, thus, on  $\eta$ ), is to observe how the *MMF* at  $f_r$ , *MMF*<sub> $f_r</sub>$ , changes with changes in the degree of asymmetry Q. Using Equation (11), *MMF*<sub> $f_r</sub> can be observed by observing the peak current <math>\hat{l}$ . The higher  $\hat{l}$ , the stronger  $MMF_{f_r}$  and the larger  $\eta$ , i.e., the poorer the capability for mitigating DFIG-SSR. Different levels of asymmetry in phase A were evaluated. Table 1 provides the resonance frequency and maximum phase current  $\hat{l}_A$  for five values of  $Q_A$ . For the analysis, the operating point of the DFIG was fixed at a windspeed of 10 m/s (the nominal windspeed of the DFIG is 10.2 m/s). DFIG-SSR occurs mainly below nominal windspeeds [1], which is the reason why the windspeed of 10 m/s was selected. However, lower windspeeds will not alter the main conclusions of the analysis. For comparison purposes, the results from the classical compensation scheme are included as well.</sub></sub>

Q	$f_{r,A}(Hz)$	$\hat{I}_{f_{r,A}}(kA)$
Classical Compensation	10	0.214
0.1	20	0.383
0.2	16	0.368
0.5	14	0.279
2.5	13	0.231
5.0	12	0.226

**Table 1.** Maximum current at  $f_r$  for phase A for different levels of asymmetry Q.

Two observations can be made. First, it is confirmed again that the resonance frequency increases by reducing the degrees of asymmetry. This is in line with Equation (19). Second, with decreasing  $Q_A$  (i.e., increasing  $f_r$ ),  $\hat{I}_A$  increases, which implies that, with lowering degrees of asymmetry,  $MMF_{f_r}$  increases. Figure 11 shows the measured instantaneous current in phase A for three cases: no series compensation, classical series compensation, and phase imbalance compensation. It shows a worse performance of the phase imbalance series compensation compared to the classical series compensation. This  $Q_A$ - $\hat{I}$ - $MMF_{f_r}$  relation explains why  $\eta$  is relatively large for small values of  $Q_A$  and small for larger values of  $Q_A$ .



**Figure 11.** Measured instantaneous current in phase *A* for three cases: without series compensation, with classical series compensation, and with phase imbalance series compensation.

Lastly, to explain why  $\hat{l}$  is increasing as  $Q_A$  decreases, it is necessary to investigate the damping characteristic of the DFIG. Using the perturbation-based dynamic frequency scan [30], the damping of the DFIG was estimated at windspeeds of 6 m/s (i.e., below nominal windspeed), 10 m/s (i.e., around nominal windspeed), and 20 m/s (i.e., above nominal windspeed). The results are given in Figure 12.



Figure 12. DFIG damping under different windspeed conditions.

The following conclusions can be drawn from Figure 12. First, Considering the  $f_r$  of 10 Hz, any  $\Delta f_r$  achieved by means of phase imbalance compensation, where  $f_r''$  remains in the negative resistance region, will deteriorate the damping provided by the DFIG. To achieve an  $f_r''$  outside the negative resistance region requires *Q*-values, which are not economical for practical applications. For different windspeeds, the negative resistance region of the DFIG is given in Table 2.

Table 2. Negative resistance region of DFIG under different windspeed conditions.

Wind Speed (m/s)	$f_{min}(Hz)$	$f_{max}(Hz)$
5	6	41
6	6.5	41.25
7	7	41.5
8	7.5	42
9	8	43
10	8	43
15	9.8	43

Second, for the same *Q*, the damping worsens with decreasing windspeeds. Therefore, the design of the PIC as a mitigation solution for DFIG-SSR needs to be validated across several operating conditions.

#### 4.5. Mitigating Torsional SSR with PIC

The analytical model that was developed in Section 4.2 proved that, for any degree of asymmetry in the phase imbalance compensation, the resulting resonance frequency is increased. Furthermore, Section 4.4 showed that, in the negative resistance region of a DFIG, the negative damping increases with a growing resonance frequency. It was concluded that the PIC cannot mitigate DFIG-SSR, as the resulting shift in  $f_r$  deteriorates the electrical damping even further.

Why is it then that the PIC is theoretically able to mitigate classical SSR, as was shown in Reference [8]? This is due to the fact that the negative resistance region for a conventional generator is rather limited compared to a DFIG. This is briefly discussed by using two examples. For a wind power plant in China, torsional interaction (SSR) was investigated. Using the complex torque coefficient method, electrical damping coefficients under different wind power output levels were calculated (Figure 13 [33]).



Figure 13. Electrical damping coefficients under different wind generation levels [33].

The possibility of a torsional interaction with  $f_r$  of 31 Hz was identified. At this frequency, the damping coefficients were negative. Compared to the DFIG damping (Figure 12), the negative resistance region in Figure 13 is limited. Increasing  $f_r$  with as little as 2 Hz results in positive damping and eliminates the adverse torsional SSR interaction. The PIC is capable of achieving this shift in  $f_r$  and, therefore, could be a potential mitigation solution.

In another practical example, SSR analysis was carried out to investigate the torsional characteristics of the Hanul nuclear power plant in Japan [34]. Increased probability of torsional interaction between the generator and a fixed series capacitor was identified, where, depending on the operating condition,  $f_r$  ranged between 35 and 39 Hz. A 2–5 Hz shift in  $f_r$  would result in positive damping and, therefore, eliminate the adverse torsional interactions. In this case, the PIC could be one of the potential solutions to achieve this.

# 5. Conclusions

The goal of this paper was to evaluate to which extent the phase imbalance compensation scheme, as an alternative for the classical compensation scheme, can mitigate DFIG-SSR. To this end, an analytical model of the imbalance concept was developed and validated using EMT simulations. To prevent biased conclusions, an analysis based on optimisation was proposed and conducted to identify the degrees of asymmetry that could mitigate DFIG-SSR. The optimisation showed that, in the investigated resonance scheme of the imbalance compensation concept, any degree of asymmetry increases the

resonance frequency. Due to the impedance characteristic of the DFIG, increasing resonance frequencies make the damping of the DFIG more negative. As a result, the *MMF* at this frequency only gets stronger and further deteriorates the DFIG-SSR behaviour. Therefore, the investigated phase imbalance

Compared to a DFIG, the negative resistance region for conventional synchronous machines is small in the subsynchronous frequency range. Therefore, the phase imbalance compensation scheme can theoretically mitigate e.g., torsional interactions with a conventional synchronous machine by shifting the resonance frequency outside of the negative resistance region.

Lastly, the evaluation of the capability of other resonance schemes in mitigating DFIG-SSR is left as a topic for further research.

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compensation concept cannot mitigate DFIG-SSR.

# Appendix A

Parameter	Value	
GENERATOR		
Machine rating	4 MVA	
Nominal system frequency	50 Hz	
Rated voltage	0.9 kV	
Stator leakage inductance	0.10 p.u.	
Rotor leakage inductance	0.11 p.u.	
Magnetizing inductance	4.5 p.u.	
Stator resistance	0.0054 p.u.	
Rotor resistance	0.00607 p.u.	
Stator/rotor turns ratio	0.391	
Angular moment of inertia	6 s	
Drive train stiffness	200 p.u.	
Drive train damping	1.7 p.u.	
CONVERTER		
$K_{p,dc}, K_{p,O}, K_{p,dS}, K_{p,qS}$	5, 1, 1.2, 1.2	
$T_{i,dc}, T_{i,O}, T_{i,dS}, T_{i,qS}$	0.05, 0.1, 0.02, 0.02	
$K_{pP,PCC}, K_{pO,PCC}, K_{p,dR}, K_{p,qR}$	0.2, 0.3, 1.5, 2	
$T_{iP,PCC}, T_{iQ,PCC}, T_{i,dR}, T_{i,qR}$	0.5, 0.06, 0.02, 0.02	

Table A1. DFIG generator and converter data.

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