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Mitigating Impulsive Noise for Wavelet-OFDM Powerline Communication

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Abstract: Advanced metering infrastructure (AMI) is an important application of smart grid communication technology used for the remote monitoring and control of smart meters. Broadband powerline communication (BB-PLC) systems could perhaps be used for AMI; however, impulsive noise (IN) greatly degrades performance. In addition to the fast Fourier transform (FFT)-based orthogonal frequency-division multiplexing (OFDM), IEEE 1901 specifications have defined the other physical layer called wavelet-based OFDM. Even though many existing studies have reported the IN mitigation algorithms for the FFT-based OFDM system, these approaches may not directly apply to the wavelet-OFDM-based PLC systems. In this paper, we propose a robust receiver for PLC systems based on wavelet-OFDM. The proposed receiver comprises a pre-IN mitigation block, an adaptive inverse discrete wavelet transform, and an iterative IN reconstruction block. The iterative cancellation of strong IN samples leads to a gradual improvement in the quality of the received signal. Instead of using the frequency domain approach, we reduce the inter-dependency of the channel estimation and per-subchannel equalization by using the time domain signal processing. Besides, we apply variable step-size adaptive algorithms to reduce the impact of IN during the training processes for the channel estimator and per-subchannel equalizer. In accordance with IEEE 1901 specifications, we built a simulation environment to evaluate the effectiveness of the proposed method. Simulation results demonstrated that conventional blanking devices fall short in terms of IN mitigation, and that the proposed scheme is able to achieve performance values approaching those obtained in cases without IN.

Keywords: advanced metering infrastructure (AMI); wavelet-OFDM; impulsive noise (IN); broadband powerline communication (BB-PLC)

1. Introduction

With an advanced metering infrastructure (AMI), researchers have shown that broadband powerline communication (BB-PLC) systems are applicable to backhaul communication [1] and service quality estimation [2]. In Korea, a power company has adopted BB-PLC systems for nationwide AMI to serve 22.50 million low-voltage customers until 2020 [2]. For distributed smart grid applications, BB-PLC systems are possible wired technologies to indoor communications between smart meters and photovoltaic inverters [3]. One of the advantages of powerline communication (PLC) technologies is that the powerline infrastructure has already deployed so that installation costs could be reduced. The transmission distance for BB-PLC is up to about 1.5 kilometers, it is more suitable for home area networks [4] (p. 117). Some field trials have been conducted to validate feasibility of using BB-PLC for smart grid applications [5]. It has pointed out that narrowband PLC (NB-PLC) is affected by inverters used in some home appliances. This is mainly because of NB-PLC and inverters are operating on the same frequency band (3–148.5 kHz). However, BB-PLC does not suffer from such problems because of

using a broad frequency band (3–30 MHz). In addition, the highest available date rate with NB-PLC, which is around 500 Kb/s, is insufficient for common and future smart grid services. However, PLC was designed for power delivery rather than data transmission; therefore, from the perspective of data transmission, issues associated with PLC channels like impulsive noise (IN), frequency-dependent attenuation, narrowband interference, and time-varying impedance are needed to be addressed [6,7]. The most intractable issue is IN. Due to the short duration and high magnitude properties, IN can be regarded as high-power wideband noise in the frequency domain [8]. In PLC systems based on orthogonal frequency-division multiplexing (OFDM), the noise bucket effect [9] significantly increases in the bit error rate (BER) [10]. BB-PLC schemes with two physical specifications have been proposed by IEEE: fast Fourier transform (FFT) -based OFDM and wavelet-based OFDM [11]. Wavelet-OFDM-based PLC employs an *M*-band transmultiplexer, which allows the stopband attenuation of subcarriers to exceed 35 dB [12]. Comparing with FFT-based OFDM systems, wavelet-based OFDM systems offer better subchannel isolation. An inverse discrete wavelet transform (IDWT) block is used at the

transmitter and a discrete wavelet transform (DWT) block is used at the receiver.

Many IN mitigation schemes have been proposed for FFT-OFDM-based PLC systems. Zhidkov assessed a number of nonlinear IN mitigation techniques, included clipping, nulling, and a mixture of both nulling and clipping approaches [13]. A scheme combining nulling, clipping, and replacement was proposed in Reference [14], and the multipath issue was further considered in Reference [15]. A scheme based on deep clipping was proposed in Reference [16] to enhance the effectiveness of the nonlinear IN mitigation methods described above. Unfortunately, some pre-determined parameters, which tend to vary with the channel conditions, are required for determining the threshold values. In Reference [17], the authors utilized zero carriers to estimate noise power and the IN parameters were extracted to calculate the optimal clipping threshold. However, Yin pointed out that nonlinear devices used to mitigate IN cause intercarrier interference and degrade system performance [18]. Consequently, Zhidkov reported an iterative IN mitigation scheme for OFDM-based digital terrestrial video broadcasting systems [19]. A number of sophisticated multilayer perceptron [20] and deep neural network [21] schemes have been developed to mitigate IN. An iterative receiver for FFT-OFDM-based PLC systems was proposed in Reference [22], which simultaneously considered the channel estimation, channel equalization, and IN mitigation issues in the frequency domain. By iteratively mitigating part of IN samples with stronger amplitude, the quality of channel estimation could be improved gradually. Thus, the quality of the estimation of residual IN samples was gradually improved as well, i.e., the channel estimation and IN mitigation are mutually beneficial. In Reference [23], the authors used a linear precoder at the transmitter and applied overlapped frequency-domain equalizers to mitigate IN for wavelet-OFDM-based PLC systems. The linear precoder, which is implemented by using a Walsh–Hadamard transform, improves the efficiency of the frequency domain equalizer. However, adding the linear precoder to the transmitter does not comply with IEEE 1901 specifications. Moreover, the frequency domain equalizer requires extra FFT and inverse FFT operations.

Although many IN mitigation approaches have been proposed on FFT-based OFDM systems, these existing approaches cannot directly apply to the wavelet-based OFDM. To the best of our knowledge, researchers have largely overlooked the issue of IN mitigation for wavelet-OFDM-based PLC systems. Inspired by the iterative receiver for the FFT-OFDM-based PLC system [22], this paper presents a robust receiver architecture for wavelet-OFDM-based PLC systems to fill this research gap. The receiver comprises a pre-IN mitigation block, an adaptive DWT block, and an iterative IN reconstruction block. The pre-IN mitigation block copes with the stronger portion of the IN to facilitate the training of equalizers and channel estimator. Adaptive per-subchannel equalizers are applied to the conventional DWT block to flatten out the passband of each subchannel, and the IN reconstruction block iteratively estimates the residual IN. Note that to alleviate the impact of IN samples on the training algorithms, we applied a variable step-size adaptive algorithm [24]. Comparing with our previous work [22], the new contributions of this paper are summarized as follows. First, we proposed an adaptive analysis filter bank, comprised of the DWT block and adaptive

per-subchannel equalizers, such that the channel is equalized in the time domain for each subchannel. In this way, no additional FFT operation is required to perform equalization in the frequency domain. Note that a wavelet-based OFDM system does not implement guard interval and causes performance degradation in the multipath PLC channels. Thus, the realization of equalizers form wavelet-based OFDM systems is an important issue that needed to be addressed. Second, we decoupled the channel estimation from the per-subchannel channel equalization, such that the inter-dependency between these two functions could be reduced. Third, we exploited a variable step-size adaptive algorithm to estimate the channel impulse response in the time domain, such that the impact of the IN on the channel estimation could be reduced.

The remainder of this paper is organized as follows. Section 2 describes the system models used in this paper. Section 3 details the proposed IN mitigation scheme. Section 4 presents results obtained using a simulation model based on IEEE 1901 specifications. Conclusions are drawn in Section 5.

2. System Model

Figure 1 presents the system model of our proposed IN mitigation scheme for wavelet-OFDM PLC. The details of each block are described below.



Figure 1. Proposed system model. DWT = discrete wavelet transform; IDWT = inverse discrete wavelet transform; IN = impulsive noise; S/P = serial-to-parallel; P/S = parallel-to-serial; AWGN = additive white Gaussian noise; and MUX = multiplexer.

2.1. Transmission Using IDWT

First, a serial-to-parallel (S/P) block converts the binary information source x[n] into M parallel subchannels, where we denote $X_k[n]$ as the information source on the k-th subchannel. Next, $X_k[n]$ is assumed to be mapped into the data symbol $S_k[n]$ with a pulse amplitude modulation (PAM) scheme by a mapper. Then, an IDWT block converts the carriers to the time domain data s[n]. The IDWT for baseband communication is the synthesis side of a cosine modulated filter bank (CMFB). The IDWT block depicted in Figure 1 is detailed in Figure 2. IDWT operations involve the application of M synthesis filters. Each input signal is up-sampled using factor M and passed through a set of synthesis filters. Without loss of generality, we consider the baseband transmission scenario, in which the number of subchannels M = 512 and the length of prototype filter N = gM. Note that g = 4 is the overlapping factor, which implies that the symbol duration in the wavelet-OFDM system is extended

over *g* consecutive symbols so that a guard interval is not used. We use $F_k(z)$ to denote the Z-transform of the *k*-th synthesis filter $f_k[n]$, which can be described as follows:

$$f_k[n] = \sqrt{\frac{2}{M}} \cdot p[n] \cdot \cos\left[\left(k + \frac{1}{2}\right) \frac{\pi}{M} \left(n + \frac{M+1}{2}\right)\right] \cos(\theta_k),\tag{1}$$

where k = 0, ..., M - 1 is the index of the subchannel, p[n] is the prototype filter with coefficients given in [11] (Annex B), and θ_k equals 0 or π . By adding the *M* output of the synthesis filters, we have the channel inputs s[n] at the output of the IDWT block.



Figure 2. Details in an inverse IDWT block with M subchannels.

2.2. Powerline Channel Model

Powerline channels can generally be modeled as multipath channels. Zimmerman [25] proposed using the following frequency response H(f) to model the multipath effects caused by the powerlines:

$$H(f) = \sum_{i=1}^{N} \alpha_i \cdot \exp\left\{-(a_0 + a_1 f^\beta)d_i\right\} \cdot \exp\left\{-j2\pi f\tau_i\right\},\tag{2}$$

where N_{path} denotes the number of paths, α_i is the weighting factor, a_0 and a_1 are the attenuation parameters, d_i is the length of the channel, β is the exponent of the attenuation factor, and τ_i is the delay associated with the *i*-th path.

2.3. Additive Noise Model

For indoor BB-PLC systems, asynchronous IN is the dominant source of noise [26]. In this paper, we consider two additive noise sources: background noise and asynchronous IN. We assume that background noise w[n] is additive white Gaussian noise (AWGN) with zero mean and variance of σ_w^2 . The asynchronous IN i[n] is modeled as Bernoulli-Gaussian (BG) IN [27] with zero mean and variance σ_i^2 and can be described as follows:

$$i[n] = b[n] \cdot g[n], \tag{3}$$

where b[n] denotes the Bernoulli process with a probability of success p and g[n] is the white Gaussian process with zero mean and variance σ_g^2 . Note that we use the occurrence probability of IN p and the Gaussian-to-impulsive-noise ratio (GINR) $\Gamma := \sigma_w^2 / \sigma_I^2$ to characterize the intensity of the IN. A higher value for p and smaller value for Γ indicate a situation with a stronger noise within a channel. The

probability density function (PDF) of the aggregated noise $\eta[n] = w[n] + i[n]$ seen by the receiver is given as follows:

$$P_{\eta}(\eta) = (1-p)G(\eta, 0, \sigma_w^2) + pG\left(\eta, 0, (1+\Gamma)\sigma_I^2\right),$$
(4)

where $G(\cdot)$ denotes the Gaussian PDF, which is defined by:

$$G(\eta, \mu, \sigma^2) = \frac{1}{\sqrt{2\pi\sigma}} \exp\left(\frac{-(\eta - \mu)^2}{2\sigma^2}\right),\tag{5}$$

where μ and σ^2 , respectively, denote the associated mean and variance. Thus, the received signal can be expressed as follows:

$$r[n] = s[n] \otimes h[n] + w[n] + i[n], \tag{6}$$

where s[n] is the channel input signal; h[n] denotes the channel impulse response (CIR) of the powerline; and \otimes represents the convolution operation.

2.4. Proposed Receiving Block

In accordance with the iterative IN mitigation approach in Reference [22], the received signal is clipped by a pre-IN mitigation block. A multiplexer (MUX) is then used to select the input signal of the adaptive DWT block, which outputs the symbols for each subchannel. The MUX selects a less contaminated received signal as the input for the adaptive DWT block. We denote $\hat{S}_k[n]$ as the output signal of the adaptive DWT block for the *k*-th subchannel. The signal $\hat{X}_k[n]$ for each subchannel is then demapped and applied to the iterative IN reconstruction block to estimate the IN data $\hat{u}[n]$. This makes it possible to obtain a received signal $\hat{r}[n]$ of greater clarity by subtracting the estimated IN $\hat{u}[n]$ from the input signals of the adaptive DWT block, i.e., $\tilde{r}[n]$. Following this iterative procedure, a parallel-to-serial (P/S) block converts $\hat{X}_k[n]$ into the estimated information source $\hat{x}[n]$.

3. Proposed IN Mitigation Scheme

3.1. Pre-IN Mitigation

The Kurtosis value ρ of r[n] is defined as:

$$\rho\left(r[n]\right) = \frac{\mathbb{E}\left[\left(r[n] - \mu_r\right)^4\right]}{\sigma_r^4},\tag{7}$$

where $\mathbb{E}[\cdot]$ represents expectation operation; μ_r and σ_r , respectively, denote the mean and standard deviation of r[n]. If the received signal is infected by strong IN, then $\rho(r[n]) \ge 3$ holds. Previous work has revealed that disabling the blanking function of the pre-IN mitigation block is desirable, due to the fact that $\rho(r[n]) < 4.6$ [28]. Otherwise, the pre-IN mitigation block nullifies portions of the strong IN samples, as follows:

$$\bar{r}[n] = \begin{cases} 0, & |r[n]| > T_1[n] \\ r[n], & \text{otherwise} \end{cases},$$
(8)

where $T_1[n]$ is an adaptive threshold [22], which can be calculated as follows:

$$T_1[n] = 4.12 \cdot \mathbb{E}\{|r[n]|\}.$$
(9)

3.2. Adaptive DWT

After pre-IN mitigation, $\tilde{r}[n]$ is input to the adaptive DWT block. The adaptive DWT block (see Figure 3) comprises an *M*-subchannel analysis filter bank, a per-subchannel equalizer (PSE_k),

and a down-sampling operation with a factor *M*. Note that $G_k(z)$ denotes the Z-transform of the *k*-th analysis filter $g_k[n]$, which can be expressed as follows:

$$g_k[n] = \sqrt{\frac{2}{M}} \cdot p[n] \cdot \cos\left[\left(k + \frac{1}{2}\right) \frac{\pi}{M} \left(N - 1 - n + \frac{M + 1}{2}\right)\right] \cdot \cos(\theta_k),\tag{10}$$

where k = 0, ..., M - 1 is the index of the subchannel, p[n] is the prototype filter, and θ_k equals 0 or π . The PSE_k is a finite impulse response (FIR) adaptive filter, used to equalize the powerline channel within the *k*-th subband. Specifically, PSE_k equalizes the equivalent channel $f_k[n] \otimes h[n] \otimes g_k[n]$. In this paper, we employ the variable step-size normalized least mean square (VSS-NLMS) algorithm [24] to train the equalizer to alleviate the impact of IN samples on the training process. The weight updating recursion for PSE_k can be expressed as follows:

$$\mathbf{w}_k[n] = \mathbf{w}_k[n-1] + \mu_k[n] \frac{\mathbf{x}_k[n]e_k[n]}{\mathbf{x}_k^T[n]\mathbf{x}_k[n] + \delta_k},$$
(11)

where $\mathbf{w}_k[n] = \left[w_{(k,0)}[n], w_{(k,1)}[n], \dots, w_{(k,N_E-1)}[n]\right]^T$ denotes the weight vector with length N_E at time index n; $\mathbf{x}_k[n] = [x_k[n], x_k[n-1], \dots, x_k[n-N_E+1]]^T$ denotes the regressor vector; the superscript T denotes the transpose operator; and $e_k[n]$ is the error signal associated with the *k*-th PSE, which can be expressed as follows:

$$e_k[n] = d_k[n] - y_k[n]$$

= $d_k[n] - \mathbf{w}_k^T[n-1]\mathbf{x}_k[n].$ (12)

The desired signal $d_k[n]$ is the input of $f_k[n]$ at the IDWT block; δ_k is a small positive constant to prevent numerical errors during the weight updating process, and $\mu_k[n]$ is the variable step-size, which can be calculated as follows:

$$\mu_{k}[n] = \left| 1 - \frac{\sqrt{\left| \hat{\sigma}_{d,k}^{2}[n] - \hat{\sigma}_{y,k}^{2}[n] \right|}}{\hat{\sigma}_{e,k}[n] + \zeta_{k}} \right|,$$
(13)

where ζ_k is a small positive number; $\hat{\sigma}_{d,k}^2[n]$ is the estimated variance of the desired signal, which can be calculated using the following recursion:

$$\hat{\sigma}_{d,k}^2[n] = \lambda_k \hat{\sigma}_{d,k}^2[n-1] + (1-\lambda_k) d_k^2[n],$$
(14)

 $\hat{\sigma}_{y,k}^2[n]$ is the estimated variance of the output signal of the PSE_k, which can be calculated as follows:

$$\hat{\sigma}_{y,k}^2[n] = \lambda_k \hat{\sigma}_{y,k}^2[n-1] + (1-\lambda_k) y_k^2[n],$$
(15)

and $\hat{\sigma}_{e,k}^2[n]$ is the estimated variance of the error signal of the PSE_k, which can be calculated as follows:

$$\hat{\sigma}_{e,k}^2[n] = \lambda_k \hat{\sigma}_{e,k}^2[n-1] + (1-\lambda_k) e_k^2[n],$$
(16)

where $\lambda_k \in (0, 1)$ is the smoothing factor. After down-sampling by a factor of M, we obtain the output of the adaptive IDWT with respect to the *k*-th subchannel ($\hat{S}_k[n]$). This output is then de-mapped to $\hat{X}_k[n]$.



Figure 3. Details in an adaptive DWT block with *M* subchannels.

3.3. Iterative IN Reconstruction

Figure 4 details the proposed iterative IN reconstruction block. After re-mapping and pilot-insertion, we can estimate the input of the IDWT block on the transmitter side. Thus, if an estimate of the CIR $\hat{h}[n]$ is available, then it is possible for the IN to be re-generated as follows:

$$\hat{d}[n] = \tilde{r}[n] - \hat{s}[n] \otimes \hat{h}[n].$$
(17)

However, $\hat{s}[n]$ and $\hat{h}[n]$ are not exactly the same with s[n] and h[n], respectively. Thus, for the samples in $\hat{d}[n]$, only the samples with large amplitude are reliable estimation for the IN. This leads us to use a peak detector to enhance the reliability of the estimation process, such that only significant estimates of $\hat{d}[n]$ are retained. The operation involving the peak detector can be expressed as follows:

$$\hat{u}[n] = \begin{cases} \hat{d}[n], & \left| \hat{d}[n] \right|^2 > T_2[n] \\ 0, & \text{otherwise} \end{cases}$$
(18)

where $T_2[n] = 4.12 \cdot \mathbb{E}\left(\left|\hat{d}[n]\right|\right)$ is an adaptive threshold for the peak detector [22]. Channel estimation can be performed using the following recursions:

$$\hat{\mathbf{h}}[n] = \hat{\mathbf{h}}[n-1] + \mu_h[n] \frac{\mathbf{x}_h[n]e_h[n]}{\mathbf{x}_h^T[n]\mathbf{x}_h[n] + \delta_h},\tag{19}$$

where $\hat{\mathbf{h}}[n] = \left[\hat{h}_0[n], \hat{h}_1[n], \dots, \hat{h}_{N_h-1}[n]\right]^T$ denotes the weight vector with length N_h at time index n; $\mathbf{x}_h[n] = \left[\hat{s}[n], \hat{s}[n-1], \dots, \hat{s}[n-N_h+1]\right]^T$ denotes the input regressor vector; δ_h is a small positive constant used to prevent numerical errors during the weight updating process; and $e_h[n]$ is an error signal, which can be expressed as follows:

$$e_h[n] = \tilde{r}[n] - y_h[n] = \tilde{r}[n] - \hat{\mathbf{h}}^T[n-1]\mathbf{x}_h[n].$$
(20)

 $\mu_h[n]$ is the variable step-size, which can be calculated as follows [24]:

$$\mu_{h}[n] = \left| 1 - \frac{\sqrt{\left| \hat{\sigma}_{d,h}^{2}[n] - \hat{\sigma}_{y,h}^{2}[n] \right|}}{\hat{\sigma}_{e,h}[n] + \zeta_{h}} \right|,$$
(21)

where ζ_h is a small positive number; $\hat{\sigma}_{d,h}^2[n]$ is the estimated variance of the desired signal, which can be calculated using the following recursion:

$$\hat{\sigma}_{d,h}^2[n] = \lambda_h \hat{\sigma}_{d,h}^2[n-1] + (1-\lambda_h) \tilde{d}^2[n],$$
(22)

 $\partial_{y,h}^2[n]$ is the estimated variance of the output signal of the channel estimator which can be calculated as follows:

$$\hat{\sigma}_{y,h}^2[n] = \lambda_h \hat{\sigma}_{y,h}^2[n-1] + (1-\lambda_h) y_h^2[n],$$
(23)

and $\hat{\sigma}_{e,h}^2[n]$ is the estimated variance of the error signal of the channel estimator which can be calculated as follows:

$$\hat{\sigma}_{e,h}^2[n] = \lambda_h \hat{\sigma}_{e,h}^2[n-1] + (1-\lambda_h)e_h^2[n],$$
(24)

where $\lambda_h \in (0, 1)$ is the smoothing factor.

When the estimated IN $\hat{u}[n]$ becomes available, we are able to obtain a cleaner received $\hat{r}[n]$ by subtracting $\hat{u}[n]$ from $\tilde{r}[n]$, such that in the following iteration, $\tilde{r}[n]$ becomes less contaminated. As a result, the mis-adjustments imposed by the equalizers $\mathbf{w}_k[n]$ and channel estimator $\hat{\mathbf{h}}[n]$ are iteratively reduced. In cases where more iterations are unnecessary, the output of the de-mapper $\hat{X}_k[n]$ passes through a P/S block to produce the estimation of the information $\hat{x}[n]$ that has been transmitted.



Figure 4. Details of the iterative IN reconstruction block. CIR = channel impulse response.

3.4. Computational Complexity

The additional computational complexity incurred by the proposed IN mitigation algorithm mainly comes from the pre-IN mitigation block, the adaptive per-subchannel equalizer, and the iterative IN reconstruction block. We list these additional computational complexity as follows:

- 1. For the pre-IN mitigation block, the computational complexity is dominated by calculating Equations (7) and (9). To calculate the Kurtosis value $\rho[n]$, it requires 5 adders, 5 multipliers, and 4 dividers. To compute the adaptive threshold $T_1[n]$, it needs one adder, one multiplier, and one divider.
- 2. To train the coefficients of the PSE_k , the calculation of Equations (11)–(13) is necessary. The computation of the variable step-size needs 9 multiplier, one divider, 6 adders, and one square-root operation. The calculation of the error signal $e_k[n]$ requires N_E multipliers and N_E adders. The weight updating recursion needs $(2N_E + 1)$ multipliers, one divider, and $2N_E$ adders. Thus, for M per-subchannel equalizers, it totally requires $M \cdot (3N_E + 10)$ multipliers, $M \cdot (3N_E + 6)$ adders, 7M dividers, and M square-root operations.
- 3. In addition to one additional IDWT operation, the main computation burden in the iterative IN reconstruction block is resulted from the calculation of Equations (19)–(21). Assuming that the channel estimator is implemented by an FIR filter of length N_h , then the calculation of the recursion (19) needs $2 \cdot N_h + 1$ multipliers, one divider, and $2 \cdot N_h$ adders; the calculation

of Equation (20) needs N_h multipliers and N_h adders; the calculation of Equation (21) needs 9 multiplier, one divider, 6 adders, and one square-root operation. Thus, it requires about $(3N_h + 10)$ multiplier, 7 dividers, $(3N_h + 6)$ adders, and one square-root operation in total.

With the fixed number of subchannel M, the main computational cost is linear growth with respect to N_E . Thus, the additional computation complexity associated with our IN mitigation scheme is affordable.

4. Simulation Results

We evaluated the performance of the proposed IN mitigation algorithm by establishing transmitters for use in a wavelet-OFDM PLC system in accordance with IEEE 1901 specifications [11]. The frequency band was 1.8 MHz to 28 MHz, and the modulation was assumed to be 2-PAM. Table 1 lists the parameters of a four-path channel model [25] and the corresponding magnitude and impulse responses are illustrated in Figure 5.



Figure 5. (a) Magnitude and (b) impulse responses for the four-path channel model.

Using the BG IN model described in Equation (3), we considered two IN environments: (1) strongly-disturbed IN (p = 0.1 and $\Gamma = 0.01$) and (2) weakly-disturbed IN (p = 0.01 and $\Gamma = 0.1$). The performance metric was the BER performance. The effectiveness of the proposed iterative IN mitigation method is confirmed in Figure 6. In the strongly-disturbed case, the E_b/N_0 required to achieve BER of 10^{-2} were as follows: iteration 1 (36 dB), iteration 2 (32 dB), and iteration 3 (28 dB). However, in the weakly-disturbed case, the improvements in E_b/N_0 were marginal. Note that E_b/N_0 , which denotes the ration of energy per bit (E_b) to noise power spectral density (N_0), is an essential parameter in digital communication systems. It can be treated as a normalized signal-to-noise ratio (SNR) measurement, i.e., SNR per bit. Therefore, for a target BER, we prefer that the required E_b/N_0 is as small as possible; on the other hand, for a fixed value of E_b/N_0 , we favor that the resulting BER is as low as possible.



Figure 6. Performance of proposed algorithm with different iteration in (**a**) strongly-disturbed and (**b**) weakly-disturbed cases.

As shown in Figure 7, the conventional blanking method [28] (with blanking device threshold optimized empirically) resulted in poor BER performance. Specifically, only subtle improvements (in terms of the BER) were obtained when E_b/N_0 was below 24 dB. When E_b/N_0 exceeded 24 dB, no blanking was performed. In contrast, the proposed method with estimated channel impulse response $\hat{\mathbf{h}}[n]$ achieved BER performance on par with what can be obtained when the channel information is known exactly. Compared to the case with no IN, the loss in E_b/N_0 at BER = 10^{-3} were as follows: strongly-disturbed IN (2.5 dB) and weakly-disturbed IN (0.5 dB).



Figure 7. Performance comparison of the proposed algorithm (iteration = 3) with other related works in (**a**) strongly-disturbed and (**b**) weakly-disturbed cases.

Attenuation Parameters					
k = 1		$a_0 = 0$		$a_1 = 7.8 \times 10^{-10}$	
Path Parameters					
i	<i>8i</i>	d_i	i	<i>8i</i>	d_i
1	0.64	200	3	-0.15	244.8
2	0.38	222.4	4	0.05	267.5

Table 1. Parameters of the Four-path Model.

5. Conclusions

This paper proposes an enabling technology for BB-PLC-based AMI. Specifically, we present a robust iterative receiver to combat IN in wavelet-OFDM-based PLC systems. The proposed receiver iteratively eliminates IN in two stages. In the first stage, a pre-IN block is used to eliminate the most prominent portions of the IN source. The second stage employs an adaptive IDWT block, which serves as an analytical filter bank and adaptively equalizes each subchannel, and an IN reconstruction block. Note that the IN reconstruction block iteratively estimates samples with pronounced residual IN that were not detected by the pre-IN block in the first stage. Simulation results confirmed that the proposed scheme is able to reduce the impact of IN on BER performance, bringing it down to an acceptable level. Compared to the case with no IN, the E_b/N_0 loss at BER = 10^{-3} were as follows: strongly-disturbed IN (2.5 dB) and weakly-disturbed IN (0.5 dB).

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