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Integrated Full-Frequency Impedance Modeling and Stability Analysis of the Train-Network Power Supply System for High-Speed Railways

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Abstract: To investigate the harmonic resonance, harmonic instability and low-frequency oscillation phenomena in high-speed railways, this paper proposes a full-frequency impedance model of the train-traction network system (simplified as 'train-network') based on *d-q* coordinates system. Compared with traditional models which deal with only the grid-side converter, the proposed model also includes its load models—the inverter and the traction motor. It also reflects complete control scheme of grid-side converter, which makes it easier to analyze unstable phenomena mentioned above. Moreover, this paper improves the impedance modeling of the network by taking the network impedance and admittance into detailed consideration. In addition, based on the proposed train-network model, the 3D figure and zero-pole diagram are also presented for the analysis of the stability of the integral system. Simulation and experiment results verify the accuracy of the model.

Keywords: harmonic resonance; harmonic instability; low-frequency oscillation; full-frequency impedance modeling; train- network power supply system

1. Introduction

Being developed rapidly, more and more high-speed trains (HSTs) have been put into service in China. According to reports, more than 1600 HSTs are now being used for passenger service [1]. Meanwhile, the power systems of high-speed railways face significant challenges, including the harmonic resonance, harmonic instability and low-frequency oscillation (LFO) phenomena noticed in recent years, which can hinder the normal operations of the trains. Based on the previous literature, these unstable phenomena in high-speed railway system can be summarized as follows:

• Harmonic Resonance

The frequency of harmonic resonance usually ranges from a few hundred Hertz to kilohertz. During the train operation, high-frequency harmonic components are generated because of the pulse-width modulation (PWM) switching action of the grid-side converter on the train. Such high-frequency components act as harmonic sources and then inject harmonics into the traction network. When the train is running, and if the harmonic frequency emitted by the grid-side converter in the train is in the range of the intrinsic frequency of the train-network system, the high-frequency harmonics will be amplified by resonance, hence harmonic resonance is initiated. Such amplified resonant currents and voltages always lead to various problems such as interference to adjacent railway communication lines, malfunction or miss-function of protective devices and even facility



failures [2]. For example, in the year of 2011, harmonic resonance occurred on CRH380A-6041L and CRH380B-6042L in Beijing-Shanghai high-speed railway line, resulting in the failure of several substations and nearby trains. Moreover, if the amplified resonant currents and voltages are not large enough to trigger protection or do harm to the hardware of the system, this kind of resonance can become stable and continuous in the passive network, and in an active train-network system, this kind of stable resonance will usually cause harmonic instability [3].

Harmonic Instability

The harmonic instability frequency is usually a few hundred to kilohertz. There is a feedback loop between the grid-side converter and the traction network. When there is harmonic resonance in the train-network system, it easy causes harmonic instability because of the small damping of the system [4]. Moreover, if various controller parameters (voltage loop controller parameters, current loop controller parameters and phase-locked controller parameters) in the grid-side converter not match the parameters of the grid, harmonic resonance can also lead to harmonic instability. From the view of mathematical model, when there are unstable high-frequency poles in the closed-loop system of the train-network system, the harmonic resonance will be further stimulated, resulting in harmonic instability [5]. For example, in 1995, harmonic instability occurred in Switzerland, affecting the normal operation of a high-speed railway [6]. A similar situation also happened in Chengdu, China.

• Low-Frequency Oscillation (LFO)

The frequency of LFO is usually 1 Hertz to several Hertz. The LFO is truly a power oscillation [5]. It mostly occurs when trains are in the rail depot with the pantographs rised, while the trains are just prepared for running [7]. In this case, the loads to grid-side converter are only the auxiliary inverter and the loads after it, such as air conditioners and the lighting system. As the number of trains is increased, the overall impedance of the train decreases, which results in mismatching between the impedance of the train and the network and leads to LFO. When LFO occurs, the damping of the system is very small, and even negative. Under the worst condition, the protection of the grid-side converter will be triggered due to the large voltage oscillation in the traction network, which results in traction blockade and makes the train lose traction [8]. For example, in 2011, LFO happened in the Beijing South Station, when the trains were in depot preparing for operation, which lead to traction blockade and therefore the train could not be leave in time [9].

In order to analyze these instability phenomena, the establishment of an appropriate train-network model has become a top priority. At present, according to frequency, the modeling method can be divided into two categories: the first one is high-order harmonic load model [10]. This model can reflect the high-frequency characteristics of trains using Fourier to analyze the characteristic of PWM generated by the grid-side converter. The second one is the small-signal impedance modeling method [11–13] focusing on certain frequency ranges limited to specific oscillation phenomena. Is there a model that can be applied both to high frequency and low frequency? Harnefors proposed a full-frequency impedance modeling method in grid-connected voltage source converter [14], which opens a door to the model of grid-side converter in HST. Then Danielsen analyzed the grid-side converter control structure, and established a simplified impedance model for the phase-locked loop, voltage controller, and current controller, which laid a foundation for subsequent research [15]. Reference [16] established a full-frequency impedance-based model of the grid-side converter in HST and provided reference for subsequent model researches, but this modeling method ignores the model of second order generalized integrator (SOGI). SOGI is mainly composed of bandpass and low-pass filters, which can suppress harmonics and make the system more stable [17]. Reference [18] was based on [16], but it did not consider the impedance model of the grid-side converter load. The model is not complete and it cannot reflect the HSTs operating conditions.

This paper further studies the train-network impedance in full frequency. The layout of this paper is as follows: in Section 2, a detailed impedance modeling method which considers the modeling of

grid-side converter, inverter, traction motor and the network is introduced. In Section 3, the stability of train-network system is analyzed based on a zero-pole diagram and 3D figures. In Section 4, a Matlab (Software version: Matlab 2013b, developer: USA) simulation and experimental results are used to verify the accuracy of the overall model. Finally, the whole contents of this paper are summarized in Section 5.

2. Integrated Full-Frequency Impedance Model of Train-Network

2.1. Input Impedance Model of Train

Traction converter is the main part of HSTs, which including grid-side converter, inverter and traction motor. Figure 1 shows the structure of a traction converter.



Figure 1. Structure figure of a traction converter.

In Figure 1, the catenary voltage is 25 kV, and the second-side voltage of traction transformer is e_s . The leakage inductance and resistance of traction transformer are *L* and *R*, respectively. The grid-side current is i_s . The grid-side converter is a four-quadrant converter (4QC), which converts between AC and DC. The bridge arm voltage is u_{ab} . The switching components Q_1-Q_4 are four IGBTs (Insulated Gate Bipolar Transistor). The secondary resonant circuit is composed of inductance L_r and capacitor C_r . The DC side supporting capacitor is C_d , and the DC voltage is U_{dc} . Inverter and traction motor are connected on the DC side, which are the load of grid-side converter. According to Figure 1, the train model can be divided into two parts: a gird-side converter impedance model, and its load-inverter and a traction motor impedance model.

2.1.1. Impedance Model of the Grid-Side Converter

In order to make the grid-side converter impedance model more accurate, the modeling process is divided into several parts: Voltage Synchronization System (VSS), Current Synchronization System (CSS), AC Current Controller (ACC) and DC Voltage Controller (DVC). The whole control structure is shown in Figure 2.



Figure 2. Controller of network-side converter.

In the following sections, the impedance models of VSS, CSS, ACC, and DVC are established in turns.

A. Model of Voltage Synchronization System (VSS)

The structure of VSS is shown in Figure 3 [11]:



Figure 3. The structure of VSS.

In Figure 3, *e* is the transformer secondary side voltage. The superscript *c* represents the control quantity. The gain of SOGI is K_{eSOGI} . The angular frequency of the power frequency is ω_0 . The synchro angle generated by the phase-locked loop (PLL) is θ . From the quantities introduced above, the following equation can be formed:

$$\underbrace{\begin{bmatrix} e_{\alpha}^{c} \\ e_{\beta}^{c} \end{bmatrix}}_{e_{\alpha\beta}^{c}} = \underbrace{\begin{bmatrix} H_{e} & 0 \\ 0 & H_{e} \end{bmatrix}}_{H_{e}} \underbrace{\begin{bmatrix} e_{\alpha} \\ e_{\beta} \end{bmatrix}}_{e_{\alpha\beta}}$$
(1)

$$H_e(s) = \frac{K_{eSOGI}\omega_0 s}{s^2 + K_{eSOGI}\omega_0 s + \omega_0^2}$$
(2)

After Park transformation of $e^c_{\alpha\beta'} e^c_{dq}$ can be obtained [11]:

$$e_{dq}^{c} = P_{\theta} H_{e} P_{\theta_{0}}^{-1} e_{dq} \approx P_{\Delta \theta} H_{edq} e_{dq}$$
(3)

In Equation (3), $P_{\Delta\theta} = \begin{bmatrix} 1 & \Delta\theta \\ -\Delta\theta & 1 \end{bmatrix}$. Because $\Delta\theta$ is quite small, H_{edq} is equal to $P_{\theta_0}H_eP_{\theta_0}^{-1}$ [11]. The form of H_{edq} is as follows:

$$\boldsymbol{H}_{edq} = \begin{bmatrix} H_{edq} & 0\\ 0 & H_{edq} \end{bmatrix}$$
(4)

According to [15]:

$$H_{edq} = \frac{1}{\frac{1}{K_{eSOGI} \cdot (\frac{1}{\omega_0} + \frac{T_0}{8})s + 1}}$$
(5)

From the Equations (3)–(5) and a small signal transformation, we can get:

$$\begin{cases} \Delta e_d^c = H_{edq} \Delta e_d + \Delta \theta H_{edq} e_{q0} \\ \Delta e_q^c = -H_{edq} e_{d0} \Delta \theta + H_{edq} \Delta e_q \end{cases}$$
(6)

Because of PLL, the form of $\Delta \omega$ is as follows:

$$\Delta \omega = s \Delta \theta = (K_{pPLL} + \frac{K_{iPLL}}{s}) \Delta e_q^c = K_{PLL} \Delta e_q^c$$
⁽⁷⁾

From Equations (6) and (7), the form of $\Delta\theta$ can be obtained:

$$\Delta \theta = \underbrace{\frac{K_{PLL}H_{edq}}{s + e_{d0}K_{PLL}H_{edq}}}_{G_{PLL}}\Delta e_q \tag{8}$$

Substituting (8) into (6), the relation between Δe^c and Δe is in Equation (9):

$$\underbrace{\begin{bmatrix} \Delta e_d^c \\ \Delta e_q^c \end{bmatrix}}_{\Delta e^c} = \underbrace{\begin{bmatrix} H_{edq} & G_{PLL}H_{edq}e_{q0} \\ 0 & H_{edq} - H_{edq}e_{d0}G_{PLL} \end{bmatrix}}_{G_{eVSS}} \underbrace{\begin{bmatrix} \Delta e_d \\ \Delta e_q \end{bmatrix}}_{\Delta e}$$
(9)

B. Model of Current Synchronization System (CSS)

According to VSS, the equations of CSS can be obtained easily:

$$i_{dq}^c \approx P_{\Delta\theta} H_{idq} i_{dq}$$
 (10)

Combining (8) and (10):

$$\begin{bmatrix}
\Delta i_d^c \\
\Delta i_q^c
\end{bmatrix} = \underbrace{\begin{bmatrix}
H_{idq} & 0 \\
0 & H_{idq}
\end{bmatrix}}_{H_{idq}} \underbrace{\begin{bmatrix}
\Delta i_d \\
\Delta i_q
\end{bmatrix}}_{H_{idq}} - \underbrace{\begin{bmatrix}
0 & -i_{q0}H_{idq}G_{PLL} \\
0 & i_{d0}H_{idq}G_{PLL}
\end{bmatrix}}_{G_{iCSS}} \underbrace{\begin{bmatrix}
\Delta e_d \\
\Delta e_q
\end{bmatrix}}_{\Delta e}$$
(11)

C. Model of AC Current Controller (ACC)

The structure of ACC is shown in Figure 4.



Figure 4. The structure of ACC.

In Figure 4, the subscript *ref* represents a reference value. The grid-side current is *i*, and the bridge arm voltage in grid-side converter is *v*. The actual value of modulated wave in *d*-*q* axis is v_d^c and v_q^c . The following matrix can be obtained through Figure 1:

$$\begin{bmatrix} v_d^c \\ v_q^c \end{bmatrix} = \begin{bmatrix} e_d^c \\ e_q^c \end{bmatrix} - \begin{bmatrix} R+sL & -\omega_0L \\ \omega_0L & R+sL \end{bmatrix} \begin{bmatrix} i_d^c \\ i_q^c \end{bmatrix}$$
(12)

After current loop decoupling, the *d-q* reference value of modulation wave can be obtained:

$$\begin{bmatrix} v_{dref} \\ v_{qref} \end{bmatrix} = \begin{bmatrix} e_d^c \\ e_q^c \end{bmatrix} - K_{ACC} \begin{bmatrix} i_{dref} - i_d^c \\ i_{qref} - i_q^c \end{bmatrix} - \begin{bmatrix} 0 & -\omega_0 L \\ \omega_0 L & 0 \end{bmatrix} \begin{bmatrix} i_d^c \\ i_q^c \end{bmatrix}$$
(13)

In this equation, $K_{ACC} = K_{ip} + \frac{K_{ii}}{s}$, which is the PI of current loop.

The d-q axis reference value of the modulated wave is delayed, and then, the actual value of modulated wave in d-q axis can be obtained:

$$\begin{bmatrix} v_d^c \\ v_q^c \end{bmatrix} = G_d \begin{bmatrix} v_{dref} \\ v_{qref} \end{bmatrix}$$
(14)

In Equation (14), $G_d = \frac{1}{T_d s + 1}$, which is the delay part [16]. From Equations (12)–(14):

$$\Delta i^c = G_{idqref} \Delta i_{ref} + G^c_{edq} \Delta e^c \tag{15}$$

where G_{idqref} , Δi_{ref} , and G_{edq}^{c} can be expressed as:

$$\begin{bmatrix}
G_{idqref} = (G_{dq}^{c})^{-1}G_{d}K_{ACC} \\
\Delta i_{ref} = \begin{bmatrix}
\Delta i_{dref} \\
\Delta i_{qref}
\end{bmatrix} \\
G_{edq}^{c} = (G_{dq}^{c})^{-1}(1 - G_{d}) \\
G_{dq}^{c} = \begin{bmatrix}
G_{d}K_{ACC} + R + sL & \omega_{0}L(G_{d} - 1) \\
-\omega_{0}L(G_{d} - 1) & G_{d}K_{ACC} + R + sL
\end{bmatrix}$$
(16)

Combining (9), (11) and (15):

$$\Delta i = \underbrace{(H_{idq})^{-1} G_{idqref}}_{G_i} \Delta i_{ref} + \underbrace{(H_{idq})^{-1} (G_{iCSS} + G_{edq}^c G_{eVSS})}_{G_e} \Delta e \tag{17}$$

The equation of Δi is as follows:

$$\Delta i = G_i \Delta i_{ref} + G_e \Delta e \tag{18}$$

D. Model of DC Voltage Controller (DVC)

The purpose of modeling voltage loop is to find the relation of Δi_{ref} and Δe . With PI control, the *d*-axis reference i_{dref} in small signal form is in Equation (19):

$$\Delta i_{dref} = -K_{VCC}(s)\Delta v_{dc} \tag{19}$$

where $K_{VCC}(s) = K_{vp} + \frac{K_{vi}}{s}$ is the PI of voltage loop.

The instantaneous active and reactive powers flowing into 4QC are, respectively, given by [14]:

$$P = \operatorname{Re}\{\boldsymbol{e}\boldsymbol{i}^*\} = \underbrace{\underline{e_0}i_{d0}}_{P_0} + \underbrace{\underline{e_0}\Delta i_d + \Delta e_d i_{d0} + \Delta e_q i_{q0}}_{\Delta P}$$
(20)

$$Q = \operatorname{Im}\{ei^*\} = \underbrace{-e_0 i_{q0}}_{Q_0} - \underbrace{e_0 \Delta i_q - \Delta e_d i_{q0} + \Delta e_q i_{d0}}_{\Delta Q}$$
(21)

Set the total power *P*, and the load power of 4QC is P_L :

$$P - P_L = \frac{1}{2} C_{dc} \frac{dv_{dc}^2}{dt} \Rightarrow s C_{dc} v_{dc0} \Delta v_{dc} = \Delta P - \Delta P_L$$
⁽²²⁾

where $\Delta P_L = \Delta v_{dc} i_L + v_{dc} \Delta i_L$, and $i_L = \frac{v_{dc}}{Z_L}$. Substituting ΔP in (20) into (22):

$$\Delta P_L = \Delta v_{dc} \frac{v_{dc0}}{Z_L} + v_{dc0} \frac{\Delta v_{dc}}{Z_L} = \frac{2v_{dc0}}{Z_L} \Delta v_{dc}$$
(23)

Combining (18), (20), (21) and (23), Δv_{dc} can be calculated:

$$\Delta v_{dc} = \underbrace{\frac{e_0 G_{e(1,1)} + \frac{P_0}{e_0}}{SC_{dc} v_{dc0} + \frac{2v_{dc0}}{Z_L} + e_0 G_{i(1,1)} K_{vcc}}}_{M_d} \cdot \Delta e_d + \underbrace{\frac{e_0 G_{e(1,2)} - \frac{Q_0}{e_0}}{SC_{dc} v_{dc0} + \frac{2v_{dc0}}{Z_L} + e_0 G_{i(1,1)} K_{vcc}}}_{M_q} \cdot \Delta e_q \tag{24}$$

Substituting (24) into (19), and set $\Delta i_{qref} = 0$, Δi_{ref} can be obtained:

$$\begin{bmatrix} \Delta i_{dref} \\ \Delta i_{qref} \end{bmatrix} = \underbrace{\begin{bmatrix} -K_{vcc} \mathbf{M}_d & -K_{vcc} \mathbf{M}_q \\ 0 & 0 \end{bmatrix}}_{G_{ref}} \begin{bmatrix} \Delta e_d \\ \Delta e_q \end{bmatrix}$$
(25)

Combining (18) and (25), the admittance of train is as follows:

$$Y_{train} = G_i G_{ref} + G_e \tag{26}$$

2.1.2. Impedance Model of Inverter and Traction Motor

Firstly, the equivalent circuit of inverter and traction motor is shown in Figure 5.



Figure 5. Equivalent circuit of inverter and traction motor.

The state equation of the inverter in the *d-q* coordinate system is given by:

$$\frac{d}{dt} \begin{bmatrix} i_d \\ i_q \end{bmatrix} = \frac{1}{L_m} \begin{bmatrix} v_d \\ v_q \end{bmatrix} - \begin{bmatrix} 0 & -\omega_e \\ \omega_e & 0 \end{bmatrix} \begin{bmatrix} i_d \\ i_q \end{bmatrix} - \frac{R_m}{L_m} \begin{bmatrix} i_d \\ i_q \end{bmatrix}$$
(27)

The small signal model is used for modeling inverter and the traction motor. The small signal equation on DC side is given by [19]:

$$\Delta i_L = D_d \Delta i_d + I_d \Delta d_d + D_q \Delta i_q + I_q \Delta d_q \tag{28}$$

In *d*-*q* axis, the duty ratios in steady state are D_d and D_q . The small signals of the duty ratio are Δd_d and Δd_q . The stable currents are i_d and i_q . The small signals of currents are Δi_d and Δi_q .

The equation of small signal in AC side is:

$$\begin{cases} \Delta d_d v_{dc} + D_d \Delta v_{dc} = \Delta i_d (sL_m + R_m) - \omega_e L_m \Delta i_q \\ \Delta d_q v_{dc} + D_q \Delta v_{dc} = \Delta i_q (sL_m + R_m) + \omega_e L_m \Delta i_d \end{cases}$$
(29)

The next step is to get inverter open-loop input impedance, which is also the input impedance of inverter during stable operation. Set $\Delta d_d = \Delta d_q = 0$ [19], and the following equations can be obtained:

$$\begin{cases} \Delta i_d = \frac{\omega_e L_m D_q + D_d (sL_m + R_m)}{(sL_m + R_m)^2 + \omega_e^2 L_m^2} \Delta v_{dc} \\ \Delta i_q = \frac{D_q (sL_m + R_m) - \omega_e L_m D_d}{(sL_m + R_m)^2 + \omega_e^2 L_m^2} \Delta v_{dc} \end{cases}$$
(30)

Combining (28) and (30), the input impedance of inverter and traction motor is:

$$Z_{L} = \frac{\Delta v_{dc}}{\Delta i_{L}} = \frac{(sL_{m} + R_{m})^{2} + \omega_{e}^{2}L_{m}^{2}}{(D_{d}^{2} + D_{d}^{2})(sL_{m} + R_{m})}$$
(31)

The input impedance model of train can be obtained by substituting (31) into (24). In order to make the whole calculation process more distinct, and distinguish the control variables and the actual variables more clearly, the structure of all calculations is shown in Figure 6:



Figure 6. The structure of all calculations.

Since there is no frequency limit in the model process of VSS, CSS, ACC, DVC and the load, this modeling method is applicable to the full frequency range.

2.2. Output Impedance Model of Traction Network

The main difficulty of traction network modeling is to model the *d-q* coupling impedance. Here the modeling process is shown in detail. According to the uniform transmission line theory, the voltage and current of terminal can be calculated:

$$\begin{cases} u = u_1 \cosh(\gamma x) - Z_c i_1 \sinh(\gamma x) \\ i = i_1 \cosh(\gamma x) - \frac{u_1}{Z_c} \sinh(\gamma x) \end{cases}$$
(32)

where γ is the propagation constant. The characteristic impedance is Z_c . Both of them are complex numbers. The beginning voltage and current are u_1 and i_1 .

The impedance model of network is shown in Figure 7 [20]:



Figure 7. Impedance model of traction network.

In Figure 7, the substation is abbreviated as SS, and the section post is abbreviated as SP. The equivalent impedance of a substation is Z_{ss} . The equivalent impedance and admittance in left side and right side of the train are Z_{T1} , Y_{T1} and Z_{T2} , Y_{T2} respectively. The total length and impedance between SS and train are l_1 and Z_1 . The total length and impedance between train and SP are l_2 and Z_2 . And the impedance of right-side of the train can be calculated:

$$\begin{cases} Z_{T2} = \frac{Z_c [\cosh(\gamma l_2) - 1]}{\sinh(\gamma l_2)} \\ Y_{T2} = \frac{\sinh(\gamma l_2)}{Z_c} \end{cases}$$
(33)

The next step is to make the model more concrete by showing every component, and calculate the coupling impedance of the network. The equivalent circuit of traction network diagram is shown in Figure 8:



Figure 8. Equivalent circuit of traction network.

In Figure 8, the resistance R_1 and inductance L_1 form the equivalent impedance Z_{T1} in Figure 7. The capacitance C_1 and conductance G_1 form the equivalent admittance Y_{T1} . It is similar with the components in the right side of the train. Voltage u_1 and current i_1 are generated by train. Voltage u is the terminal port voltage.

Based on Kirchhoff's theorem, voltage and current equation in right side of the circuit is given by:

$$i_1 R_2 + L_2 \frac{di_1}{dt} + i_1 \frac{1}{G_2} + \frac{1}{C_2} \int i_1 dt = u_1$$
(34)

Combining (32) and (34), and make it in *s* domain:

$$i_1 R_2 + i_1 L_2 s + u_1 \cosh(\gamma l_2) - Z_c i_1 \sinh(\gamma l_2) = u_1$$
(35)

The current i_1 and voltage u_1 can be written in *d*-*q* form:

$$\begin{cases} i_1 = i_d \sin \omega t + i_q \cos \omega t \\ u_1 = u_d \sin \omega t + u_q \cos \omega t \end{cases}$$
(36)

Combining (35) and (36), and make the sine and cosine part in left side equal to sine and cosine part in right side respectively. The following matrix is obtained:

$$\begin{bmatrix} u_d \\ u_q \end{bmatrix} = \begin{bmatrix} \frac{R_2 + sL_2 - Z_c \sinh(\gamma l_2)}{1 - \cosh(\gamma l_2)} & -\frac{L_2 \omega_0}{1 - \cosh(\gamma l_2)} \\ \frac{L_2 \omega_0}{1 - \cosh(\gamma l_2)} & \frac{R_2 + sL_2 - Z_c \sinh(\gamma l_2)}{1 - \cosh(\gamma l_2)} \end{bmatrix} \begin{bmatrix} i_d \\ i_q \end{bmatrix}$$
(37)

where ω_0 is the angular frequency of network.

What needs to be clear here is that R_2 , G_2 , C_2 , L_2 are related to Z_{T2} and Y_{T2} :

$$\begin{cases} Z_{T2} = R_2 + j\omega L_2 = \frac{Z_c[\cosh(\gamma l_2) - 1]}{\sinh(\gamma l_2)}\\ Y_{T2} = G_2 + j\omega C_2 = \frac{\sinh(\gamma l_2)}{Z_c} \end{cases}$$
(38)

The aforementioned γ and Z_c are both complex numbers, so they can be expressed as:

$$\begin{cases} Z_c = \sqrt{\frac{Z_0}{Y_0}} = \sqrt{\frac{R_0 + j\omega L_0}{G_0 + j\omega C_0}} = m + jn\\ \gamma = \sqrt{Z_0 Y_0} = \sqrt{(R_0 + j\omega L_0)(G_0 + j\omega C_0)} = \alpha + j\beta \end{cases}$$
(39)

Combining (38) and (39), the R_2 , G_2 , C_2 , L_2 can be obtained.

$$\begin{cases} R_{2} = \frac{1}{C^{2} + D^{2}} \cdot \{m[C(A - 2) + BD] - n[BC - D(A - 2)]\} \\ G_{2} = \frac{1}{2(m^{2} + n^{2})} \cdot [m(e^{\alpha l_{2}} \cos \beta l_{2} - e^{-\alpha l_{2}} \cos \beta l_{2}) + n(e^{\alpha l_{2}} \sin \beta l_{2} + e^{-\alpha l_{2}} \sin \beta l_{2})] \\ C_{2} = \frac{1}{2\omega(m^{2} + n^{2})} \cdot [m(e^{\alpha l_{2}} \sin \beta l_{2} + e^{-\alpha l_{2}} \sin \beta l_{2}) - n(e^{\alpha l_{2}} \cos \beta l_{2} - e^{-\alpha l_{2}} \cos \beta l_{2})] \\ L_{2} = \frac{1}{\omega(C^{2} + D^{2})} \cdot \{m[BC - D(A - 2)] + n[C(A - 2) + BD]\} \end{cases}$$
(40)

where:

$$\begin{cases} e^{\alpha l_2} \cos \beta l_2 + e^{-\alpha l_2} \cos \beta l_2 = A\\ e^{\alpha l_2} \sin \beta l_2 - e^{-\alpha l_2} \sin \beta l_2 = B\\ e^{\alpha l_2} \cos \beta l_2 - e^{-\alpha l_2} \cos \beta l_2 = C\\ e^{\alpha l_2} \sin \beta l_2 + e^{-\alpha l_2} \sin \beta l_2 = D \end{cases}$$
(41)

Combining (37) and (40), the matrix of network impedance on right side of the train can be obtained, which is similar to the left side.

3. Stability Analysis of the Overall Power System

In the following part, the system stability will be analyzed in the full frequency domain using the impedance model proposed in this paper. First, the concept of total train-network input impedance is introduced to analyze the resonance frequency [21]. Figure 9 shows the coupling analysis model of the train-network system. There is usually more than one power unit in a high-speed train, so here the letter *n* represents the number of power units. The total train-network input impedance Z_{intk} is composed of n - 1 power unit impedances $Z_{trk}/(n - 1)$ and the network impedance Z_{ingk} in parallel.



Figure 9. Train-network system coupling analysis model.

First, calculate *Z*_{*intk*}, where *k* denotes the *k*-order harmonics:

$$Z_{intk} = \frac{Z_{ingk} \cdot \frac{Z_{trk}}{n-1}}{Z_{ingk} + \frac{Z_{trk}}{n-1}} = \frac{Z_{ingk}}{I + (n-1)Y_{trk}Z_{ingk}}$$
(42)

According to Equation (42), the relationship between total train-network input impedance, frequency and the length of catenary can be obtained and shown in Figure 10:



Figure 10. Relationship between total train-network input impedance, frequency and the length of catenary.

It can be seen from the figure that the train-network input impedances of d-d, d-q, q-d and q-q axes are slightly different. This figure can reflect the oscillation frequency of the train-network with a certain frequency range, because the frequency corresponding to the peak of total train-network input impedance is the system parallel resonance frequency [21]. From the figure, it is also obvious that the input impedances of train-network of all axis still follow the rule—the greater the length of catenary, the lower the oscillation frequency [5], which means as the length of the catenary increases, the local train-network oscillation frequency shifts to the left on the frequency axis.

In the following part, we will analyze the system stability from another perspective—closed-loop transfer function. The train-network impedance model is simplified in Figure 11:



Figure 11. Simplified model of train-network.

Here e_s and Z_s are the voltage and the output impedance of network on the secondary side of the traction transformer, respectively; the input current of the train is i_s , and the input impedance of the train is Z_{tr} .

In references [18] and [22], the equation of closed-loop transfer function of the system is given by:

$$H_{s}(s) = \frac{i_{s}(s)}{e_{s}(s)} = \frac{1}{Z_{s}(s) + \frac{Z_{tr}(s)}{n}}$$

$$= \frac{nY_{tr}(s)}{I + nY_{tr}(s) \cdot Z_{e}(s)}$$
(43)

where *n* is the number of converters. The pole figure of the closed-loop transfer function is used to determine the stability of the system by observing whether there is any pole in the right half-plane. The closed-loop transfer function in the form of matrix also has poles. Known from the Smith-Mcmillan Form, the pole of a matrix G(s) is the roots of the denominator in diagonal of M(s), which is the standard form of Smith-Mcmillan Form [23]. Therefore, the pole figure of the closed-loop transfer function could be obtained.

Figure 12 shows the pole figure of the closed-loop system while the parameter K_{p_vcc} increases. Different colors of poles indicate different K_{p_vcc} values in $Y_{tr}(s)$. When $K_{p_vcc} = 1.5$ and 2.5, the poles are all in left half-plane, which means the system is stable. With K_{p_vcc} increasing, the poles of the system shift to the right plane. When $K_{p_vcc} = 5.5$ and 8, there are poles in right half-plane, which means the system become unstable. This figure shows that with K_{p_vcc} increasing, the system becomes unstable gradually.



Figure 12. Pole figure of train-network.

4. Simulation and Experiment Result

4.1. Setup of Simulation and Experiment

In order to compare the train impedance model established in this paper with that of existing papers, and verify the proposed model, the following simulations are performed. And in order to verify the characteristic of this model in some degree, the experiment is performed. The parameters of simulation and experiment are shown in Table 1.

Symbols	Note	Simulation Value	Experiment Value
ed_0	<i>d</i> -channel steady state voltage	2000 V	2100 V
eq_0	<i>q</i> -channel steady state voltage	0 V	0 V
P_0	Active power	360 kW	360 kW
Q_0	Reactive power	0 kW	0 kW
L	Leakage inductance of transformer	2.8 mH	2.8 mH
R	Leakage resistance of transformer	0.2 Ω	$0.35 \ \Omega$
T_d	Time delay	1.2 μs	1.2 μs
K_{p_acc}	Current loop proportional gain	3	4.5
K _{i_acc}	Current loop integral gain	0.8	0.8
K_{p_vcc}	Voltage loop proportional gain	1.5	1.5
K_{i_vcc}	Voltage loop integral gain	1.4	1.5
K_{p_PLL}	PLL-Proportional gain	5.5	6
K_{i_PLL}	PLL-Integral gain	97	50
<i>K_{eSOGI}</i>	SOGI-voltage gain	0.8	0.8
K_{iSOGI}	SOGI-current gain	1	1
C_{dc}	Capacitance in DC side	16 mF	16 mF
v_{dc0}	DC voltage	1500 V	1500 V
L_m	Inductance of motor	43.8 mH	43.8 mH
R_m	Resistance of motor	0.223 Ω	0.223 Ω
w_e	Rotating speed	1000 rad/s	1000 rad/s
п	<i>n</i> modules	2	2
R_0	Unit length residence of network	$1.33 \text{ m}\Omega/\text{km}$	-
L_0	Unit length inductance of network	0.21 mH/km	-
G_0	Unit length conductance of network	2 S/km	-
C_0	Unit length capacitance of network	0.35 mF/km	-
1	Length of catenary	15 km	-

Table 1. Simulation and experiment parameters.

4.2. Simulation and Experiment Analysis

In this part, the calculated model proposed in this paper will be verified first. Referring to [11,24,25], the impedance measuring method for single-phase systems is used here, which is shown in Figure 13. In this figure, u_p is the perturbation injection. The voltage u_n and current i_s are regarded as the components in the α axis. At the very beginning of the verification experiment, the simulated grid-side converter will be operating in a steady state, and then u_p is added in this circuit. The voltage and current in *d*-*q* axis can be calculated, and the Z_{dq} can be obtained. The measured admittances are shown in Figure 14.



Figure 13. The impedance measuring method model.



Figure 14. Admittance Bode figure of train.

Figure 14 is the Bode diagram of train admittance in the *d-d*, *d-q*, *q-d* and *q-q* axes. Four conditions are shown in this figure. They are with SOGI and load model (proposed in this paper), with SOGI but without load model, without both SOGI and load model, and measured admittance. This figure shows that the measured admittance is consistence with the blue line (the model proposed in this paper). There are only some slight deviations in high-frequency. The calculated load model should be study further to make it more precise. It can also be seen from the figure that SOGI mainly affects the high-frequency part of *d-d*, *q-d* and *q-q* axes admittance. The load model mainly affects the high-frequency part of *d-d*, *q-d* and *q-q* axis admittance. It has a certain influence on the full frequency band of *d-q* axis admittance.

Figure 12 shows that the increasing of K_{p_vcc} makes the system unstable. In the following part, this conclusion will be verified by simulation and experiment. First, establish a train-network united simulation and measure the grid-side voltage e_s , grid-side current i_{s1} and i_{s2} , which are shown in Figure 15.



Figure 15. Simulation results about the influence of k_{p_vcc} increasing on the system.

Figure 15 shows the simulation results about the influence of k_{p_vcc} increasing on grid-side voltage e_s and grid-side currents i_{s1} and i_{s2} . In this figure, before 0.7 s the control parameter of 4QC voltage loop k_{p_vcc} takes the value of 1.5, and the system remains stable. At 0.7 s, k_{p_vcc} is changed to 5.5, and the grid-side voltage e_s starts to oscillate, and the grid-side currents i_{s1} and i_{s2} also begin to oscillate sharply and appear divergent. So this model can reflect the impact of parameters on the system stability. The next step is to verify whether this change is correct by experiment.

In order to simulate the actual 25 kV traction network in the laboratory, the two phases of the three-phase 380 V AC are taken and converted to 25 kV via a step-up transformer to supply the entire system. In order to make the grid-side converter (GC) work under load, it powers two inverters (INV) and the traction motors work in drag mode. The simplified schematic diagram of experimental operation is shown in Figure 16. It should be noted that there are two traction converters put into use, so the value of DC capacitor is doubled, which helps to reduce harmonics generated by two inverters. In addition, there is a *LC* resonance circuit in DC side, which reduces the 2nd order harmonic effectively, and the 2nd order is the main content in DC side if there is no such *LC* resonance circuit. Due to the reasons above, we ignore the interactions of the two inverters.



Figure 16. Simplified schematic diagram of experimental operation.

The experimental platform is shown in Figure 17. There are two IGBT modules in one grid-side converter, so it contains eight IGBTs. There is only one IGBT module in one inverter, which includes nine IGBTs. All these IGBT modules are in the back of one traction converter. All control parts (including grid-side converter controller, inverter controllers) are in the front of one traction converter. It should be noted that there are two inverters used in this experiment, so there are two traction converters paralleled in DC side. In one traction converter, both grid-side converter and inverter are put into use, and in the other one, only the inverter is put into use. There are two pairs of traction motors in drag mode.



Figure 17. Experimental platform of the whole system.

Figure 18 is the experimental waveform. The grid-side voltage is e_s , the grid-side currents are i_{s1} and i_{s2} , and U_{dc} is DC voltage. At the point of A, k_{p_vcc} increased from 1.5 to 5.5. As can be seen from this figure, the system cannot be stable again from the point of A, and the current waveforms are also divergent, which triggered the protection of the system. It should be noted that the grid-side voltage e_s in the experiment is a signal introduced from the primary side of the traction transformer directly. When the oscillation occurs, the grid-side voltage changes slightly, so after protection and when the system stops working, e_s can still become normal. Through experiments it is shown that the increase of the voltage loop parameter k_{p_vcc} does make the system unstable, which is consistent with the simulation. It further verifies the correct characteristic of the model described in this paper.



Figure 18. Experimental figure about k_{p_vcc} change.

5. Conclusions

Firstly, an impedance model of a train in the d-q coordinate system is established in this paper. The proposed model is capable of reflecting the control scheme of the grid-side converter in details, which benefits the analysis of the impact of parameter changes on the system. The equivalent load models of grid-side converter are also taken into consideration, making the model more complete. The load model mainly affects the high-frequency part of d-d, q-d and q-q train admittance. It has a certain influence on the full frequency band of d-q axis train admittance. The load model cannot be ignored because of this influence. Secondly, this paper considers the impedance and admittance components of the network in the d-q coordinate system in more details, making the whole model more accurate. Thirdly, this model can help to analyze the stability of the whole system as seen in 3D figure and zero-pole diagram. Finally, from the results of simulation and experiment, this model can reflect the impact of parameter changes on the train-network system correctly.

In future research, the impedance models of inverter and traction motor also need further study. At present, only a simple d-q axis impedance model is established in this paper, and the control components of inverter and traction motor need to be considered later to create a more accurate load model. In addition, the influence of the equivalent coupling impedance (d-q and q-d axis impedance) of the train-network on the system needs further research.

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