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A New Real Time Lyapunov Based Controller for Power Quality Improvement in Unified Power Flow Controllers Using Direct Matrix Converters

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Abstract: This paper proposes a Direct Matrix Converter operating as a Unified Power Flow Controller (DMC-UPFC) with an advanced control method for UPFC, based on the Lyapunov direct method, presenting good results in power quality assessment. This control method is used for real-time calculation of the appropriate matrix switching state, determining which switching state should be applied in the following sampling period. The control strategy takes into account active and reactive power flow references to choose the vector converter closest to the optimum. Theoretical principles for this new real-time vector modulation and control applied to the DMC-UPFC with input filter are established. The method needs DMC-UPFC dynamic equations to be solved just once in each control cycle, to find the required optimum vector, in contrast to similar control methods that need 27 vector estimations per control cycle. The designed controller's performance was evaluated using Matlab/Simulink software. Controllers were also implemented using a digital signal processing (DSP) system and matrix hardware. Simulation and experimental results show decoupled transmission line active (*P*) and reactive (*Q*) power control with zero theoretical error tracking and fast response. Output currents and voltages show small ripple and low harmonic content.

Keywords: Unified Power Flow Controllers (UPFC); Direct Matrix Converter (DMC); digital control; real time Lyapunov

1. Introduction

Direct matrix converters (DMCs) are alternative power converters to DC-link back-to-back connected inverter-rectifier with capacitive energy storage. Therefore, DMCs can be advantageously used in unified power flow controllers (UPFCs). UPFCs emerged in the 1990s by L. Gyugyi as the most versatile FACTS (Flexible AC Transmission Systems) equipment [1,2]. FACTS technology uses power electronic devices—namely power semiconductors—with turn-off capability associated to their firing circuits and modern control techniques to enforce the active and reactive power in electrical systems. These power controllers work in real time to regulate the energy transit in the transmission lines up to its thermal limits [3].

The UPFC with a classic topology, based on two AC-DC converters back to back connected through a common DC link, needs a large high-voltage DC storage capacitor bank to allow shunt and series compensation and bidirectional power flow [4–6]. The UPFC controls some line parameters

(such as impedance, bus voltage, voltage angle) guaranteeing fast, dynamic performance of the power transmission network and ensuring a reliable and precise control of both line active, *P*, and reactive, *Q*, power flow [6–8].

Several research works with UPFC show conventional control methods based on power systems with linearized models, valid around an operating point, whose dynamic control and performance evaluation are presented in [9–12]. However, these control methods present poor dynamic responses and/or undesired active-reactive power coupling. Improved approaches are based on nonlinear controllers, allowing a better tuning of linear controller parameters, and have the capability to adapt to the continuously changing power system dynamics [13–15]. In [14], nonlinear robust direct power controllers allow dynamic response improvement based on sliding mode control techniques to select adequate space vectors to control active and reactive power in real time.

In recent years, alternative UPFC topologies have emerged based on the direct three phase AC-AC matrix converter [11–16]. This power converter eliminates the DC link capacitors, thus reducing cost, size, and maintenance, while increasing reliability and lifetime. The DMC processes electrical energy directly needing almost no energy storage, allows bidirectional power flow, guarantees near sinusoidal input and output currents, and controls the voltage's amplitude and frequency at an adjustable power factor [17,18]. These AC/AC converters guarantee that the active power exchanged on the UPFC series connection is always supplied/absorbed by the shunt connection, and still have the capability to allow independent reactive control on the UPFC shunt and series converter sides. However, as there is almost no DMC stored energy, input and output powers are strongly coupled, hardening the control design.

The main contribution of this paper is, therefore, a new real-time vector modulation and control for the DMC-UPFC. This control method uses an algorithm based on the power system model equations and the direct method (2nd method) of Lyapunov to guarantee asymptotic stability. The control strategy is based on the desired references for active, P, and reactive, Q, power flow and uses a cost functional to choose the converter output vector closest to the optimum [19] considering P and Q requirements. The power controllers choose the converter output vector by minimizing the distance between the virtual control vector and all available converter vectors. This digital control method allows a significant reduction in the computing time of the digital signal processing (DSP) unit in many industrial applications [20].

A theoretical analysis of the new real time Lyapunov-based controller, applied to the DMC-UPFC with input filter, is presented (Section 3). Dynamic and steady-state performance of the *P* and *Q* power control method is evaluated and discussed using detailed simulations and experimental implementation (Sections 4 and 5). Results show a controller insensitive to power system non-linearity and active and reactive power control with zero error tracking and fast response times (Section 6). Results also show a low total harmonic distortion (THD) in the output voltages and currents ensuring a good performance of this controller in power quality applications. The controllers are insensitive to power system non-linearity to cross-coupling effects.

2. Modeling of the UPFC Power System

The proposed power system model of the DMC operating as a UPFC is presented in the simplified scheme of Figure 1.

In this power transmission network, V_S and V_R are the sending-end and receiving-end voltages, respectively, of G_S and G_R generators feeding load Z_{load} . The DMC is connected to the transmission line 2 through shunt and series coupling transformers, respectively T_1 and T_2 . Also, as shown in Figure 1, Z_{L1} and Z_{L2} are the impedances of the transmission lines 1 and 2, respectively, represented as a series inductance and resistance.



Figure 1. Transmission network with DMC-UPFC.

2.1. UPFC Power System Dynamic Model

Figure 2 shows the connection diagram of the DMC-UPFC system to the transmission line. This diagram includes a three-phase *LCR* input low pass filter to re-establish a voltage-source boundary to the DMC input and to enable smoother input currents. A three-phase shunt input transformer (T_a , T_b , T_c), a three-phase series output transformer (T_A , T_B , T_C) and a three-phase DMC are also included.



Figure 2. A detailed diagram with DMC-UPFC.

To design DMC-UPFC decoupled active and reactive power controllers, the dynamic Equation (1) is considered. This equation represents the active and reactive power of sending end generator G_S , in dq coordinates [21].

$$\begin{bmatrix} P \\ Q \end{bmatrix} = \begin{bmatrix} V_{Sd} & V_{Sq} \\ V_{Sq} & -V_{Sd} \end{bmatrix} \begin{bmatrix} I_d \\ I_q \end{bmatrix}$$
(1)

where V_{Sd} , V_{Sq} are the mains voltage components, and I_d , I_q the line currents, both in dq coordinates (Figure 1).

Choosing a *dq* rotating reference frame so that the mains voltage (V_{Sq}) is equal to zero, *P* and *Q* will be given by Equation (2).

$$\begin{bmatrix} P \\ Q \end{bmatrix} = \begin{bmatrix} V_{Sd}I_d \\ -V_{Sd}I_q \end{bmatrix}$$
(2)

The control vector components to ensure convergence of the power references values are obtained considering the time derivative of Equation (3), assuming constant V_{Sd} .

$$\frac{d}{dt} \begin{bmatrix} P \\ Q \end{bmatrix} = V_{Sd} \frac{d}{dt} \begin{bmatrix} I_d \\ -I_q \end{bmatrix}$$
(3)

Considering a symmetrical and balanced three-phase system, an equivalent per-phase model of a DMC-UPFC transmission system, shown in Figure 3, is obtained, where the voltage sources, the coupling transformers and the DMC are all considered ideal. Based on this simplified circuit, the dynamics of three-phase currents *ij* in the transmission line is:



Figure 3. Equivalent per-phase circuit of DMC-UPFC transmission system.

Applying the Kirchhoff laws to the equivalent circuit gives the equations of the AC line currents in *dq* coordinates:

$$\frac{d}{dt} \begin{bmatrix} I_d \\ I_q \end{bmatrix} = \begin{bmatrix} -\frac{R_2}{L_2} & \omega \\ -\omega & -\frac{R_2}{L_2} \end{bmatrix} \begin{bmatrix} I_d \\ I_q \end{bmatrix} + \frac{1}{L_2} \begin{bmatrix} V_{Sd} + V_{Cd} - V_{R0d} \\ + V_{Cq} - V_{R0q} \end{bmatrix}$$
(5)

In (5), Thevenin equivalent inductance and resistance (L_2 and R_2) are estimated by $L_2 = X_2/\omega$, $R_2 = R_{L2} + R_{L1} | |R_{Zload}$.

Substituting line currents (5) in (3) and simplifying the result, Equation (6) is obtained:

$$\frac{d}{dt} \begin{bmatrix} P \\ Q \end{bmatrix} = \begin{bmatrix} -\frac{R_2}{L_2} & -\omega \\ \omega & -\frac{R_2}{L_2} \end{bmatrix} \begin{bmatrix} P \\ Q \end{bmatrix} + \frac{V_{Sd}}{L_2} \begin{bmatrix} V_{Sd} + V_{Cd} - V_{R0d} \\ V_{Cq} - V_{R0q} \end{bmatrix}$$
(6)

From these dynamics, it can be seen that *P* and *Q* can be controlled by V_{Cd} and V_{Cq} , being V_{R0d} and V_{R0q} system disturbances.

2.2. Direct Matrix Converter Model

To apply a closed-loop space-vector technique, the ideal three-phase direct matrix converter may be considered as an array of nine bi-directional switches, S_{kj} . These switches have turn-on and turn-off capability, allowing the connection of each one of three output phases directly to a given three-phase input voltage.

Considering also ideal power semiconductors, each DMC bi-directional switch S_{kj} ($k, j \in \{1, 2, 3\}$) can assume two possible states: " $S_{kj} = 1$ " (switch closed) or " $S_{kj} = 0$ " (switch opened), being the nine DMC switches represented as a 3 × 3 matrix (7). Taking into account circuit topology restrictions, no input phase short-circuits and no open output phases, there are only 27 possible switching combinations [14].

$$\begin{bmatrix} S_{11} & S_{12} & S_{13} \\ S_{21} & S_{22} & S_{23} \\ S_{31} & S_{32} & S_{33} \end{bmatrix} \sum_{j=1}^{3} S_{kj} = 1 \quad k, j \in \{1, 2, 3\}$$
(7)

According to Figure 2 the output and input voltages are related by matrix *S*, being $\begin{bmatrix} v_A & v_B & v_C \end{bmatrix}^T = S \begin{bmatrix} v_a & v_b & v_c \end{bmatrix}^T$. Then, it is necessary to represent the output voltages and the input currents yielding 27 switching patterns, as time variant vectors in $\alpha\beta$ coordinates [14]. The relationship between input phase currents and output phase currents depends on the transposition of matrix *S*: $\begin{bmatrix} i_a & i_b & i_c \end{bmatrix}^T = S^T \begin{bmatrix} i_A & i_B & i_C \end{bmatrix}^T$.

3. DMC-UPFC Lyapunov-Based Control

The proposed control strategy based on the direct method of Lyapunov stability starts by defining a positive definite Lyapunov candidate function $V_X = e_X^2/2$, where e_X is the tracking error of the Xvariable, $e_X = X_{\text{Ref}} - X$. To ensure a globally asymptotically-stable control solution from Lyapunov's direct method of stability [22], the time derivative V_X of the positive-definite Lyapunov function V_X must be globally negative-definite $V_X < 0$, or $e_X e_X < 0$. This can be achieved by imposing an error dynamic such as:

$$\dot{e}_X + k_X e_X = 0 \Rightarrow \dot{e}_X = -k_X e_X \tag{8}$$

where k_X is a positive definite constant, resulting in $e_X \dot{e}_X = -k_X e_X^2 < 0$. Equation (8) ensures asymptotic stability, as the e_X error tends to zero with time constant $1/k_X$, and robustness as the error dynamic in (8) is independent of the system parameters.

3.1. Active and Reactive Power Control

The control objective is to track the power references P_{Ref} , Q_{Ref} , so that $e_X = [e_P, e_Q]^T$, the tracking errors (or deviations) being $e_P = P_{\text{Ref}} - P$ and $e_Q = Q_{\text{Ref}} - Q$:

$$\begin{bmatrix} e_P \\ e_Q \end{bmatrix} = \begin{bmatrix} P_{\text{Ref}} - P \\ Q_{\text{Ref}} - Q \end{bmatrix} \approx \begin{bmatrix} 0 \\ 0 \end{bmatrix}$$
(9)

To guarantee no cross-coupling between *P* and *Q* power controllers, fast response times and asymptotic stability, Lyapunov candidate functions are defined as $V_P = e_P^2/2$, $V_Q = e_Q^2/2$. Therefore, the controller must enforce:

$$\begin{bmatrix} e_P & 0\\ 0 & e_Q \end{bmatrix} \frac{d}{dt} \begin{bmatrix} e_P\\ e_Q \end{bmatrix} < \begin{bmatrix} 0\\ 0 \end{bmatrix}$$
(10)

This can be accomplished if it is considered:

$$\frac{d}{dt} \begin{bmatrix} e_P \\ e_Q \end{bmatrix} = -\begin{bmatrix} k_P & 0 \\ 0 & k_Q \end{bmatrix} \begin{bmatrix} e_P \\ e_Q \end{bmatrix} = -\begin{bmatrix} k_P & 0 \\ 0 & k_Q \end{bmatrix} \begin{bmatrix} P_{\text{Ref}} - P \\ Q_{\text{Ref}} - Q \end{bmatrix}$$
(11)

where k_P , k_Q are positive definite constants. Replacing the e_P , e_Q time derivates using (6), Equation (12) is received:

$$\frac{d}{dt}\begin{bmatrix} P_{\text{Ref}}\\ Q_{\text{Ref}}\end{bmatrix} - \begin{bmatrix} -\frac{R_2}{L_2} & -\omega\\ \omega & -\frac{R_2}{L_2}\end{bmatrix}\begin{bmatrix} P\\ Q\end{bmatrix} - \frac{V_{Sd}}{L_2}\begin{bmatrix} V_{Sd} + V_{Cd\text{Ref}} - V_{R0d}\\ -V_{Cq\text{Ref}} + V_{R0q}\end{bmatrix} = -\begin{bmatrix} k_P & 0\\ 0 & k_Q\end{bmatrix}\begin{bmatrix} P_{\text{Ref}} - P\\ Q_{\text{Ref}} - Q\end{bmatrix}$$
(12)

Solving Equation (12) for V_{CdRef} and V_{CqRef} the DMC controller must impose the output voltages V_{CdRef} and V_{CqRef} .

$$\begin{bmatrix} V_{CdRef} \\ V_{CqRef} \end{bmatrix} = \frac{L_2}{V_{Sd}} \begin{pmatrix} k_P & 0 \\ 0 & -k_Q \\ -k_Q & 0 \end{pmatrix} \begin{bmatrix} P_{Ref} - P \\ Q_{Ref} - Q \end{bmatrix} + \frac{d}{dt} \begin{bmatrix} P_{Ref} \\ -Q_{Ref} \end{bmatrix} \end{pmatrix} + \frac{1}{V_{Sd}} \begin{bmatrix} R_2 & \omega L_2 \\ -\omega L_2 & R_2 \end{bmatrix} \begin{bmatrix} P \\ Q \end{bmatrix} - \begin{bmatrix} V_{Sd} - V_{R0d} \\ -V_{R0q} \end{bmatrix}$$
(13)

This equation defines the reference values V_{CdRef} , V_{CqRef} that must be injected in the line by the series transformer of the space-vector PWM controlled DMC.

3.2. Input Reactive Power Control

Applying Blondel-Park transformation to the input filter state variables presented in Figure 3 and neglecting the effects of the damping resistors, Equation (14) can be written, where V_{id} , V_{iq} , i_{id} , i_{iq} represent input voltages and input currents in dq components (at the shunt transformer secondary), and V_{dM} , V_{qM} , i_{dM} , i_{qM} are DMC voltages and input currents in dq components, respectively,

$$\frac{di_{id}}{dt} = \omega i_{iq} - \frac{1}{2l} V_{dM} - \frac{1}{2\sqrt{3l}} V_{qM} + \frac{1}{l} V_{id}
\frac{di_{iq}}{dt} = -\omega i_{id} + \frac{1}{2\sqrt{3l}} V_{dM} - \frac{1}{2l} V_{qM} + \frac{1}{l} V_{iq}
\frac{dV_{dM}}{dt} = \omega V_{qM} - \frac{1}{2\sqrt{3C}} i_{iq} + \frac{1}{2C} i_{id} - \frac{1}{2C} i_{dM} + \frac{1}{2\sqrt{3C}} i_{qM}
\frac{dV_{qM}}{dt} = -\omega V_{dM} + \frac{1}{2\sqrt{3C}} i_{id} + \frac{1}{2C} i_{iq} - \frac{1}{2\sqrt{3C}} i_{dM} - \frac{1}{2C} i_{qM}$$
(14)

This transformation guarantees all input variables of the matrix converter are time invariant in the rotating reference frame.

The control objective is to have $Q_i = Q_{i\text{Ref}} = 0$. The control input is i_{qM} and the output is i_{iq} , as $Q_i = -V_{id}i_{iq}$ at constant V_{id} . Defining the tracking error $e_{Qi} = Q_{i\text{Ref}} - Q_i = -Q_i$, to guarantee Lyapunov asymptotic stability in this 2nd order system, the controller must enforce an exponentially stable error dynamics tending to zero such as $\ddot{e}_{Qi} + k_1\dot{e}_{Qi} + k_2e_{Qi} = 0$, where k_1 and k_2 are positive definite constants. Therefore:

$$\frac{d^{2}e_{Q_{i}}}{dt^{2}} + k_{1}\frac{de_{Q_{i}}}{dt} + k_{2}e_{Q_{i}} = 0 \Rightarrow \frac{d^{2}Q_{i\text{Ref}}}{dt^{2}} - \frac{d^{2}Q_{i}}{dt^{2}} + k_{1}\frac{de_{Q_{i}}}{dt} + k_{2}e_{Q_{i}} = 0 \Rightarrow$$

$$\frac{d^{2}Q_{i\text{Ref}}}{dt^{2}} + k_{1}\frac{de_{Q_{i}}}{dt} + k_{2}e_{Q_{i}} = \frac{d^{2}Q_{i}}{dt^{2}}$$
(15)

Solving (14) for $\frac{d^2Q_i}{dt^2}$, Equation (16) is obtained:

$$\frac{d^2 Q_i}{dt^2} = \frac{d}{dt} \left[\frac{d(-V_{id}i_{iq})}{dt} \right] = -V_{id} \frac{d}{dt} \left[\frac{d}{dt}i_{iq} \right] = -V_{id} \frac{d}{dt} \left(-\omega i_{id} + \frac{1}{2\sqrt{3l}}V_{dM} - \frac{1}{2l}V_{qM} + \frac{1}{l}V_{iq} \right) \Rightarrow$$

$$\frac{d^2 Q_i}{dt^2} = -V_{id} \left(-\omega \frac{di_{id}}{dt} + \frac{1}{2\sqrt{3l}} \frac{dV_{dM}}{dt} - \frac{1}{2l} \frac{dV_{qM}}{dt} + \frac{1}{l} \frac{dV_{iq}}{dt} \right)$$
(16)

Simplifying (16) and replacing the value of $\frac{d^2Q_i}{dt^2}$ in (15), Equation (17) is obtained:

$$\frac{1}{V_{id}}\frac{d^{2}Q_{i\text{Ref}}}{dt^{2}} + \frac{k_{1}}{V_{id}}\frac{de_{Q_{i}}}{dt} + \frac{k_{2}}{V_{id}}e_{Q_{i}} = \left(\omega^{2} + \frac{1}{3lC}\right)i_{iq} - \frac{\omega}{l}V_{dM} - \frac{\omega}{\sqrt{3l}}V_{qM} + \frac{\omega}{l}V_{id} - \frac{1}{3lC}i_{qM\text{Ref}} - \frac{1}{l}\frac{dV_{iq}}{dt}$$
(17)

where i_{qMRef} is the value of i_{qM} (control input) that satisfies (15). Solving (17) for the reference value i_{qMRef} of the *q* component of the matrix current i_{qM} , Equation (18) is obtained:

$$i_{qMRef} = \frac{3lC}{V_{id}} \left(\frac{d^2 Q_{iRef}}{dt^2} + k_1 \frac{de_{Q_i}}{dt} + k_2 e_{Q_i} \right) + (3lC\omega^2 + 1)i_{iq} + 3C\omega(V_{id} - V_{dM}) - \sqrt{3}C\omega V_{qM} - 3C\frac{dV_{iq}}{dt}$$
(18)

Equations (13) and (18) require distinct space-vectors to impose V_{CdRef} , V_{CqRef} and i_{qMRef} , as discussed in Section 2.2. The optimum space-vector is computed in the next section by minimizing a cost functional of the reference voltages and current errors.

3.3. Optimum Space-Vector

A weighted cost functional of squared tracking errors is used to select in real time the optimum space-vector (one out of the 27 possible switching combinations) which should be applied to the DMC operated as UPFC in the following sampling period.

The vector selection strategy minimizes the cost functional given in (19).

$$C(t_s) = \sqrt{G_{V_{Cd}} e_{V_{Cd}}^2 + G_{V_{Cq}} e_{V_{Cq}}^2 + G_{I_{iq}} e_{i_{qM}}^2}$$
(19)

where $e_{V_{Cd}} = V_{CdRef} - V_{Cdv}$, $e_{V_{Cq}} = V_{CqRef} - V_{Cqv}$ and $e_{i_{qM}} = i_{qMRef} - i_{qMv}$, represent the errors between the references and values obtained with each switching combination, and $G_{V_{Cd}}$, $G_{V_{Cq}}$ and $G_{I_{qM}}$ are the respective weights.

In each sampling time t_s , the real-time modulation control method first calculates the reference components, $V_{CdRef}(t_s)$, $V_{CqRef}(t_s)$ and $i_{qMRef}(t_s)$ given in (13) and (18). Then, the weighted cost functional (19) is evaluated for each one of the 27 matrix vectors to select the one presenting the minimum value of the cost functional.

4. Implementation of DMC-UPFC Controller

The implementation scheme of the proposed DMC-UPFC controller is presented in the block diagram of Figure 4. In this scheme, the control of the instantaneous *P* and *Q* powers requires the measurement of the instantaneous output voltages and currents. The control of the matrix input reactive power requires the measurement of the input currents, and the measurement of the mains and capacitor voltages.



Figure 4. Control scheme of the real time Lyapunov-based controller of the DMC-UPFC.

The real time Lyapunov-based controller (Figure 4) algorithm, implemented in a digital signal processor, is presented in the flowchart in Figure 5. As shown in the diagram, the control method main loop calculates the values of $V_{CdRef}(t_s)$, $V_{CqRef}(t_s)$ and $i_{qMRef}(t_s)$ once, while the cost functional, $C(t_s)$, is evaluated 27 times to find the vector with components $V_{Cd}(t_s)$, $V_{Cq}(t_s)$ and $i_{qM}(t_s)$ that minimizes the weighted cost functional. This selects the space-vector which minimizes the cost functional, and is applied to the DMC-UPFC in the following sampling time ($t_s + \Delta T$).



Figure 5. Flowchart of the control method proposed to compute V_{CdRef} , V_{CqRef} (13) and i_{qMRef} (18).

Relative to predictive controllers [19], the control process proposed here reduces the microprocessor computational effort.

5. Simulation and Experimental Evaluation

This section presents simulation and experimental results to evaluate the performance of the proposed control method. First, using detailed simulation models for the three-phase direct matrix converter, series and shunt transformers, sources, transmission lines, loads and real-time Lyapunov controller algorithm, the DMC-UPFC is simulated using the MATLAB/Simulink SimPowerSystems toolbox. Matrix converter semiconductors were simulated as SimPowerSystems switches with appropriate snubbers.

In order to validate the simulation results with experimental results, a low-power prototype DMC was built [23], using three semiconductor modules from DANFOSS, each one with six 1200 V, 25 A insulated-gate bipolar transistors (IGBT) with anti-parallel diodes, in a common collector arrangement, driven by optical isolated drives (TLP250). The second order input filter component values are:

l = 4.2 mH, *C* = 6.6 μF, *r* = 25 Ω. This prototype was connected to the laboratory low-voltage network operating as a UPFC, (Figure 4) using three-phase transformers T_1 , T_2 (2 kVA transformers with voltage ratio 220/115 V and 66.5/66.5 V, respectively). Voltage sensors were used to measure the voltages (LEM LV 25-P), while current sensors were used to measure the DMC currents (LEM LA25NP).

To carry out real-time control of the power in the transmission lines, a real-time DSP board (DS1103 model of dSPACE) with the proposed control algorithm was used to select the suitable vector at every sampling time. The DS1103 was able to execute the vector modulation, control algorithm, and output the 9 DMC bidirectional switch control signals at a sustained sampling time (T_s) of 18 µs. This sampling time is suited to switching the DMC at frequencies around 5 kHz, but is not enough to guarantee safe commutation between the DMC bidirectional switches, as this requires much faster changing signals. Therefore, to ensure safe commutation, the four-step output current commutation strategy was used [18], implemented in a field-programmable gate array (FPGA) using a Xilinx (Virtex 5) board, as represented in Figure 4.

Experimental results were obtained considering that the input phase-to-phase DMC voltage equalled 120 VRMS; the load power equalled 1.5 kW (1 pu), the transmission lines 1 and 2 were emulated by inductances $L_{L1} = 12$ mH, $L_{L2} = 15$ mH and series resistances $R_{L1} = R_{L2} = 0.2 \Omega$, respectively.

Also, experimental and simulation results for the *P* and *Q* power UPFC controller were obtained from the step response to changes in P_{Ref} and Q_{Ref} references (ΔP_{Ref} and ΔQ_{Ref} , respectively). Figure 6a,b show, respectively, simulation and experimental results for the proposed controller, considering *P* and *Q* power step responses to $\Delta P_{\text{Ref}} = +0.4$ pu and $\Delta Q_{\text{Ref}} = +0.2$ pu, with initial reference values $P_{\text{Ref}} = 0.4$ pu, $Q_{\text{Ref}} = 0.2$ pu.



Figure 6. Active and reactive series power response for *P* and *Q* steps (first $\Delta P_{\text{Ref}} = +0.4$ pu and after $\Delta Q_{\text{Ref}} = +0.2$ pu). (a) Simulation results; (b) Experimental results (Ch1 = Ch2 = 0.5 pu/div).

Both results show clearly that there is no cross-coupling between active and reactive power, being both independently controlled, and showing a fast response with a small ripple in steady state. The results of Figure 7a,b are simulation and experimental results, respectively, for the proposed control method, when the same initial conditions as in Figure 6 are retained, but with different time instants. Once again, these results prove the good performance of the power controller.

Figure 8a,b show the simulation and experimental results, respectively, of the active and reactive powers, and also the line currents, considering P and Q power step responses to $\Delta P_{\text{Ref}} = +0.4$ pu and $\Delta Q_{\text{Ref}} = -0.2$ pu, when initial reference values are $P_{\text{Ref}} = 0.4$ pu, $Q_{\text{Ref}} = 0.2$ pu. These results show no cross-coupling, steady-state errors below ripple errors, and fast response times for different changes of power references. Line currents are almost sinusoidal with small ripple content.



Figure 7. Active and reactive series power response for *P* and *Q* steps (first $\Delta Q_{\text{Ref}} = +0.2$ pu and after $\Delta P_{\text{Ref}} = +0.4$ pu). (a) Simulation results; (b) Experimental results (Ch1 = Ch2 = 0.5 pu/div).



Figure 8. Active and reactive power response and line currents for a *P* and *Q* step $\Delta P_{\text{Ref}} = +0.4$ pu and $\Delta Q_{\text{Ref}} = +0.2$ pu). (a) Simulation Results; (b) Experimental results, Ch1 = Ch2 = 0.5 pu/div (*P* Series and *Q* Series) and Ch3 = Ch4 = 1.0 pu/div (iB and iC).

Results of Figures 8b and 9a show line and input direct matrix converter currents in steady-state, for $P_{\text{Ref}} = 0.4 \text{ pu}$, $Q_{\text{Ref}} = 0.2 \text{ pu}$. Line currents are almost sinusoidal with small ripple content, while matrix currents shows some ripple content as usual in DMC [15].



Figure 9. Line currents (iB, iC) and input matrix converter currents (ib, ic) for $\Delta P_{\text{Ref}} = +0.4$ pu and $\Delta Q_{\text{Ref}} = +0.2$ pu: (a) Simulation results; (b) Experimental results (Ch1 = Ch2 = Ch3 = Ch4 = 1.0 pu/div).

The experimental power spectral density of transmission line converter current (Figure 10) shows that the main harmonics are nearly 35 dB below the 50-Hz fundamental.



Figure 10. Power spectral density of line current (iB) 10 dBV/div.

Figure 11a,b show, respectively, the harmonic spectrum of the line current and load voltage. These quantities present a low harmonic content (THD less than 5%) of 4.86% and 4.53%, respectively, which compares favourably with the improved methods proposed in [24] to enhance power quality in matrix converters (the THD of input currents is higher than 6.29% in [24]).



Figure 11. Harmonic content of the line currents (a) and of the load voltages (b).

Compared to linear [24], sliding-mode [15], and predictive [20] controllers, the Lyapunov controlled matrix-based UPFC presents the lowest THD of currents, even lower than linear controllers; while for cross-coupling and response times, they present better results, as do sliding-mode and predictive controllers. Concerning steady-state errors, all controllers theoretically show no steady-state errors, while the Lyapunov controller's asymptotic stability guarantees that the controller will enforce an exponentially stable dynamic, tending to zero error, which is not guaranteed by predictive controllers.

Simulation and experimental results show that *P* and *Q* power flow can be effectively controlled using the DMC-based UPFC and the proposed power control method.

A DMC-based UPFC was implemented in the laboratory to verify the proposed power controller, as shown in Figure 12.



Figure 12. The experimental setup using the DMC based UPFC.

6. Conclusions

This paper designs a new real-time space-vector control, based on the Lyapunov stability direct approach, for active and reactive power flow using direct matrix converters connected to the power transmission line operating as Unified Power Flow Controllers. This controller guarantees stability of the power system and improves the power quality by optimizing the space-vector error cost functional.

The presented simulation and experimental results show that active and reactive power flow can be advantageously controlled using the proposed control method. Results show steady-state errors below ripple errors, no cross-coupling, insensitivity to non-modeled dynamics, fast response times and low THD in output currents (less than 5%).

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