



Article FedMSA: A Model Selection and Adaptation System for Federated Learning

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Abstract: *Federated Learning* (FL) enables multiple clients to train a shared model collaboratively without sharing any personal data. However, selecting a model and adapting it quickly to meet user expectations in a large-scale FL application with heterogeneous devices is challenging. In this paper, we propose *a model selection and adaptation system for Federated Learning* (FedMSA), which includes a hardware-aware model selection algorithm that trades-off model training efficiency and model performance base on FL developers' expectation. Meanwhile, considering the expected model should be achieved by dynamic model adaptation, FedMSA supports full automation in building and deployment of the FL task to different hardware at scale. Experiments on benchmark and real-world datasets demonstrate the effectiveness of the model selection algorithm of FedMSA in real devices (e.g., Raspberry Pi and Jetson nano).

Keywords: federated learning; model selection; device adaptation; model adaptation; orchestration; distributed system

1. Introduction

Cloud computing has positively affected people's daily life and industrial production since it was proposed around 2005 [1], such as social media, cloud storage services and industry 4.0. Meanwhile, the Internet of Things (IoT) industry is developing rapidly, and the number of IoT devices worldwide will reach more than 41 billion by 2027, which is five times that of about 8 billion in 2019, as forecasted by Business Insider [2]. Billions of IoT devices connect to the Internet to produce large volumes of data daily, which are transferred and stored in cloud servers. However, the challenges of complying with rigorous data protection regulations such as EU/UK General Data Protection Regulation (GDPR) [3] make it extremely difficult to collect data across heterogeneous sources. Besides, centralized data storage, unified servicing and modelling in the cloud are also facing massive pressures from the constraints of network bandwidth of IoT devices and users' privacy leakage problem [4]. As a consequence, Machine Learning (ML)-applied device intelligence becomes vital and important development direction of IoT. The emergence of on-device learning solves the problem of bandwidth and privacy by directly using local data to train ML models and make predictions instead of using the predominant paradigm of ML that trains a model in the cloud and inferences locally [5].

However, because of the enormous difference in data distribution and diversity, the ondevice learning may lead to a serious problem in that the model trained locally cannot achieve the same performance as the model trained centralized in the cloud. To cope with



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Copyright: © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). the aforementioned challenge, Federated learning (FL) was proposed to let different clients (e.g., mobiles, servers or companies) collaboratively train a unified model while keeping the training data locally [6,7]. FL has been applied to various privacy-critical scenarios such as hospitals [8], banks [9] and autonomous vehicles [10]. In FL, sharing raw personal data is strictly prohibited, and only model parameters can be transmitted between the server and clients. In this way, the local model performance of all clients could be improved while ensuring privacy security. One round model training process of horizontal FL is shown in Figure 1: ① A central server broadcasts the global model to all clients; ② all clients train the latest global model using their local data to get the latest local model; 3 all clients then upload their latest local model to the central server; ④ finally, the central server aggregates all local models from clients to update the global model. Since the aggregation progress will happen after receiving all local trained models in horizontal federated learning, the overall system training time is determined by the slowest client. To address this problem, authors in [11] proposed an adaptive deadline determination algorithm for mobile device selection. Nevertheless, it effectively reduces the FL system's training time but does not trade-off both training efficiency and final model performance.



Figure 1. Vanilla Federated Learning Workflow.

Based on [6], the FL setting could be divided into "cross-silo" and "cross-device" regarding training among different organizations or devices. The focus of this paper is on cross-device FL, which means the clients include a large number of mobile or IoT devices. Selection and adaptation of the task model structure is a significant challenge for cross-device FL with heterogeneous hardware. Historically, most FL research has directly used the widely adopted model structure in centralised machine learning [7] or undertook manual designing of the structure [12] based on the target training task. More recently, several studies have started to focus on using *Neural Architecture Search* (NAS) [13] to automatically generate an optimal model structure. They mainly focus on optimising model performance for the reference stage, such as higher accuracy and lower inference latency. In practice, this may hinder the applicability of FL since model training efficiency and hardware heterogeneity also affect the efficiency and performance of the whole FL system. Thus, FL developers find it difficult to achieve the expected results without cross-platform model adaptation and training and inference running simultaneously.

To improve the applicability of practical FL, we consider a realistic setting in which FL system contains heterogeneous hardware where some clients contribute to the central model and use the local model to make predictions simultaneously. Our goal is to select an expected model structure that trades-off the training efficiency (e.g., training time, network

utility) and model performance (e.g., accuracy, inference latency), while considering the developer's choices. For example, some developers are more sensitive to model training efficiency, and some want to get a model with optimal performance. Because NAS requires high computational demand, some methods need thousands of GPU days to search the best architecture for some tasks, which can be an unacceptable and catastrophic situation for developers [14]. So our work is based on the pre-searched models' metrics from NAS-Bench-201 [15] and HW-NAS-Bench [16]. Nevertheless, although our model selection algorithm can accurately select the optimal model structure under the given parameter, we recognized that it is difficult to accurately select the optimal model structure that meets the developer's expectations through our model algorithm by one-time deployment as FL developers may have more personalized preferences. Therefore, we need a system that supports rapid task deployment at scale, iterating over the results to get the optimal result that developers expect.

In this paper, we propose *FedMSA*, a <u>Model Selection and Adaptation system for Federated</u> *Learning*, which can address the previously listed challenges. It reduces the complexity of FL system deployment by providing automation of adaptation and deployment of training task as microservices in a FL life-cycle along with *model selection* (MS) algorithm. Our MS algorithm could help FL developers search for an optimal model structure based on their expectations in training efficiency and final model performance. On comparing the results of our MS algorithm with manual design and direct selection of a model by one factor from NAS, the model selected by our algorithm achieves good performance in training efficiency and model performance. Similar ideas have been proposed in network speed optimization; for example, the MARS method proposed in [17] maintains balance for comprehensive utilization, transmission efficiency, and monetary cost to optimize high-speed mobile networks. But, to the best of our knowledge, this work is the first attempt to tackle the challenges of training efficiency and model performance by identifying the optimal model structure that meets developer expectations.

Our contributions are summarized as follows:

- Methodologically, we propose a model selection algorithm.
- Practically, we design and implement FedMSA system in real distributed devices with heterogeneous hardware.
- Empirically, we demonstrate the effectiveness of MS algorithm on real-world datasets and compare with other methods.

The paper is organized as follows. Section 2 deals with an overview of related works and state-of-the-art studies. Section 3 discusses the motivation, while Section 4 describes the proposed algorithm and the system. Section 5 presents the evaluation of our algorithm. Finally, the paper is summarized, and future work is discussed.

2. Background and Related Work

Machine Learning (ML) was proposed in 1959 [18], as a part of *artificial intelligence* (AI). ML imitates human learning by modelling experience (historical data) and can constantly self-iterate to improve its performance. During the past two decades, ML made significant progress by successful application in many areas of technology and science, for example, computer vision, natural language processing, autonomous vehicle and robotics [19]. As for the traditional ML tasks, such as classification or linear regression, the user only needs to spend several minutes or hours training the model on a server or personal computer. *Deep learning* (DL) made the number of technological breakthroughs after deep conventional neural networks were proposed in 2012 [20] and significantly outperformed ML. Meanwhile, the number of layers of models designed by ML engineers is increasing, such as ResNet [21]. ResNet allows the network layers of *neural network* (NN) to be continuously superimposed without negatively affecting the model's accuracy, so the authors of ResNet tried increasing it to 110 layers with 1.7 million parameters. In addition, the model structure continues to become increasingly complex, such as the Transformer [22] where the number of parameters of the basic Transformer model is 67 million, and the big Transformer model

could achieve 213 million. Both models have more computation complexity and require more computation resources than the three layers *multi-layer perception* (MLP) model widely used in classic ML tasks.

On the other hand, with an increasing number of edge and IoT devices being connected to the Internet, high volumes of data are being generated and transmitted to the cloud server to train a model. However, the simultaneous and high-frequency transmission of a large amount of data can cause network overload and leak user privacy. Furthermore, data regulations (such as the GDPR) make it hard to access cloud services providers or collect data from terminal devices (e.g., mobiles, sensors). In 2016, the authors in [7] proposed FL, which is a solid solution to the above problems. It lets clients train a central model by feeding their own data collaboratively. Compared with traditional ML model training, sharing raw personal data in FL is strictly prohibited, and only model parameters are transmitted between the server and clients. While protecting user privacy and network traffic, the training model of FL could learn the representation of every client's local dataset to improve model performance. However, similar to the classic distributed system, hardware heterogeneity in cross-device FL can also bring many challenges such as how to generate a model that can be trained efficiently and perform well on heterogeneous hardware.

In centralized deep learning, the developer usually manually designs a model structure for the target task based on their knowledge or experience, such as ResNet [21] in the image classification task, and Transformer [22] and Bert [23] in machine translation task. Manual design of model structures not only increases the labour and time costs of model design and testing, but also makes it difficult for machine learning engineers to design models with good performance in a short period of time. As a consequence, numerous different NN connections choices have to be tried [24]. To avoid this, in recent years, some studies have started to use *neural architecture search* (NAS) instead of manually generated network models. NAS aims to find good architectures, and NAS methods outperform manually designed architectures on some tasks such as object detection [25,26] or semantic segmentation [27–29]. After the NAS first-time proposed in [24], most of studies focused on searching for a model structure with the highest accuracy [30]. As the accuracy of model architecture has improved and becomes state-of-art, some works have started to consider how to strike a balance between performance and efficiency [29,31]. However, due to user data privacy security and network traffic pressure, centralized ML in cloud is shifting to on-device learning and inference on target devices. Thus, some studies involve searching of a model architecture by considering target hardware [15,32–34] or design a specific NAS for target hardware such as [31,35] employ NAS for *microcontrollers* (MCU). Unlike unified model training, on-device learning may lead the model to underperform in some tasks due to lack of data diversity.

To tackle this problem, FedAvg as vanilla federated learning paradigm proposed in [7] allows thousands of clients to train a central model. As a distributed learning system, selecting a model structure is very difficult. It should consider model training efficiency in local devices, whole distributed system utility, and model performance in heterogeneous hardware. Existing NAS methods can only search for the model with best performance [25–29,31,35] or list all model structures and give pre-measure metrics [15,16,34], but lack a model selection method for distributed and federated machine learning systems. However, there are some works on model selection methods that have been attempted. In [36], authors proposed an inference model selection scheme which considers the desired accuracy and inference time for a single target embedded deep learning platform (e.g., Jetson Tx2). Authors in [37] aim to select a model from a large number of available source models that can maximize the predictive performance in the target domain of transfer learning, and they select a model by sorting source models according to their *mean silhouette coefficient* (MSC) score which is calculated by using cosine distance metric. Although these methods can select the optimal model, they lack consideration of model training efficiency, model performance and heterogeneous hardware for FL system. Moreover, in a real federation learning system, rapid model adaptation and model selection

are equally important, FL developers are likely to select and redeploy the selected model multiple times to get the model they expect. This iterative process can be time-consuming and hence, designing a model adaptation orchestration system at scale to support rapid model selection is necessary. Although there have been various platforms supporting deployment and development of FL such as Flower [38], FATE [39], FedML [40], these platforms failed to consider heterogeneous hardware and model selection in real-world distributed systems. To the best of our knowledge, runtime model selection and adaptation system have not been performed in FL applications.

3. Motivation

Consider a system of a real-world large-scale FL application shown in Figure 2. Edge-IoT client devices with various hardware architectures simultaneously perform local prediction tasks which contribute to a central ML model training in a cross-device FL framework. In a networked, distributed and cross-device machine learning system, the primary challenge is model design or model selection as many factors, such as local and global training efficiency, network pressure and model performance need to be considered in heterogeneous hardware architectures. As regards efficiency-sensitive model training tasks, such as recommendation systems, the cold-start phase requires the model training to achieve high accuracy within certain time constraint when a new user or item joins the network. Moreover, some performance-sensitive model training tasks, such as CCTV anomaly detection system, real-time temperature monitoring and alert system, require the model training to perform high accuracy along with low inference latency. These factors raise challenges in manual model design or model selection. Although there exists research that focuses on model selection method [36,37], they fail to consider federated and distributed machine learning framework. Model structure selection automation is crucial in balancing model training efficiency and performance.



Figure 2. Illustration of the motivations. The client clusters represent companies where each cluster contains many heterogeneous devices as Federated Learning clients. The local model of a device participates in FL training and task inference simultaneously.

However, the generated model may not meet the FL developer's expectations. Therefore, a better way is to allow users define the satisfying proportion of model performance and training efficiency. Additionally, several existing NAS studies [15,32–34] provide model structure search space with pre-measured metrics and inference latency in various platforms (e.g., Raspberry Pi, Edge GPU device, and TPU). Nonetheless, no existing studies focus on searching for a ML model based on the developer's preference to balance training efficiency and model performance in federated learning. A system that provides model selection and multi-platform adaptation service, and simplifies the deployment of FL tasks in real devices is a fundamental requirement.

4. Proposed System

This section presents the proposed FedMSA system, large-scale FL, as well as the model selection algorithms.

4.1. System Architecture

In response to the previously mentioned challenges, we developed *FedMSA*, a crossplatform model selection and adaptation system for federated learning applications. We present the overview and architecture of *FedMSA* in Figure 3, which consists of three main components: *Orchestration Infrastructure, Federated Learning* and *Visualization. FedMSA* meets aforementioned requirements, providing flexibility and reliability for FL developers in personalized FL tasks deployment with automatic model selection and adaptation for a specific client's hardware platform.



Figure 3. System Architecture.

Orchestration Infrastructure. Automatic, rapid and agile deployment of FL task is the key to achieving effective model adaptation for a distributed system across different devices with heterogeneous hardware architecture, especially for a large-scale networked ML system. Therefore, we designed an orchestrator as the infrastructure of *FedMSA*,

which supports all aforementioned functionalities from source code auto-building to autodeployment. It consists of two main components: *controller* and *agents* running at the cloud tier and edge tier of edge-cloud computing environment respectively. Meanwhile, all messages transferring between components are handled by a message queue service, and all agents are organized by the agent manager. The training task starts after a machine learning developer pushes the latest code to Github and triggers the task handler to process the task code by calling a cross-platform builder to build FL task code into the microservices base on the hardware architecture information (e.g., x86_64, aarch64 and armv7l) of every agent host which is reported by platform detector.

Federated Learning. An FL system (e.g., FedAvg [7]) usually consists of thousands of participating heterogeneous devices. The scalability requirement of FL deployment necessitates a model selector to generate model structure automatically based on the developer's expectation. Accordingly, we design a Neural Architecture Search (NAS) based model selection algorithm that traded-off model training efficiency and model performance on the heterogeneous hardware, which we discuss in detail in Section 4.2. Further, aggregator and executors are deployed by orchestration infrastructure while message queue handles communication between them where each executor deploys one/multiple clients based on the number of computation units (e.g., CPU, GPU) in the target device. A global client manager manages all clients in the aggregator including client registration, join or quit training requests, and client selection algorithm management.

Furthermore, we provide two ways for fast model adaptation: 1. developers can send an instruction to switch the current training model by the public API whereby the aggregator and all clients will stop current training tasks and load new model to execute by received commands. 2. developers can modify a parameter in the configuration file to calculate new scores for all model candidates and once the latest codes are pushed, the orchestration infrastructure will redeploy all components automatically.

Visualization. All model training information, model performance are uploaded to visualization tools in real-time by model monitor of Federated Learning of FedMSA. Our system supports online monitoring tool Wandb (https://wandb.ai/site accessed on 18 August 2022) and local visualization tool Influxdb (https://www.influxdata.com/ accessed on 18 August 2022).

4.2. Model Selection Algorithm

As mentioned in Section 1, all model structures *x* that our algorithm uses are from the search space of NAS-201-Bench *X* (the *i*th model structure is $x^{(i)} \in X$), essential metrics of every model are from NAS-201-Bench, and metrics about model inference latency in different hardware are from HW-NAS-Bench. We propose a method in Algorithm 1 to collect and organize the metrics from both NAS benchmark studies. The platform detector component in the FL to collects platform information *P* and bandwidth *B* from all clients *C* Figure 3. In particular, our system allows FL developers to pre-filter out the model with unexpected accuracy according to given set model accuracy expectation parameter $\alpha \in [0, 100]$ in the configuration file while abnormal model structures are filtered out by system. Besides, to ensure that the impact of each metric on the final score calculation is similar, we adopt the data Min-max normalization [41] that reorganizes the distribution space of all metrics belonging to the values between 0 and 1, and is shown as:

$$f(metrics) = \frac{x_{i,j} - x_j^{min}}{x_j^{max} - x_j^{min}}$$
(1)

In the Equation (1), *j* is the index of attribute of models (e.g., size of model, estimated accuracy of model), *i* is the index of a model, $x_{i,j}$ represents the exact value of attribute *j* of the model *i*, and x_j^{min} and x_j^{max} is the minimum and maximum value of attribute *j* respectively.

Algorithm 1: Pseudocode for model metrics collection				
Input :Client information <i>c</i> , Model accuracy threshold α				
Output: A set of normalized model metrics M_n				
1 function MODEL_METRICS_COLLECTION(c)				
2	$P, B \leftarrow Platform Detector;$ // Collect platform information P and Device			
	Network Bandwidth B from all clients			
3	for <i>i</i> in range(N) do			
4	$m append HW_{lat} \leftarrow HW-NAS-Bench(i);$ // A row of metrics m			
5	m append T_{acc} , Acc_{eval} , $Loss_{eval}$, F_{model} , $N_{params} \leftarrow NAS$ -Bench-201(i)			
6	<i>m</i> append E_{train} which is calculated by Equation (4)			
7	<i>m</i> append <i>U</i> which is calculated by Equation (5)			
8	M_c append data_clean(m, α); // Filter out abnormal model structure and			
	the model accuracy less than α			
9	$M_n \leftarrow normalization(M_c)$ base on Equation (1)			
10	Save M_n as a model metrics file			
11	return M_n			

In the system design view, we consider the running memory requirements of NAS-201-Bench to be around 25G. Accordingly, in the Algorithm 1, we use one-time data collecting or pre-collecting and loading from NAS-201-Bench in the future method to save M_n as a model metrics file. This approach not only dramatically reduces the spatial complexity of our system, but also enables our system to support more devices.

Our model score calculation algorithm mainly follows the Equation (2), and working details shown in Algorithm 2. It aims to trade-off model training efficiency and model performance base on a FL developer's input parameter $\gamma \in [0, 1]$ which indicates the extent to which the developer is concerned about the efficiency of model training.

> $Score = f(ModelTrainingEfficiency) \times \gamma + f(ModelPerformance) \times (1 - \gamma)$ (2)

Algorithm 2: Pseudocode for score calculating			
Input :Normalized model metrics set <i>M_n</i>			
Output: A set of model metrics with scores M_s			
1 function $MODEL_SCORE_CALCULATE(M_n)$			
2 if <i>Not exist model metrics file</i> then			
$M_n \leftarrow \text{Algorithm } 1$			
4 else			
$_{5} \qquad \qquad$			
6 for <i>i</i> in range(NAS-201-Bench search space) do			
$7 \qquad m \leftarrow M_n[i]$			
8 Score \leftarrow Calculate <i>m</i> by Equation (2)			
9 M_s append Score			
10 return M_s			

Based on the model metrics M_n from Algorithm 1, we found that five factors can form Equation (3) to calculate model training efficiency: training time to the model accuracy T_{train} , FLOPs of the model F_{model} , numbers of parameters of the model N_{params} , training efficiency in accuracy increasing E_{train} , and bandwidth utility of a device U. Since some metrics are negatively correlated with model training efficiency, we set their value as negative and upon averaging, we get:

$$f(ModelTrainingEfficiency) = -(T_{train} + F_{model} + N_{params} + E_{train}) - U$$
(3)

where E_{train} is calculated by T_{train} and expected final evaluation accuracy of the model Acc_{eval} which is:

$$E_{train} = \frac{T_{train}}{Acc_{eval}} \tag{4}$$

and *U* calculated by the average of all bandwidth of client devices B_c , and the S_{bit} which means the storage bit of the development platform. In this work, we use Pytorch (https://pytorch.org/ accessed on 18 August 2022) as our development platform, so this parameter is equal to 32 bit.

$$U = \frac{\sum_{c=1}^{C} B_c}{N_{params} * S_{bit}}$$
(5)

We can then summarize the equation for the model training efficiency calculation as:

$$f(ModelTrainingEfficiency) = -(T_{train} + F_{model} + N_{params}) + \frac{T_{train}}{acc_{eval}} + \frac{\sum_{c=1}^{C} B_c}{N_{params} * S_{bit}}$$
(6)

In order to let every metric have the same impact for score calculating, we set the calculation of E_{train} and U before the data normalization process as shown in Algorithm 1. As for the model performance calculating, we mainly focus on the impact of model evaluation accuracy and loss and the model inference latency in every FL client device, which is negatively correlated with model performance. It can be described as:

$$f(ModelPerformance) = acc_{eval} + loss_{eval} - \sum_{n=1}^{N} Lat_n$$
(7)

Overall, the model score calculation equation can be summarized as Equation (8). We calculate scores for all model structures and wrap them up into a set M_s for further model selection decision.

$$g(Score) = \underbrace{-(T_{train} + -F_{model} + -N_{params}) + \frac{T_{train}}{acc_{eval}} + \frac{\overline{\sum_{c=1}^{C} B_c}}{N_{params} * S_{bit}}}_{ModelTrainingEfficiency} \times \gamma + \underbrace{acc_{eval} + loss_{eval} - \overline{\sum_{n=1}^{N} Lat_n}}_{ModelPerformance} \times (1 - \gamma)$$
(8)

After defining the necessary functions, we start primary federated learning training as presented in Algorithm 3. The task starts from procedure D, and our system directly starts training if FL developer has already assigned a specific model index *idx* in the configuration file. If not, the system will select the model index with the highest score from the ranked model metrics list M_r .

During model training, the public API allows the developer to decide whether the current training needs to be terminated and the model adapted from the results of the monitoring data visualization tool shown in Figure 3 by sending instructions at any time. In this way, the cost of task redeployment can be significantly reduced and the developer can re-score all model structures by modifying the γ in the configuration file.

Algo	orithm 3: Algorithm of FedMSA system
Se	erver input: training efficiency concern rate γ , global step-size α_g , global communication round <i>R</i> , and local updating iterations <i>I</i> , NAS-Bench-201 search space <i>N</i> , FL developer assigned model index <i>idx</i>
1 W	e start with Procedure D .
2 Th	nen initializing clients with Procedure A .
3 Fc	for $r = 1 \rightarrow R$ rounds, we run Procedure B and Procedure C iteratively .
4 pi	cocedure A CLIENT_INIT(c, index)
5	$f_c \leftarrow \mathrm{NAS}\text{-}201\text{-}\mathrm{Bench}(\mathrm{index})$; // load model from NAS-201-Bench base on index
6 pi	cocedure B CLIENT_UPDATE(c)
7	$f_c \leftarrow f$; // Receive updated model from SERVER_EXECUTE
8	for $l = 1 \rightarrow l$ do
9	$f_c \leftarrow f_c - sgd;$ // Local model update
10	send $f_c - f$ to SERVER_EXECUTE
11 p1	cocedure C SERVER EXECUTE(index)
12	for $i \leftarrow 0$ to R do
13	$f \leftarrow \text{NAS-201-Bench(index)};$ // Load global model
14	Broadcast f to all clients ;
15	Receive local models' updates $\{f_c - f\}_{c=1}^C$ from CLIENT_UPDATE
16	$f \leftarrow f \cdot \Sigma_{c-1}^{C}(f_{c}-f);$ // FL aggregation broadcast f to CLIENT_UPDATE
L	
17 pi	cocedure D main()
18	If Developer pre-assigned lax then
19	Run Procedure C with input <i>iax</i>
20	
21	$M_{\rm s} \leftarrow {\rm Algorithm} 2$
22	$N_{\rm r} \leftarrow Sort(N_{\rm s})$
23	$l \leftarrow 0$ index () M index[i] . (/ Cat the model index from higher term
24	$P_{\text{und}} \leftarrow N_{\text{r}}$ $nuex[i]$, // Get the model index from highest score
25	while and EL training do
26	if request / Hear's instruction: // Sustem get upor's instruction from
27	APT
28	then
29	if Users' assigned model index in request then
30	$index \leftarrow request$
31	else
32	$i + = 1 index \leftarrow M_r.index[i];$ // Switch to next model in M_r
33	Broadcast message to aggregator and all clients to stop current training, starting a new training with model <i>index</i> .

5. Evaluation

In this section, we present the results of the experiments performed with proposed FedMSA system. We also discuss the technical details of the experimental testbed.

5.1. Experiment

We deploy all our system components, including the MS algorithm on real-world testbeds, as shown in Figure 4. In *FedDAS*, the cloud server is deployed on an Ubuntu server with 20 core (Intel(R) Xeon(R) Silver 4114 CPU @ 2.20 GHz), Gigabit Ethernet and 64 GB memory. The edge devices consist of five Jetson-Nanos (https://developer.nvidia.com/embedded/jetson-nano-developer-kit accessed on 18 August 2022) with 4GB RAM, 128-core Maxwell GPU, Gigabit Ethernet and ARM Cortex-A57 CPU each, and two Respberry Pi 4 models with 4GB RAM, Gigabit Ethernet and 1.5 GHz 64-bit quad-core ARM Cortex-A72 CPU. The networking devices consist of a switcher with Gigabit Ethernet and a router with 10 Gigabit Ethernet.



Figure 4. Experiment Devices.

For the proof-of-concept, all of our experiments have been conducted on a widely adopted benchmark Cifar10 [42] in vanilla federated learning FedAvg [7] and we trained the experimental models for 140 communication rounds with 1 local epoch in total.

We present the comparison of our solution with three well-known neural network models in Table 1: (i) a manually designed, widely used and influential model ResNet [21]. In order to let the size of the model close to the model searched from our system, we used ResNet20 (20 layers ResNet); (ii) two models searched from NAS-201-Bench sorted by a single indicator. They were searched by maximising evaluation accuracy and minimising total training time to accuracy. Three models searched by our FedMSA system were defined by $\gamma = 0.1, 0.2, 0.8$.

In addition, we let FedMSA filter out the model with less than 70% accuracy by setting $\alpha = 70$ in the configuration file. On the other hand, all models' latency measurement experiments are in batches to easily distinguish the gaps in the different experiments' results. The forward and backward propagation latency is measured base on batch size of the training set (64), and inference latency is measured by batch size of the test set (32).

To evaluate our model selection algorithm, we select a related study that is also trying to get the best model without training. Similar to FedMSA, NASWOT was proposed in [43], which is also based on the NAS-201-Bench search space. However, they did not consider training efficiency in the cross-device federated learning setting.

5.2. Results

We present a comprehensive investigation of the proposed FedMSA method on Cifar10 benchmark. We divided the experiment into three control groups, and the experimental results of our MS algorithm are reported in Table 1 from one Jetson nano and one Raspberry Pi 4.

In the first group, we use the selected model structure with a higher model performance by setting $\gamma = 0.1$ for FedMSA, a model structure searched by NASWOTand NAS (Max Acc) is the model structure with the highest accuracy directly searched in the NAS-201-Bench search space. As we can clearly see that although the global test accuracy of NAS (Max Acc) is 1.3% more than FedMSA $\gamma = 0.1$, it got two times more in FLOPs of the model, training time per round and all model latency metrics in client devices than FedMSA ($\gamma = 0.1$). Particularly noteworthy are some metrics with large base values, such as training time per round and model performance in latency where more than twice those metrics can be catastrophic for the tasks sensitive to training efficiency or inference delay. Besides, the model searched by NASWOT performs similar to NAS (Max Acc) in training efficiency, and model latencies matrix. But, the accuracy of the model was reduced 6.42% performance.

Model	Numbers of Parameters (M)	FLOPs (M)	Training Time (m)/Round	Forward Propagation Latency (ms)	Back Propagation Latency (ms)	Model Inference Latency (ms)	Global Test Accuracy	Global Test Loss
NAS(Max Acc)	0.86 (×2.5)	15.61 (×2.6)	22.95 (×2.1)	2579.47 ¹ (×2.1) 135.42 ² (×1.9)	4889.23 ¹ (×2.4) 160.49 ² (×2.2)	2371.46 ¹ (×2) 105.91 ² (×1.7)	92.05%	0.247
NASWOT	0.46 (×1.35)	8.3 (×1.37)	20.78 (×1.92)	2681.47 ¹ (×2.16) 148.65 ² (×2)	3577.14 ¹ (×1.78) 163.89 ² (×2.30)	2438.59 ¹ (×2.07) 105.03 ² (×1.68)	85.63% (-6.42%)	0.435 (+0.188)
FedMSA ($\gamma = 0.1$)	0.34	6.06	10.8	1241.79 ¹ 72.31 ²	2004.17 ¹ 71.35 ²	1175.28 ¹ 62.66 ²	90.72% (-1.3%)	0.278 (-)
RestNet20	0.27 (×2.7)	5.16 (×3.3)	7.16 (×1.2)	722.45 ¹ 29.55 ²	1560.13 ¹ (×2) 41.21 ²	675.93 ¹ 22.48 ²	89.77%	0.308
FedMSA ($\gamma = 0.2$)	0.1	1.56	5.73	754.07 ¹ (-) 59.35 ² (×2)	779.78 ¹ 47.86 ² (×1.2)	695.81 ¹ (-) 48.84 ² (×2.2)	86.21% (-3.5%)	0.424 (+0.116)
NAS (Min T _{train})	0.07	1	3.29	410.5 ¹ (-) 35.53 ² (-)	395.29 ¹ (-) 21.78 ²	393.10 ¹ (-) 31.20 ² (-)	74.9% (-4.2%)	0.743 (+0.11)
FedMSA $(\gamma = 0.8)$	0.07	1	3.29	409.03 ¹ 35.04 ²	388.13 ¹ 22.50 ² (-)	389.31 ¹ 30.82 ²	79.10%	0.633

Table 1. Result Summary Total Communication Round 140.

¹ From Raspberry Pi 4; ² From Jetson nano.

In the second group, we set $\gamma = 0.2$ for FedMSA to search a model structure and select a model structure as 20 layers ResNet, which is widely used and has good performance. FedMSA ($\gamma = 0.2$) has clear advantages in the number of parameters and FLOPs, which are important for resource-constrain and network congestion devices. Although the metrics in model forward propagation and inference latency of ResNet20 perform better on Jetson nano, many tasks are not sensitive to this small gap due to the relatively small base value. However, although the accuracy of ResNet is more than 3.5%, the model back propagation latency of FedMSA ($\gamma = 0.2$) is twice as high as on a ResNet20, and the base value of it is so large that it has a significant impact on the overall model training efficiency. Thus our algorithm gets a smaller model and a faster training speed by sacrificing some accuracy.

In the final group, we get a model structure of the same model size as the model with the smallest model size in the NAS-201-Bench search space by setting $\gamma = 0.8$ for FedMSA. At the same time, these two models have a very intimate performance regarding model forward and backward propagation latency and inference latency. It is worth noting that the model structure selected by FedMSA ($\gamma = 0.8$) has additional 4.3% accuracy over the model structure searched by NAS (Min T_{train}).

Training Time Complexity with Accuracy

The experimental result about training time complexity with accuracy is shown in Figure 5. For a distributed machine learning system, the training time complexity of the whole system decides the model training efficiency where a big training time complexity is unacceptable. The model structure search by NAS (Max Acc) has the highest accuracy in Figure 5a in 140 communication rounds, but the time spent on model training grows significantly rapidly in Figure 5b, and ended up being more than twice as high as the model structure searched by FedMSA ($\gamma = 0.1$). In the early stage of Figure 5c, the accuracy achieved of NAS (Max Acc) is much less than others. Besides, the model structure searched by FedMSA ($\gamma = 0.1$) can achieve similar accuracy with less training time growth rate, and it can be observed that through the complete training time of FedMSA ($\gamma = 0.1$), it's accuracy has always been significantly better than the NAS (Max Acc) in Figure 5c.



Figure 5. Experiments on Cifar10 under FedMSA system. (**a**) Validation accuracy of models, (**b**) Total training time spend in every communication round of models, (**c**) Changes in model accuracy over time.

Furthermore, although FedMSA ($\gamma = 0.2$) has less 3.5% accuracy than ResNet20 in Figure 5a, it has lower training time growth rate and finishes training earlier than ResNet20 in Figure 5b,c. Furthermore, although the two models searched by FedMSA ($\gamma = 0.8$) and NAS (Min Ttrain) have the same total training time and growth rate in Figure 5b, the FedMSA ($\gamma = 0.8$) has better performance during the whole training period with additional 4.2% accuracy than NAS(Min Ttrain) in Figure 5a,c.

6. Conclusions and Future Work

In this paper, we proposed FedMSA, a model selection and fast adaptation system which reduces the complexity of FL system deployment by providing automation of adaptation and deployment of training tasks as microservices in a FL life-cycle along with an optimal model selection algorithm. The evaluation shows that the model structure searched by the proposed MS algorithm consistently outperforms the model selected directly by one factor and the widely used model structure of other studies. Meanwhile, the results show that without a model selection algorithm, FL developers may end up selecting a model with more training time complexity and less accuracy compared with the model searched by a model selection algorithm from NAS search space under the same conditions.

As for future work, since our current model selection algorithm relies on the metrics of NAS-201-Bench, we will upgrade our algorithm to support more metrics of both models and distributed systems and extensively adapt our algorithm to other NAS. On the other hand, we will improve the system's ability in finer tuning and adapting the result of model selection to improve the match between the results and the developer's expectation by providing further configured parameters for developers or applying recommendation machine learning model.

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Abbreviations

The following abbreviations are used in this manuscript:

Symbol	Description
γ	User training efficiency concern rate
T_{train}	Total training time of the model to accuracy
E _{train}	Training efficiency of the model
Acc _{eval}	Expected final evaluation accuracy of the model
Loss _{eval}	Expected final evaluation loss of the model
Lat	The model inference latency in the hardware
F _{model}	The FLOPs of the model
U	Bandwidth utility of a device
В	Bandwidth of a device
Nparams	Number of parameters of the model
P	All clients platform architecture information
Μ	All metrics of the model
M_c	All cleaned metrics of the model
M_n	All normalized metrics of the model
M_s	All metrics of the model with score
M_r	All ranked model base on the score
Χ	A set of model structures
x	A model structure
S _{bit}	Storage bit of developing framework
С	all clients of FL

References

- Armbrust, M.; Fox, A.; Griffith, R.; Joseph, A.D.; Katz, R.; Konwinski, A.; Lee, G.; Patterson, D.; Rabkin, A.; Stoica, I.; et al. A view of cloud computing. *Commun. ACM* 2010, 53, 50–58. [CrossRef]
- Insider, B. The Internet of Things 2020: Here's What over 400 IoT Decision-Makers Say about the Future of Enterprise Connectivity and How IoT Companies Can Use It to Grow Revenue. May 2020. Available online: https://www.businessinsider.com/internetof-things-report?r=US&IR=T (accessed on 18 August 2022).
- Regulation (EU) 2016/679 of the European Parliament and of the Council of 27 April 2016 on the Protection of Natural Persons with Regard to the Processing of Personal Data and on the Free Movement of Such Data, and Repealing Directive 95/46/EC (General Data Protection Regulation) (Text with EEA Relevance). 4 May 2016. Available online: https://eur-lex.europa.eu/eli/ reg/2016/679/oj (accessed on 18 August 2022).
- 4. Shi, W.; Cao, J.; Zhang, Q.; Li, Y.; Xu, L. Edge computing: Vision and challenges. *IEEE Internet Things J.* 2016, 3, 637–646. [CrossRef]
- Dhar, S.; Guo, J.; Liu, J.; Tripathi, S.; Kurup, U.; Shah, M. A survey of on-device machine learning: An algorithms and learning theory perspective. ACM Trans. Internet Things 2021, 2, 1–49. [CrossRef]
- Kairouz, P.; McMahan, H.B.; Avent, B.; Bellet, A.; Bennis, M.; Bhagoji, A.N.; Bonawitz, K.; Charles, Z.; Cormode, G.; Cummings, R.; et al. Advances and open problems in federated learning. *Found. Trends[®] Mach. Learn.* 2021, 14, 1–210. [CrossRef]
- McMahan, B.; Moore, E.; Ramage, D.; Hampson, S.; y Arcas, B.A. Communication-efficient learning of deep networks from decentralized data. In Proceedings of the Artificial Intelligence and Statistics, PMLR, Fort Lauderdale, FL, USA, 20–22 April 2017; pp. 1273–1282.
- Xu, J.; Glicksberg, B.S.; Su, C.; Walker, P.; Bian, J.; Wang, F. Federated learning for healthcare informatics. *J. Healthc. Inform. Res.* 2021, 5, 1–19. [CrossRef]
- Yang, W.; Zhang, Y.; Ye, K.; Li, L.; Xu, C.Z. Ffd: A federated learning based method for credit card fraud detection. In Proceedings of the International Conference on Big Data, Los Angeles, CA, USA, 9–12 December 2019; Springer: Berlin/Heidelberg, Germany, 2019; pp. 18–32.
- Pokhrel, S.R.; Choi, J. A decentralized federated learning approach for connected autonomous vehicles. In Proceedings of the 2020 IEEE Wireless Communications and Networking Conference Workshops (WCNCW), Seoul, Korea, 25–28 May 2020; IEEE: Hoboken, NJ, USA, 2020; pp. 1–6.

- 11. Lee, J.; Ko, H.; Pack, S. Adaptive Deadline Determination for Mobile Device Selection in Federated Learning. *IEEE Trans. Veh. Technol.* **2021**, *71*, 3367–3371. [CrossRef]
- Lu, N.; Wang, Z.; Li, X.; Niu, G.; Dou, Q.; Sugiyama, M. Federated Learning from Only Unlabeled Data with Class-Conditional-Sharing Clients. arXiv 2022, arXiv:2204.03304.
- He, C.; Mushtaq, E.; Ding, J.; Avestimehr, S. Fednas: Federated Deep Learning via Neural Architecture Search. 2021. Available online: https://openreview.net/forum?id=10HZX4YDqhT (accessed on 18 August 2022).
- 14. Ren, P.; Xiao, Y.; Chang, X.; Huang, P.Y.; Li, Z.; Chen, X.; Wang, X. A comprehensive survey of neural architecture search: Challenges and solutions. *ACM Comput. Surv. (CSUR)* **2021**, *54*, 1–34. [CrossRef]
- 15. Li, C.; Yu, Z.; Fu, Y.; Zhang, Y.; Zhao, Y.; You, H.; Yu, Q.; Wang, Y.; Lin, Y. HW-NAS-Bench: Hardware-aware neural architecture search benchmark. *arXiv* **2021**, arXiv:2103.10584.
- 16. Dong, X.; Yang, Y. NAS-Bench-201: Extending the Scope of Reproducible Neural Architecture Search. In Proceedings of the International Conference on Learning Representations (ICLR), Addis Ababa, Ethiopia, 26–30 April 2020.
- Song, F.; Li, L.; You, I.; Yu, S.; Zhang, H. Optimizing High-Speed Mobile Networks with Smart Collaborative Theory. *IEEE Wirel. Commun.* 2022, 29, 48–54. [CrossRef]
- 18. Wei, J.; Chu, X.; Sun, X.Y.; Xu, K.; Deng, H.X.; Chen, J.; Wei, Z.; Lei, M. Machine learning in materials science. *InfoMat* 2019, 1, 338–358. [CrossRef]
- Jordan, M.I.; Mitchell, T.M. Machine learning: Trends, perspectives, and prospects. *Science* 2015, 349, 255–260. [CrossRef] [PubMed]
- 20. Krizhevsky, A.; Sutskever, I.; Hinton, G.E. Imagenet classification with deep convolutional neural networks. *Adv. Neural Inf. Process. Syst.* **2012**, 25. [CrossRef]
- He, K.; Zhang, X.; Ren, S.; Sun, J. Deep residual learning for image recognition. In Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition, Las Vegas, NV, USA, 27–30 June 2016; pp. 770–778.
- Vaswani, A.; Shazeer, N.; Parmar, N.; Uszkoreit, J.; Jones, L.; Gomez, A.N.; Kaiser, Ł.; Polosukhin, I. Attention is all you need. Adv. Neural Inf. Process. Syst. 2017, 30.
- 23. Devlin, J.; Chang, M.W.; Lee, K.; Toutanova, K. Bert: Pre-training of deep bidirectional transformers for language understanding. *arXiv* **2018**, arXiv:1810.04805.
- 24. Zoph, B.; Le, Q.V. Neural architecture search with reinforcement learning. *arXiv* 2016, arXiv:1611.01578.
- Ghiasi, G.; Lin, T.Y.; Le, Q.V. Nas-fpn: Learning scalable feature pyramid architecture for object detection. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition, Long Beach, CA, USA, 15–20 June 2019; pp. 7036–7045.
- 26. Peng, J.; Sun, M.; ZHANG, Z.X.; Tan, T.; Yan, J. Efficient neural architecture transformation search in channel-level for object detection. *Adv. Neural Inf. Process. Syst.* **2019**, *32*.
- 27. Fan, Z.; Hu, G.; Sun, X.; Wang, G.; Dong, J.; Su, C. Self-attention neural architecture search for semantic image segmentation. *Knowl.-Based Syst.* **2022**, *239*, 107968. [CrossRef]
- Liu, C.; Chen, L.C.; Schroff, F.; Adam, H.; Hua, W.; Yuille, A.L.; Li, F.-F. Auto-deeplab: Hierarchical neural architecture search for semantic image segmentation. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition, Long Beach, CA, USA, 15–20 June 2019; pp. 82–92.
- Nekrasov, V.; Chen, H.; Shen, C.; Reid, I. Fast neural architecture search of compact semantic segmentation models via auxiliary cells. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition, Long Beach, CA, USA, 15–20 June 2019; pp. 9126–9135.
- Liu, C.; Zoph, B.; Neumann, M.; Shlens, J.; Hua, W.; Li, L.J.; Li, F.-F.; Yuille, A.; Huang, J.; Murphy, K. Progressive neural architecture search. In Proceedings of the European conference on computer vision (ECCV), Munich, Germany, 8–14 September 2018; pp. 19–34.
- 31. Fedorov, I.; Adams, R.P.; Mattina, M.; Whatmough, P. Sparse: Sparse architecture search for cnns on resource-constrained microcontrollers. *Adv. Neural Inf. Process. Syst.* **2019**, 32.
- Zhang, L.L.; Yang, Y.; Jiang, Y.; Zhu, W.; Liu, Y. Fast hardware-aware neural architecture search. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition Workshops, Seattle, WA, USA, 14–19 June 2020; pp. 692–693.
- Tan, M.; Chen, B.; Pang, R.; Vasudevan, V.; Sandler, M.; Howard, A.; Le, Q.V. Mnasnet: Platform-aware neural architecture search for mobile. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition, Long Beach, CA, USA, 15–20 June 2019; pp. 2820–2828.
- Wu, B.; Dai, X.; Zhang, P.; Wang, Y.; Sun, F.; Wu, Y.; Tian, Y.; Vajda, P.; Jia, Y.; Keutzer, K. Fbnet: Hardware-aware efficient convnet design via differentiable neural architecture search. In Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition, Long Beach, CA, USA, 15–20 June 2019; pp. 10734–10742.
- Lin, J.; Chen, W.; Cai, H.; Gan, C.; Han, S. MCUNetV2: Memory-Efficient Patch-based Inference for Tiny Deep Learning. *arXiv* 2021, arXiv:2110.15352.
- Taylor, B.; Marco, V.S.; Wolff, W.; Elkhatib, Y.; Wang, Z. Adaptive deep learning model selection on embedded systems. ACM SIGPLAN Not. 2018, 53, 31–43. [CrossRef]
- Meiseles, A.; Rokach, L. Source model selection for deep learning in the time series domain. *IEEE Access* 2020, *8*, 6190–6200. [CrossRef]

- 38. Beutel, D.J.; Topal, T.; Mathur, A.; Qiu, X.; Parcollet, T.; Lane, N.D. Flower: A Friendly Federated Learning Research Framework. *arXiv* 2020, arXiv:2007.14390.
- 39. Liu, Y.; Fan, T.; Chen, T.; Xu, Q.; Yang, Q. FATE: An industrial grade platform for collaborative learning with data protection. *J. Mach. Learn. Res.* 2021, 22, 1–6.
- 40. He, C.; Li, S.; So, J.; Zhang, M.; Wang, H.; Wang, X.; Vepakomma, P.; Singh, A.; Qiu, H.; Shen, L.; et al. FedML: A Research Library and Benchmark for Federated Machine Learning. *arXiv* 2020, arXiv:2007.13518.
- 41. Han, J.; Kamber, M.; Pei, J. Data Mining: Concepts and Techniques; Morgan Kaufmann: San Francisco, CA, USA, 2006.
- 42. Krizhevsky, A.; Hinton, G. Learning Multiple Layers of Features from Tiny Images. 2009. Available online: https://citeseerx.ist. psu.edu/viewdoc/download?doi=10.1.1.222.9220&rep=rep1&type=pdf (accessed on 18 August 2022).
- 43. Mellor, J.; Turner, J.; Storkey, A.; Crowley, E.J. Neural architecture search without training. In Proceedings of the International Conference on Machine Learning, PMLR, Virtual, 18–24 July 2021; pp. 7588–7598.