

Article

# Temperature Gradient Method for Alleviating Bonding-Induced Warpage in a High-Precision Capacitive MEMS Accelerometer

Dandan Liu <sup>1</sup>, Huafeng Liu <sup>1</sup> , Jinquan Liu <sup>1</sup>, Fangjing Hu <sup>1</sup> , Ji Fan <sup>1,2</sup>, Wenjie Wu <sup>1,\*</sup>   
and Liangcheng Tu <sup>1,2,\*</sup> 

<sup>1</sup> MOE Key Laboratory of Fundamental Physical Quantities Measurement, School of Physic, Huazhong University of Science and Technology, Wuhan 430074, China; liudandan@hust.edu.cn (D.L.); huafengliu@hust.edu.cn (H.L.); jinquanliu@hust.edu.cn (J.L.); fangjing\_hu@hust.edu.cn (F.H.); fanji@hust.edu.cn (J.F.)

<sup>2</sup> Institute of Geophysics and PGMF, Huazhong University of Science and Technology, Wuhan 430074, China

\* Correspondence: wjwu@hust.edu.cn (W.W.); tlc@hust.edu.cn (L.T.);  
Tel.: +86-27-8754-3880 (W.W.); +86-27-8755-8394 (L.T.)

Received: 10 January 2020; Accepted: 19 February 2020; Published: 21 February 2020



**Abstract:** Capacitive MEMS accelerometers with area-variable periodic-electrode displacement transducers found wide applications in disaster monitoring, resource exploration and inertial navigation. The bonding-induced warpage, due to the difference in the coefficients of thermal expansion of the bonded slices, has a negative influence on the precise control of the interelectrode spacing that is essential to the sensitivity of accelerometers. In this work, we propose the theory, simulation and experiment of a method that can alleviate both the stress and the warpage by applying different bonding temperature on the bonded slices. A quasi-zero warpage is achieved experimentally, proving the feasibility of the method. As a benefit of the flat surface, the spacing of the capacitive displacement transducer can be precisely controlled, improving the self-noise of the accelerometer to 6 ng/ $\sqrt{\text{Hz}}$  @0.07 Hz, which is about two times lower than that of the accelerometer using a uniform-temperature bonding process.

**Keywords:** MEMS accelerometer; capacitive transducer; temperature gradient; bonding warpage

## 1. Introduction

Recently, high-precision MEMS accelerometers are emerging for applications in precision gravity measurements [1], seismic monitoring [2], resource exploration [3] and inertial navigations [4], showing advantages in terms of both the size and the cost [5]. Specially, MEMS accelerometers using an area-variable periodic-electrode displacement transducer (APDT) are some of the most widely used MEMS accelerometers, due to their good linearity, high sensitivity and mature back-end electronics [5–8].

The APDT is formed by two electrode arrays on a moveless glass top cap and a matching electrode array on the movable silicon proof mass through a high-temperature bonding process [9]. Caused by the difference in the coefficients of thermal expansion (CTE) of glass and silicon, the bonding-induced stress and warpage are unavoidable. The uneven surfaces go against the precise control of the interelectrode spacing of the APDT, which is essential to achieve a high sensitivity for the displacement transducer, as demonstrated in our previous works [10,11]. In addition, the bonding-induced stress may damage the device or affect the long-term stability due to slow-releasing of the stress [12–14].

Various approaches have been reported for reducing the bonding-induced stress and warpage. The anchor was isolated from the spring-mass system by using a stress-isolation guard-ring structure in Reference [15]. However, it not only made the structure complicated, but also enlarged the size

of the devices. A circular disk located in the center of the device and eight L-shaped elastic beams were used to isolate the bonding-induced stress in Reference [16]. However, it affect both the bonding strength and the structural stability. The bonding-induced warpage was reduced by selecting materials with similar CTE in References [17–19]. However, the method is not universal due to its limited choice of materials.

In this paper, we propose the theory, simulation and experimental demonstration of a new bonding strategy to alleviate the bonding-induced stress and warpage in a high-precision MEMS accelerometer. By applying different bonding temperatures to the bonded slices to adjust their shrinkage, the bonding induced warpage is reduced to quasi-zero. Being low-cost and convenient, the proposed bonding technology shows great prospect in industrial applications.

## 2. Design

A typical MEMS accelerometer using an APDT consists of a glass top cap and a silicon spring-mass system. Two columns of arrayed electrodes on the top cap and one column of arrayed electrodes on the spring-mass system form the APDT via a bonding process at a temperature over 504 K, as shown in Figure 1. A low-frequency acceleration variation ( $\Delta a$ ) is translated to the displacement of the proof-mass ( $\Delta x$ ) by

$$\Delta x = -\frac{1}{(2\pi f_0)^2} \times \Delta a, \quad (1)$$

where  $f_0$  is the resonant frequency of the spring-mass structure. Then the displacement is theoretically sensed by the ADPT, given by

$$\Delta C = \frac{2N \times \varepsilon \times l'}{d} \times \Delta x \quad (2)$$

where  $\Delta C$  is the capacitance variation of the APDT,  $N$  is the number of electrodes in one array,  $l'$  is the length of the electrodes and  $d$  is the spacing between the opposite electrode arrays [10]. It should be noted in Equation (2) that reducing the interelectrode spacing is essential for increasing the scale factor of the displacement transducer ( $G_{x-c}$ ). The capacitive changes can be detected using a lock-in amplifier circuit or highly sensitive low-noise quartz methods [20–22]. As the noise of the circuit ( $Noise_{electronics}$ ) is dominant in the self-noise of most MEMS accelerometers [11,23], increasing the scale factor of the capacitive transducer will lower the self-noise of the accelerometer ( $Noise_{accelerometer}$ ), which is given by

$$Noise_{acceleration} = \frac{Noise_{electronics}}{G_{a-x}G_{x-c}} \quad (3)$$

where  $G_{a-x}$  is the scale factor of the spring-mass system [11]. According to Equation (3), the noise of the accelerometer can be effectively decreased by reducing the interelectrode spacing. Since the warpage caused by the bonding process directly affects the interelectrode spacing, reducing the bonding warpage can effectively reduce the noise of the accelerometer.

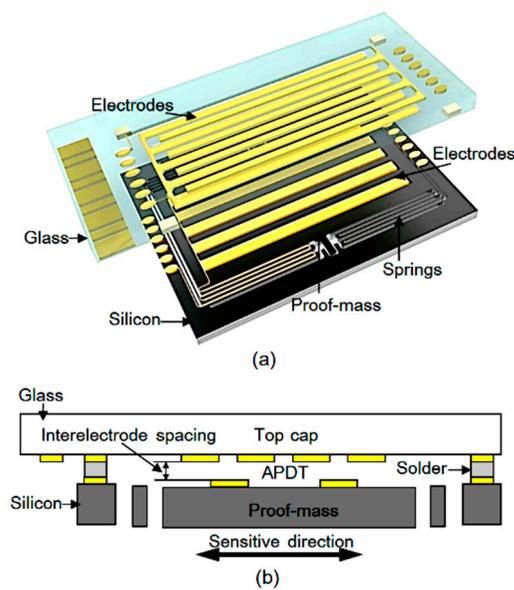
When the accelerometer was cooled down to room temperature after the bonding process, the shrinkage of the top cap and the spring-mass system is respectively represented by

$$S_{Top} = \overline{CTE_{glass}} \times (T - 23), \quad (4)$$

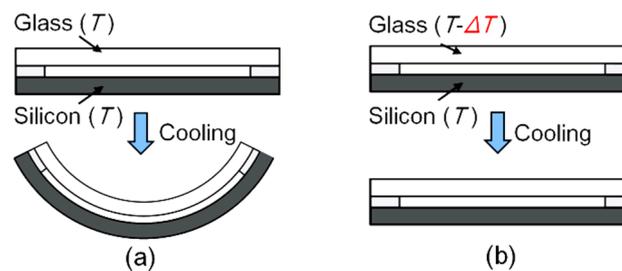
and

$$S_{spring} = \overline{CTE_{silicon}} \times (T - 23), \quad (5)$$

where  $\overline{CTE_{glass}}$  is the CTE of glass,  $T$  is the melting point of the solder and  $\overline{CTE_{silicon}}$  is the CTE of Silicon. Because of the difference in CTE for glass and silicon, the deformation of the top cap is different from the spring-mass system, which induces stress and surface warpage, as shown in Figure 2a. It is clearly illustrated that the bonding-induced warpage limits the precise control of the interelectrode spacing, which is of great importance to the sensitivity of the ADPT [10].



**Figure 1.** Schematic of the MEMS accelerometer using an area-variable periodic-electrode displacement transducer (APDT); (a) three-dimensional model; (b) cross-section view.



**Figure 2.** Schematic of bonding-induced warpage; (a) traditional bonding process with uniform high temperature; (b) the proposed bonding process with different high temperatures applied on the top cap and the spring-mass system.

In order to alleviate the bonding-induced warpage, we propose a new bonding strategy by applying different high temperatures to the top cap and spring-mass system. The temperature difference ( $\Delta T$ ) is set based on the CTE difference of glass and silicon to provide the same shrinkage between the two slices, given by:

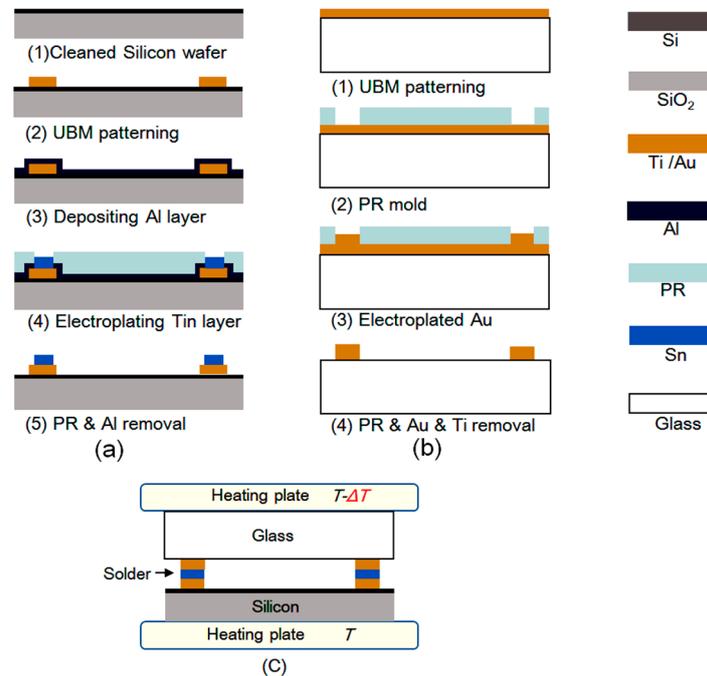
$$\overline{CTE}_{glass} \times (T - \Delta T - 23) = \overline{CTE}_{silicon} \times (T - 23). \quad (6)$$

According to Equation (6), the bonding-induced warpage and stress can be eliminated theoretically, as shown in Figure 2b.

### 3. Experiment and Simulation

Five samples with various temperature differences applied to the top cap and the spring-mass structure were bonded to validate the proposed method. The process flow started from a four-inch n-type silicon wafer with a thickness of 500  $\mu\text{m}$ . A 200-nm-thick SiO<sub>2</sub> layer was deposited on the surface (Figure 3). Firstly, the wafer was successively cleaned with acetone and isopropanol. Then, a patterned under-bump metal (UBM) consisting of a 40-nm-thick titanium (Ti) and a 200-nm-thick gold (Au) was deposited with electron beam evaporation followed by a lift-off process. Afterwards, a 100-nm-thick aluminium (Al) seed layer was deposited as conducting layer for electroplating [24]. Afterwards, a patterned Tin layer with a thickness of 20  $\mu\text{m}$  was electroplated, followed by removing the Photoresist and seed layer by wet etching. The four-inch glass wafer (Borofloat 33) was fabricated by a similar

process, except that the electroplated metal was gold with a thickness of 2.5  $\mu\text{m}$ . The processed glass and silicon wafer were sawed into slices by a precision dicing machine (DS620). Finally, the glass slice and the silicon slices were bonded together by reflow soldering.



**Figure 3.** Process of the tested samples; (a) fabrication of the silicon slice; (b) fabrication of the glass slice; (c) bonding process with different temperature applied to the silicon slice and the glass slice.

The bonding process was carried out in a FINEPLACER @ sigma which can directly show the real-time temperature of the silicon slice and glass slice. The temperature applied to the silicon slice was 504 K, which is the melting point of Tin. The temperature applied to the glass slice was different for the five samples, as shown in Table 1.

**Table 1.** Temperature applied to the silicon slice and glass slice.

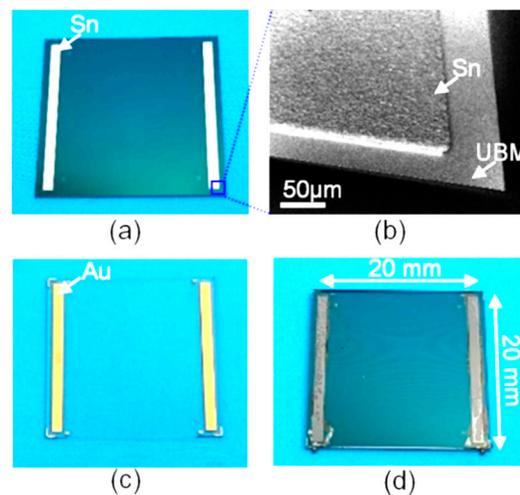
No.	1	2	3	4	5
Si	504 K				
Glass	475 K	486 K	493 K	506 K	511 K
$\Delta T (T_{Si} - T_{glass})$	-29 K	-18 K	-11 K	+2 K	+7 K

In order to verify the proposed bonding strategy, the bonding process was simulated by finite element analysis (FEA) using COMSOL Multiphysics. The three-dimensional geometry and the constraints were identical to the experimental model. The simulation consisted of two steps: Firstly, a thermal analysis was simulated by applying different temperatures on the surface of the glass and silicon based on the experiment. Secondly, the results of the thermal analysis were used as inputs for statics analysis, which obtains the bonding warpage. It should be noted that only elastic deformation was considered in the calculation and simulation for simplification.

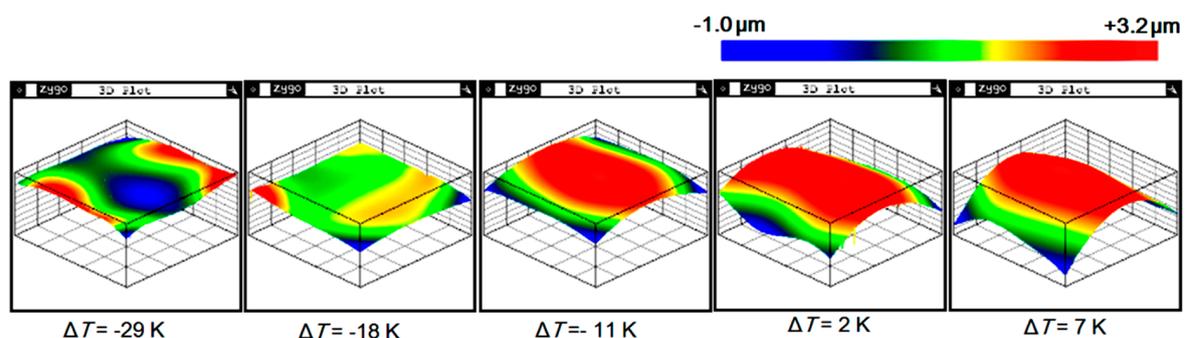
#### 4. Results

A fabricated sample was shown in Figure 4. The warpage of the samples was measured using a white light scanning profiler (Zygo NewViewTM7100). The surface topography of the samples was highly relevant to the temperature difference of the two bonded slices, as shown in Figure 5. The topography of the top caps had a hump-up tendency when the temperature applied to the top

cap increased. The quantitative warpage was shown in Figure 6, which was defined by the height in the center subtracting that on the corner of the top cap's surface. In order to verify the testing results, calculation results based on Equation (A3) of the Appendix A and the FEA results were used for comparison. The experiment, calculation and simulation results showed the same tendency that the warpage (positive when convex to the silicon and negative when convex to the glass) decreased with the enlargement of the glass temperature when the silicon temperature remains unchanged. When the temperature difference was  $-22$  K, the warpage approached zero. The errors between the experimental, calculation and simulation results were within 23%. The experimental warpage was less than that of the calculation and simulation when the bonding warpage was large. The reason was probably the plastic deformation, which was not considered in the calculation and simulation, which would have released partial bonding stress.

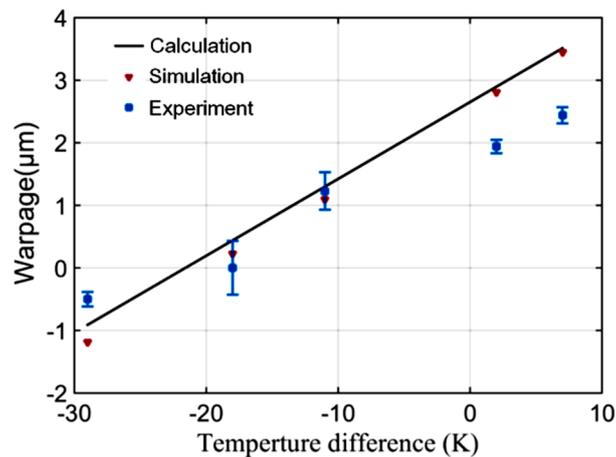


**Figure 4.** Photography of a fabricated sample; (a) silicon slice; (b) zoomed-in SEM picture of the Tin layer on the silicon slice; (c) glass slice; (d) bonded sample.

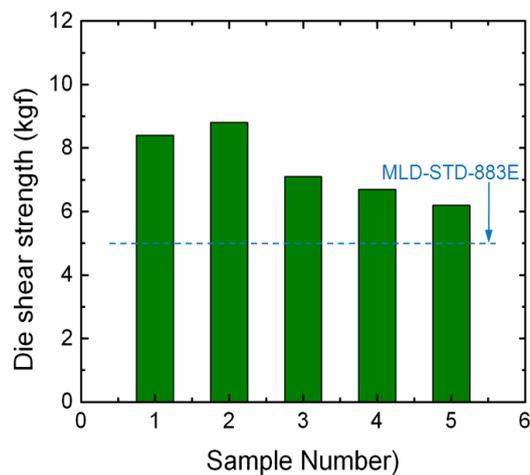


**Figure 5.** Topography of the glass slice in the tested samples with various temperature differences applied to the glass slice and the silicon slice.

In order to verify the bonding strength of the proposed bonding process, all five samples were tested using a multi-function push-pull tester (XYZTEC @Condor Sigma Lite). Figure 7 shows the measured slice shear strength of the samples. Based on the MIL-STD-883E standards, the bonding strength was required to be greater than 5.0 kgf as the bonding area was 0.07 square inches. Hence the strength of all the samples using the proposed bonding process met the MLD-STD-883E standards.



**Figure 6.** Experimental warpage of the tested samples with theoretical and simulated results as a reference.



**Figure 7.** Shear strength of the samples bonded with different temperature gradients.

Figure 8 shows the bonded chip and the assembled accelerometer with the circuit. The low-warpage bonding process was applied to the development of a high-precise MEMS accelerometer using an APDT that has been discussed in our previous publications [11]. The design and fabrication were the same as Reference [11], except for the bonding process. Beneficial from the flat surface by applying a proper temperature difference to the glass cap and the spring-mass structure, the interelectrode spacing was able to be controlled precisely. The accelerometer using the proposed bonding process had an interelectrode spacing of 8  $\mu\text{m}$ , which was approximately two-thirds of that in accelerometer bonded with a uniform temperature. In order to calibrate the self-noise, the accelerometer bonded by the proposed strategy and another accelerometer bonded by a traditional process were tested statically in our cave laboratory with a quiet environment. A commercial seismometer (CMG-3EPS, GURALP) was installed adjacent to our accelerometers as a reference, as shown in Figure 9a. The results shows that the MEMS accelerometer bonded by the proposed strategy obtained an ultra-low self-noise of 6  $\text{ng}/\sqrt{\text{Hz}}$  @ 0.07 Hz, which was two times lower than that of the accelerometer bonded using a uniform temperature, as shown in Figure 9b. The MEMS accelerometer is one of the most sensitive MEMS accelerometer in the world, as shown in Table 2. It is worth emphasizing that there is no need for addition mechanical structures, metal films or process steps for the proposed bonding strategy, which shows great prospect in industrial applications compared with those methods introduced in Section 1.

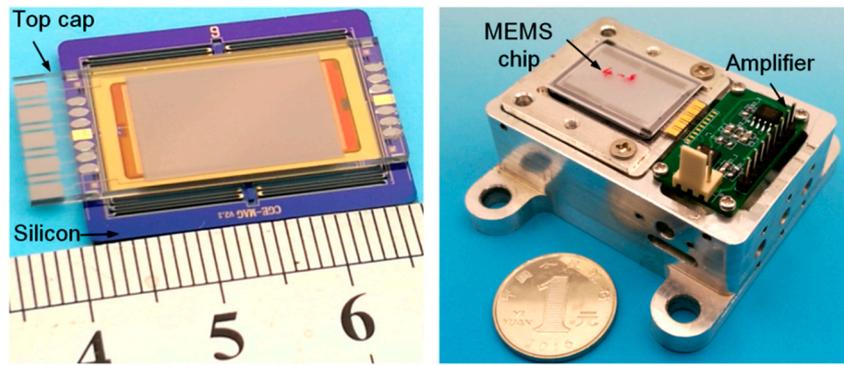


Figure 8. MEMS accelerometer using the proposed bonding process.

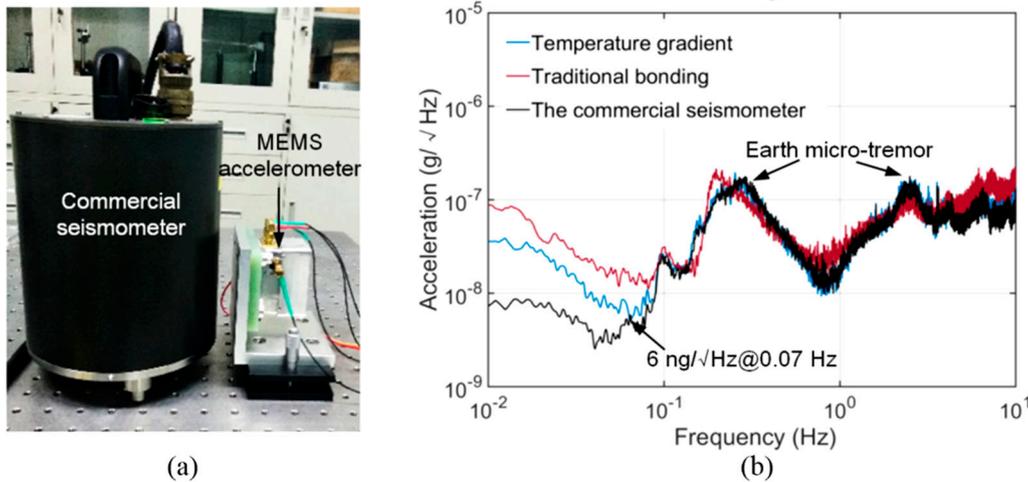


Figure 9. (a) Calibrating the self-noise of the MEMS accelerometer with a commercial seismometer for reference. (b) The power spectral density (PSD) of the output of the MEMS accelerometers and the commercial seismometer. The earth micro-tremor is dominant in the noise floor for the bandwidth above 0.1 Hz. For the bandwidth below 0.05 Hz, the self-noise of the sensors is dominant.

Table 2. The self-noise comparison between the proposed MEMS accelerometer and typical high-precision MEMS accelerometers.

Accelerometers	University of Glasgow [1]	Imperial College [23]	Hewlett Packard [3]	This Work
Applications	Gravimeter	Seismic Sensor	Seismic Sensor	Acceleration Sensor
Self-noise (ng/√Hz)	10	2	10	6

### 5. Conclusions

A new bonding strategy for alleviating the bonding-induced warpage was proposed in this paper, which effectively optimized a nano-g MEMS accelerometer. The effect of the bonding strategy was validated by calculation, simulation and experiment with errors within 23%. The interelectrode spacing of the proposed bonding process was 1.5 times better than the accelerometer bonded with a uniform temperature, and the noise floor was 6 ng/√Hz @ 0.07 Hz, which is two times better than other accelerometers. The strategy has no need for additional process steps and special materials, which allows us to use low-cost materials for bonding. In addition to accelerometer applications, the bonding technology also plays a key role in the semiconductor industry for forming multi-layer-structure devices and providing mechanical supports, as well as for ambient-disturbance isolation and input-output interfaces for MEMS devices or integrated circuits. The proposed strategy shows great prospect in

industrial applications such as alleviating the bonding stress and warpage of MEMS-based sensors or integrated circuits as it is cheap, convenient and compatible with the MEMS process.

**Author Contributions:** Conceptualization, W.W. and L.T.; Data curation, H.L., J.L., F.H. and J.F.; Formal analysis, D.L.; Funding acquisition, W.W.; Project administration, D.L.; Writing—original draft, D.L. and W.W.; Writing—review & editing, F.H. and L.T. All authors have read and agreed to the published version of the manuscript.

**Funding:** This work was partially supported by The National Key Research and Development Program of China (Grant No. 2017YFC0601603 and 2018YFC0603301), the Natural Science Foundation of China (Grant Nos. 41874089, 61871439 and 41904157), the China Postdoctoral Science Foundation Funded Project (2019T120652) and the HUST Key Innovation Team Foundation for Interdisciplinary Promotion (Grant No. 2016JCTD102). We thank the staff in our laboratory, especially D. H. Peng, for help and support in undertaking the MEMS fabrication.

**Acknowledgments:** We thank Shihao Tang for his constructive suggestions for this paper.

**Conflicts of Interest:** The authors declare no conflicts of interest.

## Appendix A

The shape of the intermediate solder layer is different from the shape of the top cap and spring-mass system, which makes the model complicated (Figure A1a). In order to simplify the model, we used an effective medium model to replace the intermediate solder layer with another material that covers the whole surface and the thickness of solders were consistent, which has been widely used in the calculation of warpage in multilayers [25,26], with an effective Young's modulus of  $E_2$ , Poisson's ratio of  $\nu_2$  and CTE of  $\alpha_2$ , as shown in Figure A1b.

The material properties of the intermediate equivalent solder layer are given as

$$M_{eq-solder} = \frac{2l}{L} * M_{solder}, \quad (A1)$$

where  $M_{solder}$  represents the material properties of the intermediate solder layer. Hence, the structure was transferred to a typical three-layer model.

The Stoney equation is widely used to calculate the thermal warpage for multilayer structures [25–32]. When a sample's warpage ( $w$ ) is much smaller than its dimensions [31], the warpage can be expressed as

$$w = \frac{L^2}{2r}, \quad (A2)$$

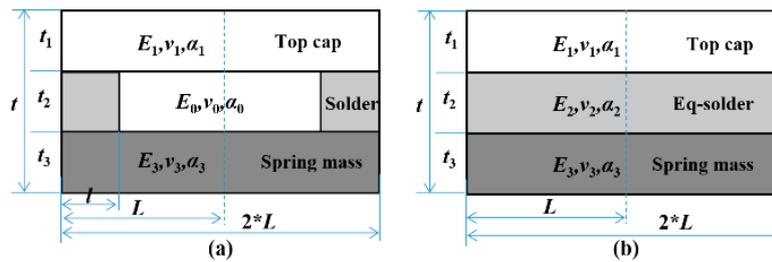
where  $L$  is half the length of the sample and  $r$  is the radius of curvature, which is given in Reference [31] as:

$$\frac{1}{r} = \frac{(A - B)\alpha_2(T_2 - T_0) - A\alpha_1(T_1 - T_0) + B\alpha_3(T_3 - T_0)}{C + D}, \quad (A3)$$

in which,

$$\begin{cases} A = 6E_1t_1[E_2t_2t_1 + E_2t_2^2 + E_3t_3(t_1 + 2t_2 + t_3)] \\ B = 6E_3t_3[E_2t_2t_3 + E_2t_2^2 + E_1t_1(t_1 + 2t_2 + t_3)] \\ C = \sum_{i=1}^3 E_it_iE_{i+1}t_{i+1}(4t_i^2 + 4t_{i+1}^2 + 6t_it_{i+1}) + \sum_{i=1}^3 E_i^2t_i^2 \\ D = E_1t_1E_3t_3(4t_1^2 + 4t_3^2 + 6t_1t_3 + 12h_2(t_1 + t_2 + t_3)) \end{cases} \quad (A4)$$

where  $E_i$  ( $i = 1, 2$ , and  $3$ ) represents the Young's modulus,  $t_i$  ( $i = 1, 2$ , and  $3$ ) represents the thickness,  $\alpha_i$  ( $i = 1, 2$ , and  $3$ ) represents the CTE,  $T_i$  ( $i = 1, 2$ , and  $3$ ) represents the applied temperature and  $T_0$  is the room temperature, where  $i = 1$  refers to the top cap,  $i = 2$  refers to the equivalent solder and  $i = 3$  refers to the spring-mass system.



**Figure A1.** Schematic of the three-layer structure of the accelerometer; (a) original model; (b) equivalent model.

Specially, the Young's modulus and CTE of silicon is related to the temperature [25,33], which is respectively given as

$$E_3 = 1.6806 \times 10^{11} - 8.2225 \times 10^6 T - 5.9816 \times 10^3 T^2, \quad (\text{A5})$$

and,

$$\alpha_3 = \left( 3.725 \times \left( 1 - e^{-5.88 \times 10^{-3}(T-124)} \right) + 5.548 \times 10^{-4} T \right) \times 10^{-6}, \quad (\text{A6})$$

where  $T$  is the temperature of silicon.

The value of parameters used for calculation are listed in Table A1. With such model, the warpage can be calculated.

**Table A1.** Key Parameters Used in this Paper.

	$E$ (GPa)	$\nu$	CTE (ppmK <sup>-1</sup> )	Thermal Conductivity (W/m.K)	Thickness ( $\mu\text{m}$ )	Width (mm)	Length (mm)
Spring-mass	Equation (A5)	0.28	Equation (A6)	13.1	500	20	20
Top cap	72.4	0.25	3.25	1.2	500	20	20
Eq-solder	6.53	0.04	2.62	11.9	22.0	20	20

## References

- Middlemiss, R.P.; Samarelli, A.; Paul, D.J.; Hough, J.; Rowan, S.; Hammond, G.D. Measurement of the Earth tides with a MEMS gravimeter. *Nature* **2016**, *531*, 614–617. [[CrossRef](#)] [[PubMed](#)]
- Wu, W.J.; Li, Z.; Liu, J.Q.; Fan, J.; Tu, L.C. A nano-g MEMS accelerometer for earthquake monitoring. In Proceedings of the 19th International Conference on Solid-State Sensors, Actuators and Microsystems (TRANSDUCERS), Kaohsiung, Taiwan, 18–22 June 2017; pp. 599–602.
- Milligan, D.J.; Homeijer, B.D.; Walmsley, R.G. An ultra-low noise MEMS accelerometer for seismic imaging. *Proc. IEEE Sens.* **2011**, 1281–1284. [[CrossRef](#)]
- Zwahlen, P.; Dong, Y.; Nguyen, A.-M.; Rudolf, F.; Stauffer, J.-M.; Ullah, P.; Ragot, V. Breakthrough in high performance inertial navigation grade Sigma-Delta MEMS accelerometer. In Proceedings of the 2012 IEEE/ION Position, Location and Navigation Symposium, Myrtle Beach, SC, USA, 23–26 April 2012; pp. 15–19.
- Pike, W.T.; Kumar, S. Improved design of micromachined lateral suspensions using intermediate frames. *J. Micromech. Microeng.* **2007**, *17*, 1680–1694. [[CrossRef](#)]
- Walmsley, R.G.; Kiyama, L.K.; Milligan, D.M.; Alley, R.L.; Erickson, D.L.; Hartwell, P.G. Micro-G silicon accelerometer using surface electrodes. In Proceedings of the 2009 IEEE Sensors, Christchurch, New Zealand, 25–28 October 2009; pp. 971–974.
- Benmessaoud, M.; Nasreddine, M.M. Optimization of MEMS capacitive accelerometer. *Microsyst. Technol.* **2013**, *19*, 713–720. [[CrossRef](#)]
- Keshavarzi, M.; YavandHasani, J. Design and optimization of fully differential capacitive MEMS accelerometer based on surface micromachining. *Microsyst. Technol.* **2019**, *25*, 1369–1377. [[CrossRef](#)]

9. Tez, S.; Torunbalci, M.M.; Akin, T. A novel method for fabricating MEMS three-axis accelerometers using low temperature Au-Sn eutectic bonding. In Proceedings of the 2016 IEEE SENSORS, Orlando, FL, USA, 30 October–3 November 2016; pp. 1–3.
10. Wu, W.J.; Zheng, P.P.; Liu, J.Q.; Li, Z.; Fan, J.; Liu, H.F.; Tu, L.C. High-Sensitivity Encoder-Like Micro Area-Changed Capacitive Transducer for a Nano-g Micro Accelerometer. *Sensors* **2017**, *17*, 2158. [[CrossRef](#)]
11. Wu, W.J.; Liu, D.D.; Qiu, W.R.; Liu, H.F.; Hu, F.J.; Fan, J.; Hu, C.Y.; Tu, L.C. A precise spacing-control method in MEMS packaging for capacitive accelerometer applications. *J. Micromech. Microeng.* **2018**, *28*, 125016. [[CrossRef](#)]
12. Jayaram, V.; McCann, S.; Huang, T.-C.; Kawamoto, S.; Pulugurtha, R.; Smet, V.; Tummala, R. Thermocompression Bonding Process Design and Optimization for Warpage Mitigation of Ultra-Thin Low-CTE Package Assemblies. In Proceedings of the 2016 IEEE 66th Electronic Components and Technology Conference (ECTC), Las Vegas, NV, USA, 31 May–3 June 2016; pp. 101–107.
13. Yao, W.; Belhenini, S.; Roqueta, F.; Pujos, C.; Bruno, E.; Gardes, P.; Tougui, A. Intrinsic stress effects on the warpage of silicon substrate during thin film deposition, photolithography and etching processes. In Proceedings of the 2016 17th International Conference on Thermal, Mechanical and Multi-Physics Simulation and Experiments in Microelectronics and Microsystems (EuroSimE), Montpellier, France, 18–20 April 2016; pp. 1–6.
14. Wang, P.J.; Kim, J.S.; Lee, C.C. A new bonding technology dealing with large CTE mismatch between large Si chips and Cu substrates. In Proceedings of the 2008 58th Electronic Components and Technology Conference, Lake Buena Vista, FL, USA, 27–30 May 2008; pp. 1562–1568.
15. Hsieh, H.S.; Chang, H.C.; Hu, C.F.; Cheng, C.L.; Fang, W. A novel stress isolation guard-ring design for the improvement of a three-axis piezoresistive accelerometer. *J. Micromech. Microeng.* **2011**, *21*, 1412–1415. [[CrossRef](#)]
16. Hao, Y.; Yuan, W.; Xie, J.; Shen, Q.; Chang, H. Design and Verification of a Structure for Isolating Packaging Stress in SOI MEMS Devices. *IEEE Sens. J.* **2017**, *17*, 1246–1254. [[CrossRef](#)]
17. Zhang, Z.; Wu, J.; Bernard, S.; Walmsley, R.G. Chip on Board development for a novel MEMS accelerometer for seismic imaging. In Proceedings of the 2012 IEEE 62nd Electronic Components and Technology Conference, San Diego, CA, USA, 29 May–1 June 2012; pp. 350–355.
18. Rogers, T. Considerations of anodic bonding for capacitive type silicon/glass sensor fabrication. *J. Micromech. Microeng.* **1992**, *2*, 164–166. [[CrossRef](#)]
19. Rogers, T.; Kowal, J. Selection of glass, anodic bonding conditions and material compatibility for silicon-glass capacitive sensors. *Sens. Actuators A* **1995**, *46*, 113–120. [[CrossRef](#)]
20. Matko, V.; Milanovic, M. Temperature-compensated capacitance-frequency converter with high resolution. *Sens. Actuators A Phys.* **2014**, *220*, 262–269. [[CrossRef](#)]
21. Ivanov, V. Direct electro-optic effect in langasites and  $\alpha$ -quartz. *Opt. Mater.* **2018**, *79*, 1–7. [[CrossRef](#)]
22. Matko, V.; Šafarić, R. Major improvements of quartz crystal pulling sensitivity and linearity using series reactance. *Sensors* **2009**, *9*, 8263–8270. [[CrossRef](#)]
23. Pike, W.T.; Delahunty, A.K.; Mukherjee, A.; Dou, G.; Liu, H.; Calcutt, S.; Standley, I.M. A self-levelling nano-g silicon seismometer. In Proceedings of the IEEE SENSORS 2014 Proceedings, Valencia, Spain, 2–5 November 2014; pp. 1599–1602.
24. Wei, X.L.; Liu, J.Q.; Liu, H.F.; Wu, W.J.; Fan, J.; Tu, L.C. Electroplating of 3D Sn-rich solder for MEMS packaging applications. *J. Micromech. Microeng.* **2019**, *29*, 045008. [[CrossRef](#)]
25. Tada, H.; Kumpel, A.E.; Lathrop, R.E.; Slanina, J.B.; Nieva, P.; Zavracky, P.; Miaoulis, I.N.; Wong, P.Y. Thermal expansion coefficient of polycrystalline silicon and silicon dioxide thin films at high temperatures. *J. Appl. Phys.* **2000**, *87*, 4189–4193. [[CrossRef](#)]
26. Jan, M.T.; Ahmad, F.; Hamid, N.H.B.; MdKhir, M.H.B.; Ashraf, K.; Shoaib, M. Temperature dependent Young's modulus and quality factor of CMOS-MEMS resonator: Modelling and experimental approach. *Microelectron. Reliab.* **2016**, *57*, 64–70. [[CrossRef](#)]
27. Wang, H.J.; Deng, H.A.; Chiang, S.Y.; Su, Y.F.; Chiang, K.N. Development of a process modeling for residual stress assessment of multilayer thin film structure. *Thin Solid Films* **2015**, *584*, 146–153. [[CrossRef](#)]
28. Freund, L.B.; Floro, J.A.; Chason, E. Extensions of the Stoney formula for substrate curvature to configurations with thin substrates or large deformations. *Appl. Phys. Lett.* **1999**, *74*, 1987–1989. [[CrossRef](#)]

29. Yao, W.Z.; Roqueta, F.; Craveur, J.C.; Belhenini, S.; Gardes, P.; Tougui, A. Modelling and analysis of the stress distribution in a multi-thin film system Pt/USG/Si. *Mater. Res. Express* **2018**, *5*, 046405. [[CrossRef](#)]
30. Hsueh, C.-H. Modeling of elastic deformation of multilayers due to residual stresses and external bending. *J. Appl. Phys.* **2002**, *91*, 9652–9656. [[CrossRef](#)]
31. Torun, H.; Urey, H. Thermal deflections in multilayer microstructures and athermalization. *J. Appl. Phys.* **2006**, *100*, 023527. [[CrossRef](#)]
32. Hu, Y.Y.; Huang, W.M. Elastic and elastic-plastic analysis of multilayer thin films: Closed-form solutions. *J. Appl. Phys.* **2004**, *96*, 4154–4160. [[CrossRef](#)]
33. Okada, Y.; Tokumaru, Y. Precise determination of lattice parameter and thermal expansion coefficient of silicon between 300 and 1500 K. *J. Appl. Phys.* **1984**, *56*, 314–320. [[CrossRef](#)]



© 2020 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (<http://creativecommons.org/licenses/by/4.0/>).